

# High Integrity Compilation

a case study

(Web edition)

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# Preface

Software is increasingly being used in applications where failure could result in injury or loss of life, significant damage to equipment, severe financial loss, or environmental damage. These applications continue to grow in size and complexity, increasing the risk of such failures.

High level languages are often not trusted for these critical applications: they can have complex features that are difficult to understand, and their compilers are not developed to the high degree of assurance required. Thus critical applications tend to be coded in assembly language. But as these applications grow in size using assembly language becomes infeasible; high level languages will have to be used.

To obtain an acceptable level of assurance for a high integrity compiler, it is necessary to have a mathematical specification of the source and target languages, and a formal development of the compiler that translates between them. In this book I illustrate a route for achieving a high integrity compiler by means of a small case study. Note that this is *not* a book about classical compiler development, and so some topics, perhaps surprisingly at first sight, are not covered. There is no treatment of parsing, nor of optimization, for example. The former is a well-understood and solved problem, adequately addressed elsewhere. The latter is not appropriate in high integrity applications, where a clear and traceable link between source code and target code is required for validation.

Part I introduces the problems posed by the requirement for high integrity compilation, and overviews a route to developing such a compiler. Part II specifies a small example language for which the case study compiler, and three static checkers, are developed. Part III specifies the target language and the compiler itself, proves that the compiler's specification correctly implements the high level language semantics, and describes a means of directly implementing the specification to produce an executable compiler. Part IV winds up the discussion by describing the extra components needed for producing a high integrity compiler for a full high level language, and evaluating the proposed approach.

The method for developing a high integrity compiler outlined in this book makes use of many concepts and notations from computer science, including denotational semantics, the Z formal specification language, and the Prolog programming language. Because of this, there is no way the discussion can be stand-alone: it would have to be the size of three tutorial books before I could start talking about high integrity compilers. There are many tutorials on these subjects available, however, and I provide appropriate references to them. Rather than scattering follow-up references throughout the text, I gather them together at the end of relevant chapters, in ‘further reading’ sections.

This book has evolved from a study originally carried out by Logica into implementation techniques that could be used to build a trustworthy Spark compiler for the formally developed Viper microprocessor. The study was commissioned by RSRE (the Royal Signals and Radar Establishment, which has lately metamorphosed into DRA Malvern), and I would like to thank the staff of RSRE for their input to that early work. In particular, detailed technical assistance was provided by John Kershaw, Clive Pygott and Ian Currie, and technical background was provided by Nic Peeling and Roger Smith. That early work was reported in [Stepney *et al.* 1991].

I would like to thank David Brazier, Jon Brumfitt, David Cooper, Mike Flynn, Colin Grant, Tim Hoverd, Ian Nabney and Dave Whitley of Logica for helpful discussions, and for careful reading of various versions of this work. I would also like to thank the anonymous referees, whose detailed comments helped me to clarify and expand the exposition in places. Last but not least, my thanks also to Helen Martin of Prentice Hall for her encouragement and patience, and to Logica management for providing much of the support, both moral and financial, that enabled me to write this book.



# Part I

## Introduction and Background



# Introduction

## 1.1 The problem

If the failure of a piece of software could result in injury or loss of life, significant damage to equipment, severe financial loss, or environmental damage, then that software is *safety critical*. Because the failure of such software is potentially so harmful, it is essential to minimize the chance of its failure—it becomes a *high integrity* application.

It has been argued that the only ‘safe’ way to write high integrity applications is by using assembly language, because only assembly language is close enough to the hardware that one can be sure about what is supposed to happen during program execution, and because only assemblers (that translate assembly language to machine code) are simple enough to be validated, and hence trusted.

There are two major reasons given for distrusting high level languages, which are further removed from the hardware than assembly languages. Firstly, high level languages are complex and poorly defined. Their involved and ambiguous semantics makes it impossible to know what a program written in such a language means, to know what it is supposed to do when it is executed. So it is impossible to know, looking at a program written in such a language, how it should be translated to execute on the ‘real’ machine; in some cases the only way to find out what a piece of code does is to ‘run it and see’. Secondly, a compiler is itself necessarily a large complex piece of software, and will have bugs. So, even if it were possible to have some idea of the meaning of high level language programs, it is impossible to have any confidence that a compiler correctly implements this meaning. The problem is not restricted to just high level languages; some modern chips have such large complex instruction sets that their assembly languages and assemblers are not trusted for high integrity applications.

There is more than a grain of truth in this argument: some programming languages do have notoriously baroque semantics, and their respective compilers are large complex pieces of software, with all the corresponding potential for errors

that implies. But as high integrity applications grow larger and more complex, use of assembly language is becoming infeasible; the large assembly language programs can exhibit as many bugs as the prohibition of high level languages sought to avoid. High level languages, with all their software engineering advantages, are becoming essential. How can these conflicting requirements be reconciled?

As a first step along the way (at least) the following conditions need to be met:

1. The high level source language must have a target-independent meaning; it must be possible to deduce the logical behaviour of any particular program, independent of its execution on a particular target machine.
2. This implies, among other things, that the source language must have a *mathematically defined semantics*. Otherwise it is impossible to deduce even what *should* be the effect of executing a particular program.
3. The target machine language must also have a mathematically defined semantics. Otherwise it is impossible to *prove* that the compilation translation is correct.
4. The compiler from the source to the target language must be correct. Hence it must be derived from the semantics of *both* the source language *and* the target machine's language.
5. To permit *validation*, the compiler for a high integrity language must be *seen to be correct*. It must be written clearly, and must be clearly related to the semantics.
6. The target code produced by the compiler must be clear, and easily related to the source code. This gives the *visibility* to the compilation process that is a requirement for high integrity applications.
7. The semantics for both the source and target languages must be made available for peer review and criticism.

The last three points are important for high integrity applications, in order to conform with the much more stringent validation and visibility requirements these have.

The requirement for a visible link between the source code and target code is most easily met by imperative-style source languages, because their state-based models map well onto most underlying hardware. Higher level languages, such as declarative languages, are much further removed from the machine, and it is correspondingly harder to demonstrate the link. Hence the source language described later in this book is an imperative one.

In addition to the above requirements, the equally thorny problems of proving correct the high integrity *application* being written in the high level language, and of showing that the physical *hardware* correctly implements the meaning of its assembly language, must be addressed. These are beyond the scope of this book.

One approach for constructing a high assurance compiler from the mathematical specifications of the semantics of the source and target languages is described in

this book. This is done by defining Tosca, a small, but non-trivial, high level language, and Aida, a typical assembly language, then constructing a compiler using their definitions. Note that this is not a proposal for a new high integrity language, nor is it a claim that this approach is the way to write general-purpose compilers. Rather, it demonstrates how the mathematical specification of a given language can be used in high integrity compiler development.

## 1.2 Semantics

In order to write a correct compiler, it is necessary to have a mathematically defined semantics of both the source and the target languages. There are several ways of defining the semantics of programming languages, each appropriate for different purposes. Not every form is equally suitable for the purpose of defining a language in such a way that a high integrity compiler can be clearly derived from it.

### 1.2.1 Axiomatic semantics—too abstract

Axiomatic semantics defines a language by providing assertions and inference rules for reasoning about programs.

Assertions can be expressed as ‘Hoare triples’:

$$\{P\}S\{Q\}$$

where  $S$  is a program fragment, and  $P$  and  $Q$  are predicates over program states. The triple asserts that if the pre-condition  $P$  holds before the execution of program fragment  $S$ , and if  $S$  terminates, then the post-condition  $Q$  holds afterwards.

Inference rules look like

$$\frac{H_1, H_2, \dots, H_n}{H}$$

This states that if  $H_1, H_2, \dots, H_n$  are true, then  $H$  is also true. So, when reasoning about a program, in order to prove  $H$ , it is sufficient to prove  $H_1, H_2, \dots, H_n$ . This defines the meaning of  $H$ .

For example, the inference rule for an **if** construct might be given as something such as

$$\frac{\{P \text{ and } \epsilon\}\gamma_t\{Q\}, \{P \text{ and not } \epsilon\}\gamma_f\{Q\}}{\{P\}\text{if } \epsilon \text{ then } \gamma_t \text{ else } \gamma_f\{Q\}}$$

This rule states that, when reasoning about an **if** construct, in order to prove the post-condition  $Q$  is established, it is sufficient to prove that it is established by  $\gamma_t$

whenever  $\epsilon$  holds, and that it is established by  $\gamma_f$  whenever  $\epsilon$  does not hold (in each case, assuming the common pre-condition  $P$  holds, too).

Such a style of definition is appropriate for showing that two programs have the same meaning, for example, that they establish the same post-condition. This is useful, for example, for reasoning about programs, such as defining meaning-preserving program transformations for the purpose of correct optimization. However, it is an indirect form of definition, and is not so useful for defining a language in a form suitable for direct implementation in a high integrity compiler. It is rather too abstract for our purposes.

### 1.2.2 Operational semantics—too concrete

Operational semantics defines a language in terms of the operation of a (possibly abstract) machine executing the program, and so is mostly concerned with implementations. For example, it might define the meaning of an **if** construct such as

**if**  $\epsilon$  **then**  $\gamma_t$  **else**  $\gamma_f$

in terms of labels and jumps

```

<  $\epsilon$  >
  JUMP label1
<  $\gamma_t$  >
  GOTO label2
label1:
  <  $\gamma_f$  >
label2:
```

where the program fragments in angle brackets should be replaced with their operational semantics definitions, recursively. **JUMP** transfers control to the relevant label if the previous expression evaluates to *false*; **GOTO** is an unconditional jump.

Such a definition is useful, for example, when writing a compiler for that particular machine. However, it is not so good for defining what that meaning actually is, because it is defined only in terms of another programming language, which itself needs a definition. Also, a separate operational semantics is needed for each target machine. There then arises the problem of consistency: how can you be sure all the various semantics are defining the same high level language?

So, although such a definition is ultimately required for defining a compiler, it is too concrete to be an appropriate starting point: a machine-independent definition of the language.

### 1.2.3 Denotational semantics—just right

Denotational semantics defines a language by mapping it to mathematics. Such a mathematical definition should be better-understood and better-defined. So the

language is defined by building a mathematical model that defines ‘meaning functions’. Each meaning function maps a type of language construct to a mathematical value. The mathematical values used can have simple types like numbers, and also more structured types like state transition functions. The language construct *denotes* this mathematical value; the value is the ‘meaning’ of the construct.

The mathematical model suitable for such an imperative language is one of states and state transitions. For example, the denotation of an **if** construct would be defined in terms of the mathematical meanings of its component constructs:

$$D[\text{if } \epsilon \text{ then } \gamma_t \text{ else } \gamma_f] \rho \sigma = \begin{cases} D[\gamma_t] \rho \sigma, & \text{if } D[\epsilon] \rho \sigma = \text{true} \\ D[\gamma_f] \rho \sigma, & \text{if } D[\epsilon] \rho \sigma = \text{false} \end{cases}$$

Here  $\rho$  is the environment, a mapping of program identifiers to abstract locations, and  $\sigma$  is the state, a mapping from abstract locations to mathematical values.  $D[\ ]$  is a function that maps program language commands, in an environment and state, to a new state. Hence, it maps program syntax to mathematical values. (The full notation is defined in later chapters.)

The denotational approach of modelling abstract meanings of programs, independent of any machine implementation, satisfies the requirement that a program must have the same logical behaviour no matter what hardware it runs on. It is also at just the right level of abstraction to the starting point for specifying a high integrity compiler.

In later chapters, the denotational semantics of two example languages, the source language Tosca and the target language Aida, are specified.

#### 1.2.4 Non-standard semantics

The denotational semantics described above is a ‘standard’ semantics: it describes the standard meaning usually associated with a program, that of program execution. It is also called ‘dynamic’ semantics, because it defines the dynamic changes of state that occur as a program executes.

But it is up to the language designer what the meaning of the language is chosen to be; other ‘non-standard’ meanings can equally well be defined. Each different meaning thus specified provides a different semantics for a language; the dynamic semantics is just one possible semantics, with a meaning that determines the output values when a program is executed. The best known non-standard meaning defines a type, rather than a value, to be associated with each construct. This ‘type-semantics’ can be used to determine whether a program type checks. Other non-standard meanings can be defined and used to determine other well-formedness conditions on a program, for example, that every variable is initialized to some value before it is used in an expression. These sort of semantics are called ‘static semantics’, because they define ‘static checks’, checks that can be made without executing the program, for example, at compile time.

This is described in more detail in later chapters. In particular, three static semantics for Tosca are specified—declaration-before-use, type checking, and initialization-before-use—and used to implement three static checkers.

### 1.2.5 Size of task

Before deciding to go the fully formal route of specifying source and target languages, and deriving a correct compiler, we need to know that the task is feasible. How big is the task of mathematically specifying a programming language?

Consider Modula-2, whose denotational semantics have been specified in VDM (more accurately, in the functional subset of Meta-IV, which is essentially a programming language); its specification runs to hundreds of pages. But remember, Modula-2 was specified retrospectively, and its definition was required to keep close to the existing semantics as defined operationally by its various compilers. Features that were difficult or ‘messy’ to specify had to be included, even where a different approach could have led to a simpler, cleaner, more understandable specification.

For high integrity applications, it is imperative that every potential for misunderstanding and error be reduced to a minimum. So the application language needs to be designed with a coherent and intelligible semantics. It can be argued that if a language feature is difficult to specify cleanly, it is difficult to understand, and hence should not be included in a language used for writing high integrity applications. Note that the converse does not apply: that a particular feature is easy to specify is not sufficient reason for including it in a high integrity language. Although the design of such a language could start from scratch, it is probably more practical to subset an existing language, slicing away those areas of ambiguity and confusion, not fossilizing them in the final definition. Such a language, although probably of a similar size to Modula-2 in terms of *syntax*, would be much smaller and simpler *semantically*.

Tosca itself is smaller than a real high level language, but even so still has quite a substantial specification, including as it does one dynamic and three static semantics.

The target language can be very small. Although many microprocessors have elaborate instruction sets, it is not necessary to specify the semantics of every one of these instructions. Only the subset of the instructions needed to implement the high level language need be specified. No static semantics need be defined either; all the static checks can be done in the high level language, and a correct translation introduces no new errors, so only the dynamic meaning need be considered. Even so, the specification tends to be more complex: the instruction set contains jump instructions, and the semantics of jumps are difficult to specify. Aida is typical of a pruned instruction set language.



### 1.3 From semantics to a compiler

The compiler's job is to translate each high level construct, such as

```
if < test_expr > then < then_cmd > else < else_cmd >
```

into a corresponding target language template, such as

```
<test_expr translation>
JUMP label1
<then_cmd translation>
GOTO label2
label1:
  <else_cmd translation>
label2:
```

where the program fragments in angle brackets are similarly translated, recursively. Specifying the compiler from a source to a target language consists of defining an operational semantics, in the form of a target language template for each source language construct. An obvious question arises: how can one have any confidence that these are the *correct* target language templates? For example, how can one have any confidence that the **if** statement above and its compiled translation have the same meaning?

It is possible to answer this question. Given the denotational semantics of the target language, it is possible to *calculate* the meaning of the template in the target language. This can be compared with the meaning of the corresponding high level fragment, and be shown to be the same (see Chapter 13). Proving that the correct templates have been specified reduces to calculating the meaning of every template, and showing it to be the same as the meaning of the source language construct. Notice that this approach provides a *structuring* mechanism for the proof process. Arguments are advanced on a construct-by-construct basis, using structural induction over the source language. The complete proof is constructed by working through the syntax tree of the language, until all constructs have a suitable argument supporting them; this then completes the argument in support of the compiler specification as a whole. Hence the complete proof is composed, using a divide-and-conquer strategy, from a number of smaller, independent subproofs. This structure makes the total proof much more tractable, and more understandable, than would a single monolithic approach. Structure is indispensable when doing a large proof.

### 1.4 Executable specification language—Prolog

There are various notations available for writing denotational semantics, including the conventional mathematical notation, convenient for reasoning about the correctness of the compiler templates.

If the denotational semantics specification is translated into an executable language, then executing it provides an *interpreter* for the specified language. This interpreter can, if desired, be used as a *validation tool* for checking that the formal mathematical specification defines a language with the appropriate informally expected behaviour.

It is possible, given a denotational semantics in some abstract notation, to translate it into an imperative language, such as Pascal, in order to produce an interpreter. However, because the style of such a language is so far removed from the style of the denotational specification, the mapping from the specification to the resulting implementation is very complex. Such a complex mapping process is in itself potentially error-prone, and does not produce an interpreter that is a transparently correct implementation of its specification. Furthermore, the correspondence between it and the operational semantics needed for the associated *compiler* is not obvious.

A better approach is to translate the denotational semantics into a much higher level programming language, one that closely matches the style of specification. This drastically reduces the complexity of the translation step, enabling the semantics to be written clearly and abstractly, in order to provide a transparently correct implementation of the interpreter. A functional language or logic language seems a natural choice.

Prolog is used here as the executable specification language for the Tosca compiler; there is a natural mapping from the denotational semantics into Prolog clauses. Also, an important consideration for a high integrity compiler that must be seen to be correct, Prolog is a mature language that is well supported and has a large user community. The use of an unproven Prolog system, however, is a weak link in the development process, as is the use of an unproven operating system and unproven hardware. The various sources of errors are discussed more fully in Chapter 15. What is being described here is how to strengthen one of the currently weakest links in the process.

Prolog does have some features whose misuse could obscure the mapping from specification to compiler. The most notorious of these is the ‘cut’ (written !). Cuts are used to increase execution efficiency by controlling backtracking and execution order, and are considered by some as the Prolog equivalent of the ‘goto’. However, some cuts are worse than others. Prolog has two semantics: a simple declarative semantics, which is independent of the order in which clauses and goals are written, and is used to reason about the meaning of a program, and a more complex operational semantics, which defines an execution order, and says what happens when a program is executed. It is important for understandability that these two semantics give a program the same meaning. So-called ‘green’ cuts do maintain this property. ‘Red’ cuts, on the other hand, result in the two meanings being different. The operational one is (presumably) the desired meaning, but the simpler declarative reading can no longer be used to determine what this meaning is. In order to understand the program it becomes necessary, for example, to know

in what order goals are evaluated.

It is important that the Prolog form of a high integrity compiler has the same declarative reading (equivalent to the specification) as operational reading (which provides the executable compiler). So the Prolog must be written in a disciplined manner, eschewing red cuts and other tricks, if necessary sacrificing speed of execution.

## 1.5 Further reading

Early work on compiler correctness includes [McCarthy and Painter 1966], [Milner and Weyhrauch 1972], [Morris 1973], [Cohn 1979], [Polak 1981]. Work on automatically generating a compiler from a definition of the language's semantics includes [Mosses 1975], [Paulson 1981], [Paulson 1982], [Wand 1984], [Lee 1989] and [Tofte 1990]. The Esprit supported ProCoS (Provably Correct Systems) project has investigated an algebraic approach to correct compilation, described in [Hoare 1991].

Many seminal papers on the axiomatic style of defining programming languages can be found in [Hoare and Jones 1989]. [Tennent 1991] describes the connection between denotational, operational and axiomatic semantics.

For an introduction to denotational semantics, see, for example [Gordon 1979], [Allison 1986] and [Schmidt 1988]. The classic description is [Stoy 1977]. Various static semantic analyses are described in [Cousot and Cousot 1977], [Bramson 1984] and [Bergeretti and Carré 1985]. [Allison 1986] gives examples of translating denotational semantics definitions into Pascal to provide an interpreter. [Stepney and Lord 1987] describes an example of executing a specification in order to validate it.

The formal specification of Modula-2 can be found in [Andrews *et al.* 1992]. [Carré 1989] discusses criteria for including features in high integrity languages.

Anyone still not convinced that natural language is totally unsuitable for rigorously and unambiguously specifying even a simple problem should read [Meyer 1985]. This is an entertaining account of the repeated failed attempts to use English to specify a seemingly trivial problem, and how formalism can help.

There are many good books introducing Prolog. See, for example [Sterling and Shapiro 1986] and [Clocksin and Mellish 1987]. The former discusses green and red cuts.

# Specifying a Language— by Example

## 2.1 Introduction

This chapter briefly explains the steps involved in the denotational specification of a programming language, using a trivial example language, Turandot (*‘Tiny, Unfinished, Restricted, and Overly Terse’*). This illustrative example is by no means complete; the explanation is intended solely to give an overview of the process of specifying and proving a compiler, in order to motivate the larger complete specifications and proofs in the later chapters.

The denotational semantics definition of a sizable programming language requires the use of a branch of mathematics known as *domain theory*. However, for the simple languages described below, the extra capability, and consequent complexity, provided by domains is not necessary. Set theory provides a sufficient basis for the specifications, and, since it is conceptually simpler, its use clarifies some of the later discussions.

It is necessary to use some particular notation to write a denotational semantics specification. Many programming language specifications introduce their notation for domains in a somewhat *ad hoc* manner; using the simpler set-based approach has the advantage that the well-defined formal specification language  $Z$  can be used as the specification notation. Section 2.6 describes a small liberty taken with the syntax of  $Z$  that helps improve the clarity of programming language specifications.

Note: in this chapter some of the syntactic categories are decorated with numerical subscripts. These decorations are purely a technical device to distinguish the tutorial definitions from each other and from the later ‘true’ definitions of Tosca and Aida; they have no further significance.

## 2.2 Syntax

The first step in specifying a language is a specification of its *syntax*. Such a specification gives rules for well-formed ‘sentences’ in the language. An example of a badly formed, or syntactically incorrect, English sentence is Douglas Hofstadter’s example; *This sentence no verb*.

Traditionally, a programming language’s syntax is defined concretely, in terms of well-formed strings of characters. A program text consists of such a string, which first has to be lexically analyzed into a sequence of tokens (keywords, identifiers, and so on), then these tokens have to be parsed into a tree structure or *abstract syntax*. However, lexing and parsing are solved problems, discussed at great length in many classic texts, and so are not addressed yet again here. The specification of the semantics is clearer if it is given directly in terms of the abstract syntax, rather than in concrete terms of strings, which can become cluttered with disambiguation mechanisms like parentheses, keywords, and operator precedence rules. This sort of separation of concerns also allows the concrete syntax to be changed without needing to change the abstract syntax, the semantics definitions or the correctness proofs.

### 2.2.1 Structure of a syntax specification

Syntax can be defined using three classes of construct: compounds, lists and selection.

A *compound* has a fixed number of components, usually of different types. Compounds can be modelled abstractly in  $Z$  using a Cartesian product of the components. For example, a choice command consisting of a test expression, and two branch commands, and an assignment command consisting of an identifier and an expression, can be defined as

$$\begin{aligned} \textit{Choice} &== \textit{EXPR}_0 \times \textit{CMD}_0 \times \textit{CMD}_0 \\ \textit{Assign} &== \textit{NAME}_0 \times \textit{EXPR}_0 \end{aligned}$$

The symbol ‘==’ is  $Z$ ’s *abbreviation definition*, a way of providing a meaningful name for a more complicated expression. The name can be used to stand for the expression anywhere in the specification.

A *list* has an arbitrary number of components of the same type. Lists are modelled in  $Z$  using a sequence. For example, a list of commands, and a list of declarations, can be defined as

$$\begin{aligned} \textit{CMDLIST}_0 &== \textit{seq}_1 \textit{CMD}_0 \\ \textit{DECLLIST}_0 &== \textit{seq} \textit{DECL}_0 \end{aligned}$$

This says that a command list is a non-empty sequence of commands, and a declaration list is a possibly empty sequence of declarations. A block command, consisting

of a list of declarations and commands, can be defined using a compound whose components are lists:

$$Block == DECLLIST_0 \times CMDLIST_0$$

A *selection* comprises a set of possible constructs of a particular type. Selections can be modelled in Z using its free type (disjoint union) definition. For example, defining a command to be a choice or an assignment or a block:

$$\begin{aligned} CMD_0 ::= & \textit{choice}_0 \langle\langle \textit{Choice} \rangle\rangle \\ & | \textit{assign}_0 \langle\langle \textit{Assign} \rangle\rangle \\ & | \textit{block}_0 \langle\langle \textit{Block} \rangle\rangle \end{aligned}$$

The free type notation is explained in Appendix C.

A syntax specification can be shortened by including the compounds and lists directly in the selections. The partial example above can be written more succinctly as

$$\begin{aligned} CMD_0 ::= & \textit{choice}_0 \langle\langle \textit{EXPR}_0 \times \textit{CMD}_0 \times \textit{CMD}_0 \rangle\rangle \\ & | \textit{assign}_0 \langle\langle \textit{NAME}_0 \times \textit{EXPR}_0 \rangle\rangle \\ & | \textit{block}_0 \langle\langle \textit{seq} \textit{DECL}_0 \times \textit{seq}_1 \textit{CMD}_0 \rangle\rangle \end{aligned}$$

### 2.2.2 Turandot's abstract syntax

Turandot has only three types of construct: binary operators, expressions, and commands.

$$\begin{aligned} OP_1 ::= & \textit{plus} \\ & | \textit{lessThan} \\ & | \textit{equalTo} \\ & | \dots \end{aligned}$$

This is a typical non-recursive free type definition, and can be thought of as a simple 'enumerated type', listing all Turandot's binary operators, which include comparison and arithmetic operators.

Turandot's expression syntax is

$$\begin{aligned} EXPR_1 ::= & \textit{number} \langle\langle \mathbb{Z} \rangle\rangle \\ & | \textit{variable} \langle\langle \textit{NAME}_1 \rangle\rangle \\ & | \textit{negate} \langle\langle \textit{EXPR}_1 \rangle\rangle \\ & | \textit{operation} \langle\langle \textit{EXPR}_1 \times \textit{OP}_1 \times \textit{EXPR}_1 \rangle\rangle \end{aligned}$$

This is a typical recursive free type definition. It has two base cases (an expression can be an integer or a variable name) and two recursive cases (a new expression can be formed from another by negating it, or from two others by combining them

with a binary operator). Note that all values in Turandot are just numbers; the value 1 is also used to indicate ‘true’ and 0 to indicate ‘false’.

Turandot’s command syntax is

$$\begin{aligned}
 \text{CMD}_1 &::= \text{skip} \\
 &| \text{assign}\langle\langle \text{NAME}_1 \times \text{EXPR}_1 \rangle\rangle \\
 &| \text{choice}\langle\langle \text{EXPR}_1 \times \text{CMD}_1 \times \text{CMD}_1 \rangle\rangle \\
 &| \text{compose}\langle\langle \text{CMD}_1 \times \text{CMD}_1 \rangle\rangle
 \end{aligned}$$

Again, this is a recursive definition. The base cases are the *skip* command, and the assignment command, which assigns the value of an expression to a variable. The others build new commands from smaller commands: *choice* chooses between two commands based on the value of expression, and *compose* composes two commands.

### 2.2.3 Turandot’s concrete syntax

It is quite possible to specify a variety of concrete syntaxes from a single abstract syntax. For example, the concrete choice command can easily be defined as any one of the following:

- if test then then\_branch else else\_branch
- if test then then\_branch else else\_branch endif
- test ? then\_branch : else\_branch
- then\_branch ◁ test ▷ else\_branch

Using the abstract syntax when specifying the semantics literally abstracts away from these unimportant typographical details. Unimportant for specifying the semantics, that is. Concrete syntax *is* important for making a particular language usable, and so should be chosen with care.

A concrete syntax can be specified by mapping each construct in the abstract syntax to a string of characters. For example, a concrete syntax for Turandot’s commands can be specified by

$$\begin{array}{|l}
 \hline
 \text{CMD}\langle\_ \rangle : \text{CMD}_1 \longrightarrow \text{String} \\
 \hline
 \forall \xi : \text{NAME}_1; \epsilon : \text{EXPR}_1; \gamma_1, \gamma_2 : \text{CMD}_1 \bullet \\
 \text{CMD}\langle \text{skip} \rangle = \text{“skip”} \\
 \wedge \text{CMD}\langle \text{assign}(\xi, \epsilon) \rangle = \text{NAME}\langle \xi \rangle \hat{\wedge} \text{“:=”} \hat{\wedge} \text{EXPR}\langle \epsilon \rangle \\
 \wedge \text{CMD}\langle \text{choice}(\epsilon, \gamma_1, \gamma_2) \rangle = \\
 \quad \text{“if”} \hat{\wedge} \text{EXPR}\langle \epsilon \rangle \hat{\wedge} \text{“then”} \hat{\wedge} \text{CMD}\langle \gamma_1 \rangle \\
 \quad \hat{\wedge} \text{“else”} \hat{\wedge} \text{CMD}\langle \gamma_2 \rangle \hat{\wedge} \text{“endif”} \\
 \wedge \text{CMD}\langle \text{compose}(\gamma_1, \gamma_2) \rangle = \text{CMD}\langle \gamma_2 \rangle \hat{\wedge} \text{“;”} \hat{\wedge} \text{CMD}\langle \gamma_1 \rangle
 \end{array}$$

This specifies a function called `CMD` (it assumes that functions called `NAME` and `EXPR` are also specified) using a  $Z$  axiomatic definition. The part above the horizontal line declares the signature of the function: `CMD` maps elements of the syntactic category  $CMD_1$  to strings. The parts below the line provide the definition of the function, by structural induction over the structure of the language syntax. There is one term for each sort of command in the abstract syntax, and the definition is recursive in terms of the translation of subcomponents of a particular command. The infix operator ‘ $\wedge$ ’ is  $Z$ ’s *concatenation* operator for joining sequences.

Aside—a more thorough definition of concrete syntax would be broken down into steps: defining a mapping from abstract syntax to sequences of tokens, defining the character strings used to represent tokens, and defining how tokens may be separated by white space (spaces, tabs and newlines) and comment strings. The simpler treatment given here is adequate for my purpose: defining a concrete syntax in which to write example Tosca programs.

## 2.3 Semantics

In the specification of Turandot’s syntax, the ‘meaning’ of each construct is given informally in natural language, if at all. It relies heavily on our intuitive understanding of phrases such as “assigns the value of an expression to a variable”. Problems can arise if the intuition of language designer, implementor and user differ, or if areas of ambiguity are resolved differently. What if the expression refers to some variable that has not been assigned a value? Should some default value be assumed, and if so, what value, or should this be an error? In a typed language (like Tosca, but unlike Turandot), what if the expression and variable have a different type? Should the expression’s type be silently coerced to that of the variable, or should this be an error?

The denotational approach to providing a mathematical specification is to define *meaning functions*, which map syntactic constructs to what they denote: mathematical values. Appeals to intuition and common understanding can be replaced by mathematical manipulations of these formal definitions.

### 2.3.1 A mathematical model of Turandot

Different kinds of mathematical model are appropriate for different classes of programming languages. A statement in a declarative language reports a fact about the world, for example, “the cat is on the mat”. Reporting a fact does not change the state of the world. A statement in an imperative language, on the other hand, can change the state of the world, for example, “I name this ship the *Blaise Pascal*”. An appropriate model for an imperative language (the kind considered in this book) is a state, and a set of state transition functions.



An appropriate state for Turandot is a mapping from variables' names to their current values:

$$State_1 == NAME_1 \mapsto \mathbb{Z}$$

$State_1$  is the set of all functions that map names to integers. A particular function  $\sigma_1$  of type  $State_1$  can be declared as  $\sigma_1 : State_1$ .

$\mathbb{Z}$  models functions as sets of pairs; for state functions of the type given above, the first element of each pair is a name, the second is an integer. A particular state  $\sigma_1$ , which has two variables,  $x$  with the value 3, and  $y$  with the value 9, is

$$\sigma_1 = \{x \mapsto 3, y \mapsto 9\}$$

The *maplet* notation  $x \mapsto 3$ , an alternative form of the more conventional notation for a pair  $(x, 3)$ , highlights the 'mapping' nature of the term.

In  $\mathbb{Z}$ , functions are *partial*: a state need map only some names to values, not all of them. The domain of a state is the set of all those names that have values defined, so  $\text{dom } \sigma_1 = \{x, y\}$ . The range is the set of all those values that are mapped to, so  $\text{ran } \sigma_1 = \{3, 9\}$ . If some function was required to be *total*, to map every name to a value, it would be declared using an undecorated arrow:

$$\mid \sigma_t : NAME_1 \rightarrow \mathbb{Z}$$

In this case,  $\text{dom } \sigma_t = NAME_1$ .

Because a state is a function, applying it to a variable yields the value of that variable:  $\sigma_1 x = 3$ .

The simplest state change is to update the value of a single variable. The  $\mathbb{Z}$  way to do this is by *overriding* the value, using the ' $\oplus$ ' operator: for values in the domain of  $\sigma_b$  the function  $\sigma_a \oplus \sigma_b$  agrees with  $\sigma_b$ , and elsewhere it agrees with  $\sigma_a$ .

Thus,  $\sigma \oplus \{a \mapsto v\}$  produces a new state. If  $a$  was in the old state, its value in the new state is changed to  $v$ ; if it was not, it is added to the new state. So

$$\sigma_1 \oplus \{x \mapsto 1\} = \{x \mapsto 1, y \mapsto 9\}$$

$$\sigma_1 \oplus \{z \mapsto 1\} = \{x \mapsto 3, y \mapsto 9, z \mapsto 1\}$$

In Turandot, each binary operator denotes the corresponding mathematical operator, an expression denotes a value in the context of a state, and a command denotes a state transition function. (Tosca requires a slightly more elaborate model, with an environment as well as a state, as discussed in Chapter 7.)

### 2.3.2 The meaning of Turandot's binary operators

Let's take Turandot's operators first. Each operator denotes a corresponding mathematical operator. A mathematical binary operator is a function that maps two numbers, its arguments, to another number, the result. Hence it has the type

$\mathbb{Z} \times \mathbb{Z} \rightarrow \mathbb{Z}$ . The operator meaning function that maps syntactic operators onto the corresponding mathematical function is specified as

$$\left| \begin{array}{l} \mathcal{DOP}[\_ ] : OP_1 \rightarrow \mathbb{Z} \times \mathbb{Z} \rightarrow \mathbb{Z} \\ \hline \forall x, y : \mathbb{Z} \bullet \\ \quad \mathcal{DOP}[\textit{plus}](x, y) = x + y \\ \quad \wedge \mathcal{DOP}[\textit{lessThan}](x, y) = \mathbf{if } x < y \mathbf{ then } 1 \mathbf{ else } 0 \end{array} \right.$$

The function is specified for all Turandot's binary operators by specifying it for each branch of the  $OP_1$  free type definition. Remember that Turandot uses the value 1 for 'true' and 0 for 'false'.

### 2.3.3 The meaning of Turandot's expressions

An expression denotes a value; what value is denoted can depend on the values of the variables in the current state. The expression meaning function maps an expression to the value it denotes in the context of a state

$$\left| \begin{array}{l} \mathcal{DEXPR}[\_ ] : EXPR_1 \rightarrow State_1 \rightarrow \mathbb{Z} \\ \hline \forall \chi : \mathbb{Z}; \xi : NAME_1; \epsilon, \epsilon_1, \epsilon_2 : EXPR_1; \omega : OP_1; \sigma : State_1 \bullet \\ \quad \mathcal{DEXPR}[\textit{number } \chi]\sigma = \chi \\ \quad \wedge \mathcal{DEXPR}[\textit{variable } \xi]\sigma = \sigma\xi \\ \quad \wedge \mathcal{DEXPR}[\textit{negate } \epsilon]\sigma = -(\mathcal{DEXPR}[\epsilon]\sigma) \\ \quad \wedge \mathcal{DEXPR}[\textit{operation}(\epsilon_1, \omega, \epsilon_2)]\sigma = \\ \quad \quad \mathcal{DOP}[\omega](\mathcal{DEXPR}[\epsilon_1]\sigma, \mathcal{DEXPR}[\epsilon_2]\sigma) \end{array} \right.$$

The semantic definition follows the recursive structure of the  $EXPR_1$  abstract syntax. The meaning of an expression consisting of a number is just that number, irrespective of the state. The meaning of an expression consisting of a variable name is the value that name denotes in the current state, found by applying the state function  $\sigma$  to the name. (A variable that has not previously been assigned a value is not in the domain of the state function, and the result of applying the state function is undefined. A resolution of this problem is discussed below, in section 2.4.) The meaning of a *negate* expression is the mathematical negation of the meaning of the subexpression. The meaning of an *operation* expression is found by using the meaning of the operator to combine the meanings of the subexpressions.

### 2.3.4 The meaning of Turandot's commands

A command denotes a state transition. Hence, the command meaning function maps a command to a state transition function:

$$\begin{array}{|l}
 \mathcal{D}_{CMD}[-] : CMD_1 \longrightarrow State_1 \leftrightarrow State_1 \\
 \hline
 \forall \xi : NAME_1; \epsilon : EXPR_1; \gamma_1, \gamma_2 : CMD_1; \sigma : State_1 \bullet \\
 \mathcal{D}_{CMD}[\textit{skip}]\sigma = \sigma \\
 \wedge \mathcal{D}_{CMD}[\textit{assign}(\xi, \epsilon)]\sigma = \sigma \oplus \{\xi \mapsto \mathcal{D}_{EXPR}[\epsilon]\sigma\} \\
 \wedge \mathcal{D}_{CMD}[\textit{choice}(\epsilon, \gamma_1, \gamma_2)]\sigma = \\
 \quad \textbf{if } \mathcal{D}_{EXPR}[\epsilon]\sigma = 1 \textbf{ then } \mathcal{D}_{CMD}[\gamma_1]\sigma \textbf{ else } \mathcal{D}_{CMD}[\gamma_2]\sigma \\
 \wedge \mathcal{D}_{CMD}[\textit{compose}(\gamma_1, \gamma_2)]\sigma = (\mathcal{D}_{CMD}[\gamma_2] \circ \mathcal{D}_{CMD}[\gamma_1])\sigma
 \end{array}$$

The semantic definition follows the recursive structure of the  $CMD_1$  abstract syntax. *skip* is the identity function; it leaves the state unchanged. *assign* changes the value of one component of the state; the new state maps the relevant name to its new value. *choice* chooses between the two commands based on the value of the expression (remember that the value 1 is used to indicate ‘true’). Composing commands composes the state transition functions that they denote.

Notice that the words in the paragraph accompanying the mathematical specification of the semantics are very similar to the words that accompanied the syntax specification. But now they have a mathematical meaning, too. This mathematical meaning can be referred to in case of ambiguity, and can be formally manipulated if necessary, to discover the precise meaning of complex constructs.

## 2.4 Static semantics

Some sentences in a language can be syntactically correct, but meaningless. For example, in English, Chomsky's famous sentence *Colourless green ideas sleep furiously* is syntactically well-formed, but does not mean anything. In many programming languages, a statement like  $\mathbf{x} := 1 + \mathbf{true}$  might be parsed according to the syntax rules, but might have no meaning, because numbers cannot be added to booleans. In the denotational approach, rules for meaningless constructs can be formalized by providing various static semantics. These look similar to the specifications above, but instead of saying an expression means something like ‘4’, say an expression means ‘badly typed’.

We can do this because the interpretation of the meaning of a piece of syntax is up to the specifier, and so we can choose alternative meaning functions for our different purposes. The meaning defined above specifies Turandot's dynamic semantics, the meaning we would expect to be associated with the executing program. But we can associate other, non-standard, meanings, too. For example, we

can choose the state to be those names that have been assigned a value, ignoring what that value might be. Call this the s-state:

$$SState_1 ::= \mathbb{P} NAME_1$$

$\mathbb{P}$  is  $Z$ 's *power set* constructor, so any state of type  $SState_1$  has the type 'set of  $NAME_1$ '.

Let's define a flag to determine whether or not variables are being used before they are assigned a value:

$$SET_1 ::= yes \mid no$$

Then we can specify an alternative meaning function for expressions, one that maps them to *yes* or *no*, to indicate whether the variables to which they refer have been properly assigned a value or not:

$$\begin{array}{|l} \hline \mathcal{SEXPR}[-] : EXPR_1 \rightarrow SState_1 \rightarrow SET_1 \\ \hline \forall \chi : Z; \xi : NAME_1; \epsilon, \epsilon_1, \epsilon_2 : EXPR_1; \omega : OP_1; \sigma : SState_1 \bullet \\ \quad \mathcal{SEXPR}[\textit{number } \chi]\sigma = \textit{yes} \\ \quad \wedge \mathcal{SEXPR}[\textit{variable } \xi]\sigma = \textit{if } \xi \in \sigma \textit{ then yes else no} \\ \quad \wedge \mathcal{SEXPR}[\textit{negate } \epsilon]\sigma = \mathcal{SEXPR}[\epsilon]\sigma \\ \quad \wedge \mathcal{SEXPR}[\textit{operation}(\epsilon_1, \omega, \epsilon_2)]\sigma = \\ \quad \quad \textit{if } \mathcal{SEXPR}[\epsilon_1]\sigma = \textit{yes} \wedge \mathcal{SEXPR}[\epsilon_2]\sigma = \textit{yes} \textit{ then yes else no} \\ \hline \end{array}$$

This defines a different meaning for Turandot's expressions: let's call it the s-meaning. So the s-meaning of an expression consisting of a number is always *yes*, irrespective of the s-state. The s-meaning of an expression consisting of a variable name is *yes* if and only if that name is in the current s-state (as we will see in the definition of  $\mathcal{SCMD}[-]$ , the assignment statement adds names to the state). The s-meaning of a *negate* expression is the same as the s-meaning of the subexpression. The s-meaning of an operation expression is *yes* if and only if the s-meanings of both subexpressions are *yes*.

The s-meaning of a command is an s-state transition:

$$\begin{array}{|l} \hline \mathcal{SCMD}[-] : CMD_1 \rightarrow SState_1 \rightarrow SState_1 \\ \hline \forall \xi : NAME_1; \epsilon : EXPR_1; \gamma_1, \gamma_2 : CMD_1; \sigma : SState_1 \bullet \\ \quad \mathcal{SCMD}[\textit{skip}]\sigma = \sigma \\ \quad \wedge \mathcal{SCMD}[\textit{assign}(\xi, \epsilon)]\sigma = \sigma \cup \{\xi\} \\ \quad \wedge \mathcal{SCMD}[\textit{choice}(\epsilon, \gamma_1, \gamma_2)]\sigma = \mathcal{SCMD}[\gamma_1]\sigma \cap \mathcal{SCMD}[\gamma_2]\sigma \\ \quad \wedge \mathcal{SCMD}[\textit{compose}(\gamma_1, \gamma_2)]\sigma = (\mathcal{SCMD}[\gamma_2] \circ \mathcal{SCMD}[\gamma_1])\sigma \\ \hline \end{array}$$

Notice that the only time a new name is added to the environment is by the *assign* meaning function. The *choice* s-meaning takes the intersection of the states for each branch: a new variable must be assigned a value in both branches before it is considered to be assigned by the whole command.

Such a specification can be used to provide a static check of whether variables are referenced before they are assigned a value. It is called static, because it can be done without actually executing the program (for example, at compile time). Hence such a non-standard interpretation of the meaning of a program is called *static semantics*. Various alternative meanings can be defined to provide various different static semantics. This is described in much more detail in Chapter 8. Tosca has three different static semantics in addition to its dynamic semantics.

## 2.5 Operational semantics

The target language itself can be specified in the same manner. For example, the target language's syntax may include instructions such as

$$\begin{aligned} INSTR_1 ::= & store\langle\langle NAME_1 \rangle\rangle \\ & | jump\langle\langle LABEL_1 \rangle\rangle \\ & | goto\langle\langle LABEL_1 \rangle\rangle \\ & | label\langle\langle LABEL_1 \rangle\rangle \end{aligned}$$

The informal meanings of these instructions are as follows: *store* stores the value currently in the accumulator (a special location) in the location corresponding to a name; *jump* jumps to its label if the value in the accumulator is ‘false’, otherwise it does nothing; *goto* jumps unconditionally to its label. Other instructions are also needed, to load values and to combine and compare values. These meanings need to be defined formally, too, by specifying some appropriate meaning function  $INSTR[-]$ , that maps syntactic instructions to mathematical values. These further definitions are omitted here for brevity. See Chapter 10 for the specification of Aida, the ‘real’ target language.

The operational semantics (that is, the specification of the translations from source to target language) is defined in a similar manner. However, the ‘operational meaning’ function is a purely syntactic definition: it maps source language constructs not to mathematical values, but to sequences of target language instructions.

For example, the definition of the translation of Turandot’s commands into the target language might look something like

$$\begin{array}{|l}
\mathcal{O}_{CMD}\langle \_ \rangle : CMD_1 \rightarrow \text{seq } INSTR_1 \\
\hline
\forall \xi : NAME_1; \epsilon : EXPR_1; \gamma, \gamma_1, \gamma_2 : CMD_1 \bullet \\
\mathcal{O}_{CMD}\langle skip \rangle = \langle \rangle \\
\wedge \mathcal{O}_{CMD}\langle assign(\xi, \epsilon) \rangle = \mathcal{O}_{EXPR}\langle \epsilon \rangle \hat{\ } \langle store \ \xi \rangle \\
\wedge \mathcal{O}_{CMD}\langle choice(\epsilon, \gamma_1, \gamma_2) \rangle = \\
\quad \mathcal{O}_{EXPR}\langle \epsilon \rangle \hat{\ } \langle jump \ \phi_1 \rangle \hat{\ } \mathcal{O}_{CMD}\langle \gamma_1 \rangle \\
\quad \hat{\ } \langle goto \ \phi_2, label \ \phi_1 \rangle \hat{\ } \mathcal{O}_{CMD}\langle \gamma_2 \rangle \hat{\ } \langle label \ \phi_2 \rangle \\
\wedge \mathcal{O}_{CMD}\langle compose(\gamma_1, \gamma_2) \rangle = \mathcal{O}_{CMD}\langle \gamma_1 \rangle \hat{\ } \mathcal{O}_{CMD}\langle \gamma_2 \rangle
\end{array}$$

*skip* does nothing, so translates to an empty sequence of instructions. The expression in an *assign* is translated to the appropriate sequence of instructions (the definition of  $\mathcal{O}_{EXPR}\langle \_ \rangle$  is not given), and a *store* instruction is concatenated, in order to store the result of the expression at the relevant location. For a *choice* command, the expression is translated, then a *jump on false* instruction is added to jump to the *false* branch. This is followed by a translation of the *true* branch, and an unconditional *goto*, to jump over the *false* branch to the end. Then we get the *false* branch label, and the translation of the *false* branch itself. Finally, the *end* label is added. The instruction list for a *compose* command simply consists of the concatenation of the instruction lists of the composed commands.

This example has been simplified for brevity. The real definition is more involved: it needs to maintain a ‘translation environment’ to hold the mapping from variables to their storage locations, and to note what labels have been allocated. This is described in detail in Chapter 11. The overview here is merely intended to give a flavour, to motivate future definitions.

This translation to sequences of instructions is purely syntactic. However, given the denotational specification of the target language semantics, as a specification of a meaning function  $INSTR\llbracket \_ \rrbracket$ , it is possible to calculate the meaning of this translated sequence of instructions. The proof of correctness becomes a proof that the meaning of a Turandot construct is the same as the meaning of the sequence of instructions that results from translating it:

$$\gamma : CMD_1 \vdash \mathcal{D}_{CMD}\llbracket \gamma \rrbracket = INSTR\llbracket \mathcal{O}_{CMD}\langle \gamma \rangle \rrbracket$$

The Tosca compiler specification is proved correct in Chapter 13.

## 2.6 A liberty with Z

As illustrated above, Z is used to specify the various languages and their semantics. However, in order to minimize clutter in the definitions, a small liberty is taken with its syntax when defining Tosca and Aida below.

The definitions of the semantics functions are quantified over all the variables appearing on the left-hand side of the equation. For example, the meaning of Turandot's commands is defined for all possible values of names, expressions, commands, and state variables:

$$\left| \begin{array}{l} \mathcal{DCMD}[-] : CMD_1 \longrightarrow State_1 \twoheadrightarrow State_1 \\ \hline \forall \xi : NAME_1; \epsilon : EXPR_1; \gamma_1, \gamma_2 : CMD_1; \sigma : State_1 \bullet \\ \quad \mathcal{DCMD}[skip]\sigma = \sigma \\ \quad \wedge \mathcal{DCMD}[assign(\xi, \epsilon)]\sigma = \dots \\ \quad \wedge \mathcal{DCMD}[choice(\epsilon, \gamma_1, \gamma_2)]\sigma = \dots \end{array} \right.$$

The continual occurrence of such quantifications tends to clutter the specification. So this is abbreviated, by omitting the declarations of all the arguments of the meaning functions (but not of any other functions), whose types can easily be deduced. You should assume an implicit quantification over all these arguments when reading a definition such as

$$\left| \begin{array}{l} \mathcal{DCMD}[-] : CMD_1 \longrightarrow State_1 \twoheadrightarrow State_1 \\ \hline \mathcal{DCMD}[skip]\sigma = \sigma \\ \mathcal{DCMD}[assign(\xi, \epsilon)]\sigma = \dots \\ \mathcal{DCMD}[choice(\epsilon, \gamma_1, \gamma_2)]\sigma = \dots \end{array} \right.$$

To reduce any confusion this style of abbreviation might cause, the same symbols are consistently used to refer to things of the same type: this usage is summarized in Appendix D.

## 2.7 Distinguishing syntax from semantics

There is potential for confusion in distinguishing syntactic and semantic values, because some syntax is needed in order to write down the semantic value! For example, assume the value meaning function for Turandot values has been declared as

$$\left| \mathcal{DVAL}[-] : VALUE_1 \longrightarrow \mathbb{Z} \right.$$

Then the meaning of a particular value might look something like  $\mathcal{DVAL}[[42]] = 42$ . At first sight, this looks rather odd. However, the explanation is straightforward. The digit sequence appearing on the left, inside the special double square brackets, is merely syntax (the value consisting of the character '4' followed by the character '2') whereas the one on the right is the corresponding mathematical *number* (forty

two). The syntax can take a variety of forms, for example, *XLIII* in Roman numerals, or 101010 in binary, but the mathematical value is the same in each case. The confusion occurs because the concrete syntaxes chosen to write programming language syntax and to write mathematical values can have strings in common.

It is possible to map syntactic numerals to mathematical numbers by defining a mapping from character strings to integers,  $String \mapsto \mathbb{Z}$ , but this is not very illuminating, since the integers tend themselves to be written as character strings, as above. So the syntactic and semantic domains corresponding to *VALUES*, or to *NAMEs* are not distinguished.

Because of this potential confusion, denotational semantics uses a typographical convention to distinguish syntactic arguments from mathematical values. Special double square brackets are used consistently to enclose syntactic arguments, for example,  $\mathcal{M}[[3]] = 3$ . Although this convention is not standard  $\mathbb{Z}$ , it is used here for functions that return mathematical values.  $\mathbb{Z}$  uses this form of bracket for bags (multisets); there is no potential for confusion here since bags are not used in the following specifications. In addition, special triangular brackets are used to distinguish the syntactic arguments in functions that return *syntactic* results (the concrete syntax and translation functions), for example,  $\mathcal{C}\langle 3 \rangle = \text{“three”}$ .

## 2.8 Further reading

See [Meyer 1990] and [Tennent 1991] for further discussion on the use of set theory and partial functions, instead of domain theory, in denotational semantics.

Descriptions of lexing and parsing can be found in many classic texts on compiler writing; see, for example, [Aho and Ullman 1977].

The  $\mathbb{Z}$  language is described in [Spivey 1992]. [Hayes 1993] contains many case studies, using a slightly older variant of  $\mathbb{Z}$ . There are many tutorial introductions to  $\mathbb{Z}$ , for example, [Potter *et al.* 1991].

[Austin 1976] gives an account of how English can be considered an ‘imperative’ language.



---

# Using Prolog

## 3.1 Modelling Z in Prolog

The specification of the operational semantics, the translation from source to target language, specifies the compiler. A Prolog version of the specification directly gives a compiler. If we can find a clear translation from Z to Prolog, this strengthens one of the links in the chain of developing a high integrity compiler: the implementation follows directly from the formal specification.

Later sections in this chapter illustrate a direct translation of the specification of Turandot's commands into Prolog, and explains why this approach needs to be extended by a structuring mechanism: DCTGs. But first, let's see how Z sets are modelled in Prolog.

Z's sets are modelled as Prolog lists. So, for example, the Z set  $\{1, 2, 3\}$  can be represented in Prolog as the list `[1, 2, 3]`. Z functions are also sets (of pairs); they too can be modelled as Prolog lists (of lists). So the state function  $\{x \mapsto 3, y \mapsto 9\}$  can be represented in Prolog as `[ [x,3], [y,9] ]`.

Various predicates to manipulate such lists representing sets, such as `union` and `intersection`, are supplied in many Prolog libraries. These have been written to ensure that the order inherent in a Prolog list is not significant when it is used to model a set. Other predicates to perform Z-specific operations must be specially written. For example, a definition to update a state with a (name, value) pair can be written as

```
updatestate(Name, Value, Pre, Post) :-
    setminus(Pre, [Name, _], Mid),
    union(Mid, [ [Name, Value] ], Post).
```

(In Prolog, names beginning with capital letters are variables.) Here `setminus(A, B, C)` is a previously-written clause that obeys  $A \setminus \{B\} = C$ . If the clause is executed with A and B given, C will be instantiated to the set A with element B removed. `Mid` is instantiated to `Pre` with pairs whose first element is `Name` and second is anything (indicated by the underscore) removed.

Similarly, `union(A, B, C)` obeys  $A \cup B = C$ . If the clause is executed with `A` and `B` given, `C` will be instantiated to the set union of `A` and `B`. `Post` is instantiated with the union of `Mid` and the set containing the single pair `[Name, Value]` (that the second parameter is a set, not an element, explains the double pair of square brackets).

Prolog has the powerful feature that, if a different selection of parameters is provided, the remaining ones will be instantiated. So if `union` is provided with `B` and `C`, it will instantiate `A` so that  $A \cup B = C$  is true. Powerful though this feature is, it is not exploited in the following definitions, where the parameters are always supplied in the ‘forward’ order corresponding to program execution.

## 3.2 Writing semantics in Prolog—a first attempt

With a suitable set of library definitions like those above, there is a straightforward translation from the semantics definitions in *Z* to ones in Prolog. (The rules for this translation in the case of Tosca are explained in Chapter 14.)

### 3.2.1 Dynamic semantics—an interpreter

Consider the Turandot example from Chapter 2. The specification of the dynamic semantics of Turandot’s commands (section 2.3.4) can be written in Prolog as follows.

For commands, the Prolog form is

```
command(gamma, PreState, PostState) :-
    expression relating PreState and PostState
```

with one clause for each sort of command `gamma` in the abstract syntax definition. `PreState` is the state before the command, `PostState` is the state that results from executing the command.

*skip* is the easiest to translate, because the state is not changed:

```
command(skip, PreState, PostState) :-
    PreState = PostState.
```

This clause declares that, for a `skip` command, the `PreState` and `PostState` have the same value, whatever that value is. If the clause is being executed, as part of a Turandot interpreter, a value would be supplied for `PreState`, and `PostState` would become instantiated with the same value:

```
command(assign(Name, Expr), PreState, PostState) :-
    expr(Expr, PreState, Value),
    updatestate(Name, Value, PreState, PostState).
```

This clause declares that, for an `assign` command, `Value` is the value of `Expr` in the `PreState`, and that the `PreState` updated with `Name` and `Value` is the `PostState`.

If the clause was being executed, the values for `Name`, `Expr` and `PreState` would be supplied, and used to instantiate `Value` and `PostState` appropriately.

The choice command becomes

```
command(choice(Expr,Then,Else), PreState, PostState) :-
    expr(Expr, PreState, Value),
    (
        Value = 1,
        command(Then, PreState, PostState)
    ;
        Value = 0,
        command(Else, PreState, PostState)
    ).
```

`Value` is the value of `Expr` in the `PreState`. Depending on the value of `Value`, either the `PostState` is found using the `Then` parameter, or (as indicated by the Prolog ‘;’) by using the `Else` parameter:

```
command(compose(Cmd1,Cmd2), PreState, PostState) :-
    command(Cmd1, PreState, MidState),
    command(Cmd2, MidState, PostState).
```

The `MidState` is found from the `PreState` using `Cmd1`, and is used along with `Cmd2` to produce the `PostState`.

If these clauses are read declaratively, they provide a Prolog specification of the dynamic semantics. If they are read operationally (or executed), they directly provide an interpreter for the language. To illustrate this dichotomy, the English explanation given above changes tone from declarative to operational as the example progresses.

### 3.2.2 Static semantics—a use checker

Turandot’s static semantics can be written in Prolog in a very similar manner. The s-meaning of its commands (section 2.4) can be written as:

```
scommand(skip, PreSState, PostSState) :-
    PreSState = PostSState.

scommand(assign(Name,Expr), PreSState, PostSState) :-
    union(PreSState, [Name], PostSState).

scommand(choice(Expr,Then,Else), PreSState, PostSState) :-
    scommand(Then, PreSState, SStateThen),
    scommand(Else, PreSState, SStateElse),
    intersection(SStateThen, SStateElse, PostSState).

scommand(compose(Cmd1,Cmd2), PreSState, PostSState) :-
    scommand(Cmd1, PreSState, MidSState),
    scommand(Cmd2, MidSState, PostSState).
```

If these clauses are read declaratively, they provide a Prolog specification of the static semantics. If they are read operationally (or executed), they directly provide a static checker for the language.

### 3.2.3 Code templates—a compiler

The specification of Turandot's compiler can be written in Prolog in an analogous manner. Rather than defining a `PostState`, however, the Prolog translation of the commands (section 2.5) defines a list of target language instructions.

```
compile(skip, [ ]).
compile(assign(Name,Expr), [InstrList, store(Name)]) :-
    compile(Expr, InstrList).
compile(choice(Expr,Then,Else),
    [InstrExpr, jump(L1), InstrThen,
    goto(L2), label(L1), InstrElse, label(L2)]
) :-
    compile(Expr, InstrExpr),
    compile(Then, InstrThen),
    compile(Else, InstrElse).
compile(compose(Cmd1,Cmd2), [InstrList1, InstrList2]) :-
    compile(Cmd1, InstrList1),
    compile(Cmd2, InstrList2).
```

If these clauses are read declaratively, they provide a Prolog specification of the compiler translation. If they are read operationally (or executed), they directly provide the compiler.

## 3.3 Definite Clause Translation Grammars

Although this sort of translation into Prolog is quite straightforward for such a small example, it soon becomes unwieldy. For example, the syntax and semantics definitions of each construct are closely intertwined, and the syntax has to be repeated in each of the different semantics. A better structuring mechanism is needed for a full language with multiple semantics.

Many standard Prologs have a mechanism called Definite Clause Grammars (DCGs) built into them, to allow expressions such as

```
sentence --> noun_phrase, verb_phrase.
noun_phrase --> determiner, noun.
```

to be manipulated. These are automatically converted, by the Prolog system, into their standard Prolog equivalents with the necessary extra arguments:

```
sentence(S0,S) :- noun_phrase(S0,S1), verb_phrase(S1,S).
noun_phrase(S0,S) :- determiner(S0,S1), noun(S1,S).
```

This approach is suitable for defining syntax. For defining semantics as well, even more arguments are needed in the standard Prolog form. These can be provided automatically by using Definite Clause Translation Grammars (DCTGs).

DCTGs provide a general mechanism for grammar computations. The process of successfully parsing a particular input program causes a *parse tree* to be built. The semantic rules are attached to the non-terminal nodes of this parse tree, and used to define the semantic properties of a node in terms of the semantic properties of its subtrees.

Although not directly supported by the Prolog system in the same way as DCGs, Prolog operators and predicates can be defined to support the DCTG approach. These translate a program written in the DCTG form into an equivalent program in standard Prolog. As with the DCG form, this translation provides the various clauses with extra arguments, which are used to define how to build the annotated parse tree.

As a simple example, consider a possible DCTG definition for adding two expressions to produce an expression:

```

expr ::= expr^^Tree1, tPLUS, expr^^Tree2
<:>
value(V) :-
    Tree1^^value(V1),
    Tree2^^value(V2),
    V is V1 + V2.

```

The first part of the term (before the <:>) defines the concrete syntax. In this example it says that an expression can be a subexpression, a ‘plus’ token and another subexpression. The subexpressions are labelled with their parse trees. The second part of the term defines the semantics of the composite expression in terms of its subexpressions. Here it says the value of the composite expression is the arithmetic sum of the values of the two subexpressions.

The translation into plain Prolog looks like:

```

expr( node( expr, [Tree1, Tree2],
            [value(V) :-
              Tree1^^value(V1),
              Tree2^^value(V2),
              V is V1+V2]
          ),
      S0, S ) :-
    expr(Tree1, S0, S1),
    expr(Tree2, S1, S).

```

At first sight, this technique may look more complicated than using plain Prolog. It does, however, have the advantage of cleanly separating the syntax and semantics. Another important advantage of this approach is that a DCTG can be used to support multiple sets of different semantics attached to each node, as extra elements in the `node` list. So Turandot’s *choice* command can be written using

the DCTG formalism, including the code template (compiler), dynamic semantics (interpreter), and static semantics (static checker), as

```

command ::=
  tIF, expr^^E,
  tTHEN, command^^Cthen,
  tELSE, command^^Celse
<:>
  (static(PreSState, PostSState) :-
    Cthen^^static(PreSState, SState1),
    Celse^^static(PreSState, SState2),
    intersection(SState1, SState2, PostSState)
  ),
  (dynamic(PreState, PostState) :-
    E^^dynamic(PreState, Value),
    ( Value = 1,
      Cthen^^dynamic(PreState, PostState)
    ;
      Value = 0,
      Celse^^dynamic(PreState, PostState)
    )
  ),
  (compile( [InstrExpr, jump(L1), InstrThen,
            goto(L2), label(L1), InstrElse, label(L2)] )
  :-
    E^^compile(InstrExpr),
    Cthen^^compile(InstrThen),
    Celse^^compile(InstrElse)
  ).

```

Notice that the non-standard static semantics `static` is attached to the DCTG nodes in the same manner as the dynamic semantics `dynamic` and the operational semantics `compile`. Other non-standard semantics for various other types of analyses can be incrementally added to each node in a similar, consistent manner. When executed, each provides a further static analysis checker.

Notice how, with the DCTG approach, the various semantics are in similar forms, and occur textually close together in the Prolog code. This is of great advantage in the process of demonstrating the correctness of the translation from the mathematical specification to the executable compiler.

Chapter 14 gives the DCTG form of the definition of Tosca's various static and dynamic semantics, and its operational semantics in terms of Aida.

### **3.4 Further reading**

For a good introduction to Prolog, see, for example [Sterling and Shapiro 1986] and [Clocksin and Mellish 1987]. [Clocksin and Mellish 1987, Chapter 9] describes DCGs.

The style used in section 3.2 follows [Warren 1980].

DCTGs are described in [Abramson and Dahl 1989, Chapter 9], and the algorithm to convert a DCTG program to Prolog is listed in [Abramson and Dahl 1989, Appendix II.3]. The DCTG approach is the logic programming equivalent of Attribute Grammars [Knuth 1968]. The DCTG formalism does not distinguish between inherited and synthesized attributes, however, because Prolog's unification mechanism makes such a distinction largely unnecessary.





## Part II

# Tosca—the High Level Language



# Tosca—Syntax

## 4.1 Overview

The example hypothetical high level language, Tosca (*Totally Okay for Safety Critical Applications*), is the sort of simple imperative language subset often preferred for safety critical or high integrity applications, and is a typical ‘first step’ on the road to a trusted full high level language. Tosca has two types, integer and boolean, and all declarations are global. It has a **while-do** loop and an **if-then-else** choice. Other loops (for example, **repeat-until** or **for** loops) and choices (for example, **case**) have similar specifications, and their inclusion would merely add bulk, not new insight, to the discussion.

Tosca’s semantics is specified in Chapter 8, on a construct-by-construct basis. Its abstract syntax is given in this chapter, along with the specification of a mapping to its concrete syntax.

## 4.2 Strings

Tosca’s concrete syntax is produced from the abstract syntax by mapping the constructs to strings of characters. Tosca is given a rather conventional concrete syntax: free format (so white space, including new lines, can be placed between any tokens) with commands and declarations terminated (*not* separated) by semicolons:

[*CHAR*]

*String* == seq *CHAR*

Strings are enclosed in quotes, rather than *Z*’s sequence brackets, for clarity:

“**this is a string**”

rather than

$\langle \text{'t', 'h', 'i', 's', ' ', 'i', 's', ' ', 'a', ' ', 's', 't', 'r', 'i', 'n', 'g'} \rangle$

### 4.3 Names, types and values

Variable names (identifiers) are treated as a  $\mathbb{Z}$  given set, so their internal structure is not defined further:

$[NAME]$

Tosca has two types: integer and boolean. A ‘dummy’ type is also needed for the purposes of specification; it is used only in the static type checking semantics, but has to be given here to complete the definition:

$$\begin{array}{l} TYPE ::= integer \\ \quad | \quad \mathbf{boolean} \\ \quad | \quad typeWrong \end{array}$$

The concrete form of the Tosca types is just a simple keyword. The dummy type is never used in a program text, and so needs no concrete form:

$$\left| \begin{array}{l} \mathcal{C}_T \langle \_ \rangle : TYPE \leftrightarrow String \\ \hline \mathcal{C}_T \langle integer \rangle = \mathbf{“int”} \\ \mathcal{C}_T \langle boolean \rangle = \mathbf{“bool”} \end{array} \right.$$

$\mathbb{Z}$  does not have a boolean type, so one must be defined in order to model Tosca’s boolean type. Because  $\mathbb{Z}$  has keywords *true* and *false*, these cannot be used as names for the elements of the new boolean type (not even by using a different style of font, unfortunately):

$$Boolean ::= \mathbf{T} \mid \mathbf{F}$$

$\mathbb{Z}$  does have an integer type, which can be used in modelling Tosca’s integers. The range of integers supported by Tosca is specified loosely:

$$\left| \begin{array}{l} minInt, maxInt : \mathbb{Z} \\ \hline minInt < 0 < maxInt \end{array} \right.$$

$$Integer == minInt .. maxInt$$

Values in Tosca are either integer or boolean, corresponding to the two types:

$$\begin{aligned} \text{VALUE} ::= & \text{int}_v \langle\langle \text{Integer} \rangle\rangle \\ & | \text{bool}_v \langle\langle \text{Boolean} \rangle\rangle \end{aligned}$$

No functions are defined to map abstract Tosca names and values to a concrete representation. Instead, abstract and concrete names are assumed to consist of the same string of characters, concrete boolean values are written as “**true**” and “**false**”, and concrete integer values are written as a base ten digit string. The types of these functions are

$$\left| \begin{array}{l} \mathcal{C}_N \langle - \rangle : \text{NAME} \rightarrow \text{String} \\ \mathcal{C}_V \langle - \rangle : \text{VALUE} \rightarrow \text{String} \end{array} \right.$$

## 4.4 Declarations

A Tosca declaration consists of the name of the declared variable and its type. The syntax for variable declarations is

$$\text{DECL} ::= \text{declVar} \langle\langle \text{NAME} \times \text{TYPE} \rangle\rangle$$

In concrete form, a variable declaration consists of the variable name and its type, separated by a colon:

$$\left| \begin{array}{l} \mathcal{C}_D \langle - \rangle : \text{DECL} \rightarrow \text{String} \\ \hline \mathcal{C}_D \langle \text{declVar}(\xi, \tau) \rangle = \mathcal{C}_N \langle \xi \rangle \hat{\wedge} \text{“:”} \hat{\wedge} \mathcal{C}_T \langle \tau \rangle \end{array} \right.$$

So, for example,

$$\begin{aligned} \mathcal{C}_D \langle \text{declVar}(x, \text{integer}) \rangle &= \text{“x : int”} \\ \mathcal{C}_D \langle \text{declVar}(b, \text{boolean}) \rangle &= \text{“b : bool”} \end{aligned}$$

Multiple declarations are simply a sequence of variable declarations:  $\text{seq DECL}$ . In concrete form, a multiple declaration list has each declaration terminated by a semicolon:

$$\left| \begin{array}{l} \mathcal{C}_{D^*} \langle - \rangle : \text{seq DECL} \rightarrow \text{String} \\ \hline \mathcal{C}_{D^*} \langle \langle \rangle \rangle = \text{“ ”} \\ \mathcal{C}_{D^*} \langle \langle \delta \rangle \rangle = \mathcal{C}_D \langle \delta \rangle \hat{\wedge} \text{“;”} \\ \mathcal{C}_{D^*} \langle \Delta_1 \hat{\wedge} \Delta_2 \rangle = \mathcal{C}_{D^*} \langle \Delta_1 \rangle \hat{\wedge} \mathcal{C}_{D^*} \langle \Delta_2 \rangle \end{array} \right.$$

For example,

$$\mathcal{C}_{D^*} \langle \langle \text{declVar}(x, \text{integer}), \text{declVar}(b, \text{boolean}) \rangle \rangle = \text{“x : int ; b : bool ;”}$$

Aside—at first sight, it might seem simpler to define  $\mathcal{C}_{D^*}\langle - \rangle$  using only two cases: the empty case and a single recursive case of the form  $\mathcal{C}_{D^*}\langle \langle \delta \rangle \hat{\ } \Delta \rangle$ . Indeed, the three-case definition reduces to this two-case alternative when  $\Delta_1 = \langle \delta \rangle$ . However, by taking  $\Delta_2 = \langle \delta \rangle$  instead, the recursion can also be unwound from the other end, if required. And sometimes it is useful to use the full power of the three-case definition to split a sequence into two pieces both larger than singletons (a style used mainly in the proofs, see Chapter 13). So the more general three-case style is preferred.

That the final result is independent of how the sequence is split into  $\Delta_1$  and  $\Delta_2$  follows from the associativity of concatenation (and, in later more complicated expressions (sections 8.2.2 and 8.5.2), from the associativity of the operators used to combine their parts).

## 4.5 Operators

Tosca has unary operators (that take one argument and return a result) and binary operators (that take two arguments and return a result). These are further broken down into arithmetic operators (that take numbers and return numbers), comparison operators (that take numbers and return booleans), and logical operators (that take booleans and return booleans).

The binary arithmetic operators are the standard addition and subtraction (more arithmetic operators could be included in a similar manner):

$$\begin{aligned} \mathit{BIN\_ARITH\_OP} ::= & \text{plus} \\ & | \text{minus} \end{aligned}$$

The binary comparison operators are the standard tests for equality and inequality (again, more operators could be included):

$$\begin{aligned} \mathit{BIN\_COMP\_OP} ::= & \text{less} \\ & | \text{greater} \\ & | \text{equal} \end{aligned}$$

The binary logical operators are conjunction and disjunction:

$$\begin{aligned} \mathit{BIN\_LOGIC\_OP} ::= & \text{or} \\ & | \text{and} \end{aligned}$$

So the binary operators are

$$\begin{aligned} \mathit{BIN\_OP} ::= & \text{binArithOp}\langle\langle \mathit{BIN\_ARITH\_OP} \rangle\rangle \\ & | \text{binCompOp}\langle\langle \mathit{BIN\_COMP\_OP} \rangle\rangle \\ & | \text{binLogicOp}\langle\langle \mathit{BIN\_LOGIC\_OP} \rangle\rangle \end{aligned}$$

Similarly, the unary operators are broken down into arithmetic operators and logical operators. Here there is only one in each kind:

$$\begin{aligned} UNY\_ARITH\_OP &::= \text{negate} \\ UNY\_LOGIC\_OP &::= \text{not} \end{aligned}$$

So the unary operators are

$$\begin{aligned} UNY\_OP &::= \text{unyArithOp}\langle\langle UNY\_ARITH\_OP \rangle\rangle \\ &| \text{unyLogicOp}\langle\langle UNY\_LOGIC\_OP \rangle\rangle \end{aligned}$$

A note on specification style: this treatment of the unary operators may appear a little heavy-handed; why not just say

$$\begin{aligned} UNY\_OP_0 &::= \text{negate} \\ &| \text{not} \end{aligned}$$

However, this approach gives a more uniform treatment to the static semantics of operators (the definitions for unary and binary operators look similar, see section 8.3), and allows any new unary operators to be added to the language more easily. Examples like this, with forward references to the reason why certain choices have been made, indicate that writing a specification is an iterative process: early choices may be changed later when their consequences become apparent.

The concrete form of operators is simply some appropriate string chosen to represent the operator. The unary operators are written as

$$\begin{array}{|l} \mathcal{C}_U\langle \_ \rangle : UNY\_OP \rightarrow String \\ \hline \mathcal{C}_U\langle \text{unyArithOp negate} \rangle = \text{“-”} \\ \mathcal{C}_U\langle \text{unyLogicOp not} \rangle = \text{“not”} \end{array}$$

and the binary operators as

$$\begin{array}{|l} \mathcal{C}_B\langle \_ \rangle : BIN\_OP \rightarrow String \\ \hline \mathcal{C}_B\langle \text{binArithOp plus} \rangle = \text{“+”} \\ \mathcal{C}_B\langle \text{binArithOp minus} \rangle = \text{“-”} \\ \\ \mathcal{C}_B\langle \text{binCompOp less} \rangle = \text{“<”} \\ \mathcal{C}_B\langle \text{binCompOp greater} \rangle = \text{“>”} \\ \mathcal{C}_B\langle \text{binCompOp equal} \rangle = \text{“=”} \\ \\ \mathcal{C}_B\langle \text{binLogicOp or} \rangle = \text{“or”} \\ \mathcal{C}_B\langle \text{binLogicOp and} \rangle = \text{“and”} \end{array}$$

Notice that the strings chosen to represent unary negate and binary minus are the same; distinguishing between these in a program text is a parsing problem. These parsing concerns do not occur in the semantics definitions, where we are dealing with unambiguous abstract syntax.

## 4.6 Expressions

A Tosca expression is either a constant, a named variable, a unary expression, or a binary expression:

$$\begin{aligned}
 \text{EXPR} ::= & \text{const}\langle\langle \text{VALUE} \rangle\rangle \\
 & | \text{var}\langle\langle \text{NAME} \rangle\rangle \\
 & | \text{unyExpr}\langle\langle \text{UNY\_OP} \times \text{EXPR} \rangle\rangle \\
 & | \text{binExpr}\langle\langle \text{EXPR} \times \text{BIN\_OP} \times \text{EXPR} \rangle\rangle
 \end{aligned}$$

Expressions that are constants or variables are written using whatever concrete syntax has been chosen for them. Unary expressions are written with the operator first. Binary expressions are written with the binary operator in infix position, and are parenthesized; parenthesizing all binary expressions is an alternative to defining an operator precedence:

$$\begin{array}{|l}
 \hline
 \mathcal{C}_E\langle - \rangle : \text{EXPR} \rightarrow \text{String} \\
 \hline
 \mathcal{C}_E\langle \text{const } \chi \rangle = \mathcal{C}_V\langle \chi \rangle \\
 \mathcal{C}_E\langle \text{var } \xi \rangle = \mathcal{C}_N\langle \xi \rangle \\
 \mathcal{C}_E\langle \text{unyExpr}(\psi, \epsilon) \rangle = \mathcal{C}_U\langle \psi \rangle \wedge \mathcal{C}_E\langle \epsilon \rangle \\
 \mathcal{C}_E\langle \text{binExpr}(\epsilon_1, \omega, \epsilon_2) \rangle = "(" \wedge \mathcal{C}_E\langle \epsilon_1 \rangle \wedge \mathcal{C}_B\langle \omega \rangle \wedge \mathcal{C}_E\langle \epsilon_2 \rangle \wedge ")"
 \end{array}$$

For example,

$$\mathcal{C}_E\langle \text{const}(\text{int}_v 9) \rangle = \text{"9"}$$

$$\mathcal{C}_E\langle \text{var } x \rangle = \text{"x"}$$

$$\mathcal{C}_E\langle \text{unyExpr}(\text{unyArithOp negate}, \text{const}(\text{int}_v 9)) \rangle = \text{"- 9"}$$

$$\mathcal{C}_E\langle \text{binExpr}(\text{var } x, \text{binArithOp plus}, \text{const}(\text{int}_v 9)) \rangle = \text{"(x + 9)"}$$

## 4.7 Commands

A Tosca command is either a skip, a block (sequence of commands), an assignment, a conditional choice, a while loop, an input, or an output:

$$\begin{aligned}
 \text{CMD} ::= & \text{skip} \\
 & | \text{block}\langle\langle \text{seq}_1 \text{ CMD} \rangle\rangle \\
 & | \text{assign}\langle\langle \text{NAME} \times \text{EXPR} \rangle\rangle \\
 & | \text{choice}\langle\langle \text{EXPR} \times \text{CMD} \times \text{CMD} \rangle\rangle \\
 & | \text{loop}\langle\langle \text{EXPR} \times \text{CMD} \rangle\rangle \\
 & | \text{input}\langle\langle \text{NAME} \rangle\rangle \\
 & | \text{output}\langle\langle \text{EXPR} \rangle\rangle
 \end{aligned}$$



Notice that the block is required to have at least one command in it; this may be a **skip**.

Multiple commands are simply a sequence of commands:  $\text{seq } CMD$ . The concrete syntax for commands is a conventional keyword-style syntax. A multiple command list has each command terminated by a semicolon:

$\mathcal{C}_C \langle \_ \rangle : CMD \rightarrow String$ $\mathcal{C}_{C^*} \langle \_ \rangle : \text{seq } CMD \rightarrow String$
$\mathcal{C}_C \langle \text{block } \Gamma \rangle = \text{“begin”} \wedge \mathcal{C}_{C^*} \langle \Gamma \rangle \wedge \text{“end”}$
$\mathcal{C}_C \langle \text{skip} \rangle = \text{“skip”}$
$\mathcal{C}_C \langle \text{assign}(\xi, \epsilon) \rangle = \mathcal{C}_N \langle \xi \rangle \wedge \text{“:=”} \wedge \mathcal{C}_E \langle \epsilon \rangle$
$\mathcal{C}_C \langle \text{choice}(\epsilon, \gamma_1, \gamma_2) \rangle =$ $\quad \text{“if”} \wedge \mathcal{C}_E \langle \epsilon \rangle \wedge \text{“then”} \wedge \mathcal{C}_C \langle \gamma_1 \rangle \wedge \text{“else”} \wedge \mathcal{C}_C \langle \gamma_2 \rangle$
$\mathcal{C}_C \langle \text{loop}(\epsilon, \gamma) \rangle = \text{“while”} \wedge \mathcal{C}_E \langle \epsilon \rangle \wedge \text{“do”} \wedge \mathcal{C}_C \langle \gamma \rangle$
$\mathcal{C}_C \langle \text{input } \xi \rangle = \text{“input”} \wedge \mathcal{C}_N \langle \xi \rangle$
$\mathcal{C}_C \langle \text{output } \epsilon \rangle = \text{“output”} \wedge \mathcal{C}_E \langle \epsilon \rangle$
$\mathcal{C}_{C^*} \langle \langle \rangle \rangle = \text{“”}$
$\mathcal{C}_{C^*} \langle \langle \gamma \rangle \rangle = \mathcal{C}_C \langle \gamma \rangle \wedge \text{“;”}$
$\mathcal{C}_{C^*} \langle \Gamma_1 \wedge \Gamma_2 \rangle = \mathcal{C}_{C^*} \langle \Gamma_1 \rangle \wedge \mathcal{C}_{C^*} \langle \Gamma_2 \rangle$

For example,

$$\begin{aligned} \mathcal{C}_C \langle \text{assign}(x, \text{const}(int_v 3)) \rangle &= \text{“x := 3”} \\ \mathcal{C}_C \langle \text{choice}(\text{var } b, \text{assign}(x, \text{var } y), \text{skip}) \rangle &= \\ &\quad \text{“if b then x := y else skip”} \\ \mathcal{C}_C \langle \text{loop}(\text{var } b, \text{assign}(x, \text{binExpr}(\text{var } x, \text{binArithOp minus}, \text{const}(int_v 1))) \rangle &= \\ &\quad \text{“while b do x := (x - 1)”} \\ \mathcal{C}_C \langle \text{input } x \rangle &= \text{“input x”} \\ \mathcal{C}_C \langle \text{output } \text{unyExpr}(\text{minus}, \text{var } y) \rangle &= \text{“output -y”} \\ \mathcal{C}_C \langle \text{block}(\text{input } x, \text{output}(\text{const}(int_v 3))) \rangle &= \\ &\quad \text{“begin input x; output 3; end”} \end{aligned}$$

## 4.8 Program

A Tosca program is a sequence of declarations and a command:

$$PROG ::= \text{Tosca} \langle \langle \text{seq } DECL \times CMD \rangle \rangle$$

Notice that the declaration list may be empty. It is possible to write correct Tosca programs that declare no variables, but they are not very interesting.

In concrete form, a Tosca program is written by concatenating the declarations and the command:

$$\left| \begin{array}{l} \mathcal{C}_P \langle \_ \rangle : PROG \rightarrow String \\ \hline \mathcal{C}_P \langle \text{Tosca}(\Delta, \gamma) \rangle = \mathcal{C}_{D^*} \langle \Delta \rangle \frown \mathcal{C}_C \langle \gamma \rangle \end{array} \right.$$

For example,

$$\mathcal{C}_P \langle \text{Tosca}(\langle \rangle, \text{skip}) \rangle = \text{“skip”}$$

$$\mathcal{C}_P \langle \text{Tosca}(\langle \rangle, \text{output}(\text{const}(\text{int}_v \ 3))) \rangle = \text{“output 3”}$$

$$\begin{aligned} \mathcal{C}_P \langle \text{Tosca}(\langle \text{declVar}(x, \text{integer}) \rangle, \\ \text{block}(\text{assign}(x, \text{const}(\text{int}_v \ 3)), \text{output}(\text{var } x))) \rangle = \\ \text{“x : int ; begin x := 3; output x; end”} \end{aligned}$$

# A Running Example— the ‘Square’ Program

## 5.1 Introduction

In order to demonstrate how all the various specifications work, a simple running example is used throughout the book, and is introduced here. The example is a program that inputs a positive integer  $n$ , and outputs the squares from one up to  $n^2$ . In the interests of simplicity, the example does not validate its input.

The dynamic meaning of the example Tosca program is calculated in section 9.2. It is compiled into the target language in section 12.1, and the meaning of the compiled version is calculated in section 12.2.

## 5.2 Specification

A specification of the example in  $Z$  is

$$\left| \begin{array}{l} \textit{square} : \mathbb{N}_1 \rightarrow \text{seq } \mathbb{N} \\ \hline \textit{square } 1 = \langle 1 \rangle \\ \forall n : \mathbb{N} \mid n > 1 \bullet \textit{square } n = \textit{square}(n - 1) \hat{\ } \langle n * n \rangle \end{array} \right.$$

## 5.3 Concrete syntax

A suitable program that implements the *square* function, written in Tosca’s concrete syntax, is

```

n : int ; sq : int ; limit : int ;
begin
  n := 1 ; sq := 1 ;
  input limit ;
  output sq ;
  while ( n < limit ) do
  begin
    sq := ( ( sq + 1 ) + ( n + n ) ) ;
    n := ( n + 1 ) ;
    output sq ;
  end ;
end

```

## 5.4 Abstract syntax

In abstract syntax, this program is

```

square ==
Tosca(⟨declVar(n, integer), declVar(sq, integer), declVar(limit, integer)⟩,
  block⟨assign(n, const(intv 1)),
    assign(sq, const(intv 1)),
    input limit, output(var sq),
    loop(binExpr(var n, less, var limit),
      block⟨assign(sq,
        binExpr(binExpr(var sq, plus, const(intv 1)),
          plus, binExpr(var n, plus, var n))),
        assign(n, binExpr(var n, plus, const(intv 1))),
        output(var sq)⟩⟩⟩)

```

which rather graphically illustrates why concrete, not abstract, syntax is used for writing programs!

---

# Partitioning the Specification

## 6.1 Undefined meanings

Consider the following highly erroneous Tosca ‘program’:

```

b : bool;
x : int; y : int;
begin
    x := ;      - - syntax error
    z := 1;    - - z not declared
    x := z;    - - z not declared
    x := b;    - - incompatible types
    x := y;    - - y not initialized
end

```

A program must be syntactically correct before any of its meanings (static or dynamic) are defined. So the meaning of the above fragment is *undefined*.

Even if the syntax error is corrected, there are many other things wrong, and the formal meaning of the program should still be *undefined*. Hence, the semantic meaning function is *partial*; it is defined for only some programs, that satisfy certain well-formedness conditions. For example, the conditions relevant to an assignment command are (informally)

$$\mathcal{M}_C[[\textit{assignment}]] =$$

```

if (target variable is declared)
     $\wedge$  (source expression has no undeclared variables)
     $\wedge$  (source and target have the same type)
     $\wedge$  (source expression has no uninitialized variables)
then (definition of meaning)
else checkWrong

```

Checking that all these conditions are met in one lump, as above, results in a clumsy style of specification that seems to put more emphasis on what does not

happen, than on what does. A better approach is to partition the specification into several logically separate static checks and a definition of the dynamic (execution) meaning. Such partitioning means that there is no need to check, for example, in the dynamic semantic definitions, that expressions are of the right type (they are; they have passed the type checking), or that variables have been initialized before they are used (they have; they have passed the initialization checking).

This ability to separate concerns puts structure on an otherwise large monolithic specification. It results in several simpler specifications, one for each semantics, since ‘error cases’ do not have to be considered each time. It gives a more uniform approach, since it can be clearly seen that each static check has been made for each construct. The separation also serves to highlight each of these semantics, showing its purpose.

Not every language can have its semantics partitioned so neatly; achieving a clean separation in an existing language is more difficult than designing the separation in from the start. Tosca has been designed to be separable in this way, and has defined three static semantics—declaration checking, type checking and use checking—and a dynamic semantics. This chapter gives an overview of the purpose of each of the semantics, by informally describing the kind of conditions that each one is designed to check. The following chapters specify Tosca by formally defining each of its four semantics.

## 6.2 Syntax

If a Tosca program is not syntactically correct, then its declaration-before-use, type, initialization-before-use and dynamic semantics are *undefined*.

## 6.3 Declaration-before-use semantics

The first static check made on a syntactically correct Tosca program is a declaration-before-use check, to make sure that any variables used in the program have been correctly declared. For example, an informal reading of type checking an assignment command is

$$\begin{aligned} \mathcal{D}_C[[assignment]] = & \\ & \mathbf{if} \text{ (target variable is declared)} \\ & \quad \wedge \text{ (source expression has no undeclared variables)} \\ & \mathbf{then } checkOK \\ & \mathbf{else } checkWrong \end{aligned}$$

Only if the whole program passes this static check are the other semantics defined. Because this is the first check done, the declaration check meaning functions are in fact *total*: this semantics is defined for any syntactically correct Tosca program.

If  $\mathcal{D}_P[\textit{program}] = \textit{checkWrong}$ , then the program does not pass its declaration-before-use check, and so its type, initialization-before-use and dynamic semantics are *undefined*.

## 6.4 Type-checking semantics

If a Tosca program passes its declaration-before-use check, it is subject to a type-check. The type-checking semantics defines conditions for a program to be well-typed: in the case of an assignment the check is (informally)

$$\begin{aligned} \mathcal{T}_C[\textit{assignment}] = & \\ & \mathbf{if} \text{ (source and target have the same type)} \\ & \mathbf{then } \textit{checkOK} \\ & \mathbf{else } \textit{checkWrong} \end{aligned}$$

In this definition, there is no need to first check that the variables have a type: since we know they have been declared, they do. Hence  $\mathcal{T}_C[\ ]$  is a partial function: it is defined only for declaration-checked commands, and *undefined* for others.

If  $\mathcal{T}_P[\textit{program}] = \textit{checkWrong}$ , then the program does not pass its type check, and so its initialization-before-use and dynamic semantics are *undefined*.

## 6.5 Initialization-before-use semantics

If a Tosca program passes its type check, it is subject to its final static check, that any variable that is accessed has previously been initialized to some value. For an assignment command, for example, this consists of checking that there are no uninitialized variables in the source expression. Informally:

$$\begin{aligned} \mathcal{U}_C[\textit{assignment}] = & \\ & \mathbf{if} \text{ (source expression has no uninitialized variables)} \\ & \mathbf{then } \textit{checkOK} \\ & \mathbf{else } \textit{checkWrong} \end{aligned}$$

This provides a static initialization-before-use semantic check. Note that some of the formal definitions of Tosca provide a rather strict constraint on potentially unused variables (guilty until proven innocent), which eliminates programs that might otherwise be thought to be ‘correct’. It is probably appropriate to have such a strict definition for a high integrity language. More to the point, however, it does provide an unambiguous definition, which can be reasoned about, and which provides a basis for criticism if necessary.

If  $\mathcal{U}_P[\textit{program}] = \textit{checkWrong}$ , then the program does not pass its initialization-before-use check, and so its dynamic semantics are *undefined*.

## 6.6 Dynamic semantics

Only if a Tosca program passes its initialization-before-use check (and hence its declaration-before-use and type checks, too), is its dynamic meaning defined:

$$\mathcal{M}_C[\textit{assignment}] =$$

(definition of meaning)

Compare this with the form given on page 45. All the conditions to be satisfied have disappeared, because they have already been checked in the static semantics definitions. Hence, the definition is simpler and clearer.

## 6.7 Redundancy

Not all programming languages require variables to be declared, typed or initialized. For example, in the *awk* programming language, the first use of a variable implicitly declares it. If it is assigned a value of a particular type (number or string) it has that type. If its first use is on the right-hand side of an assignment, it is automatically initialized to zero, or the empty string, as appropriate. This automatic declaration and initialization allows some very concise *awk* programs, free of clutter.

So why aren't all languages like this? A famous error occurred in a Fortran program controlling the launch of a Mariner space probe. One line of the program used a full stop instead of a comma. The line should have read

```
DO 100 I = 1,10
```

This is the first line of a Fortran DO loop, and instructs the program to repeat the following code up to the line numbered 100, with I taking the values from 1 to 10. Instead, the line read

```
DO 100 I = 1.10
```

This is an assignment command, assigning the value 1.10 to the variable D0100I. (Fortran permits, and ignores, spaces in variable names.) If Fortran required variables to be declared before use, this error would have been caught by a static check; the spurious variable D0100I would have been flagged as undeclared. It was not caught, and an expensive spacecraft was lost.

So the main purpose of variable declarations and initializations is to provide a safety net; redundancy allows consistency checks. Static checks, that can be performed without having to execute the program, are particularly valuable.



## 6.8 Further reading

For a description of the *awk* language, see [Kernighan and Pike 1984, section 4.4] or [Aho *et al.* 1988]. The language's name is an acronym made from the names of its designers Aho, Weinberger and Kernighan.

The journal *ACM SIGSOFT Software Engineering Notes* often has notes and articles about computer-related incidents. The Mariner incident, along with many others, is listed in [Neumann 1985].

# Tosca—States and Environments

## 7.1 Introduction

There are two obvious ways to organize the specification of each of the four semantics for each of Tosca's various language constructs: either specify each separate semantics completely in turn, or specify each construct completely in turn. After experimenting with both organizations, the latter has been chosen. Specifying all the semantics of each language construct in one place makes it easier to understand that construct as a whole. This approach does have one drawback, however. It becomes necessary to specify quite a few auxiliary Z types and functions before their use may be apparent. These auxiliary definitions are gathered together in this chapter. It may be best to skim through on first reading, noting what things have been defined, but waiting until the next chapter to find out how they are used.

## 7.2 Semantics—general

### 7.2.1 Environment and state

A variable that has been declared but not yet initialized has no associated value. So modelling the state as a mapping from variable names to their values is not appropriate. (Using a partial function for the mapping does not help, because it does not distinguish variables not in the domain that have not been declared, from those not in the domain that have been declared, but not yet given a value.) It is conventional to introduce intermediate *locations*, and use an *environment* to map names to locations, and a *store* to map locations to values. A declaration changes the environment by assigning a new variable to a new location. But that location does not become part of the store's domain until the variable is first assigned a

value. The evolving *state* of a computation consists of this store, and the input and output streams.

In two of the static semantics, declaration-before-use and type checking, the state of a variable does not change: once declared it stays declared, once typed it retains that type. So for these an environment, but no state, is required. With the other two semantics, initialization-before-use and dynamic, the state of a variable can change. A variable starts out uninitialized when declared, and may later be initialized. In the dynamic case, a variable may take on many values as the computation progresses. So both these definitions require an environment and a state.

The domains of all these various environments are a set of variable names. Since *NAME* is a syntactic, as well as a semantic, domain, when an environment function is applied to a name it is written  $\rho[[\xi]]$  rather than  $\rho(\xi)$ , to highlight this point.

### 7.2.2 Check status

The purpose of a static semantics is to check whether a construct is okay or wrong

$$CHECK ::= checkOK \mid checkWrong$$

Although *CHECK* has only two values, *Z* has no boolean type, so *CHECK* cannot be declared as a boolean flag. Even if *Z* did have such a type, it would be better specification style to make *CHECK* a free type definition as above. If later it were decided to modify the specification by adding an extra check status, for example, *checkWarn*, it would be relatively straightforward to extend the definition given above.

Check results need to be combined.  $\bowtie$  is an infix function that combines check results ‘pessimistically’; the combination of the check results is *checkOK* only if both the arguments are *checkOK*:

$$\left| \begin{array}{l} \_ \bowtie \_ : CHECK \times CHECK \longrightarrow CHECK \\ \hline \forall c_1, c_2 : CHECK \bullet \\ \quad c_1 \bowtie c_2 = \\ \quad \quad \text{if } c_1 = checkOK \wedge c_2 = checkOK \\ \quad \quad \text{then } checkOK \text{ else } checkWrong \end{array} \right.$$

### 7.2.3 Memory locations

Store locations are modelled as integers.

$$Locn == \mathbb{Z}$$

These numbers could be interpreted as representing memory addresses, for example. Negative locations have been allowed: these could be interpreted as ‘special’

addresses, for example, registers. The interpretation chosen does not affect Tosca’s semantics; it is important only when mapping to a particular low level language.

### 7.3 Declaration-before-use semantics

For this non-standard interpretation, the declaration environment is a mapping from the names that are referenced in the program to their check status (representing declared or not declared):

$$Env_D == NAME \mapsto CHECK$$

### 7.4 Type checking semantics

The *TYPE* definition given in the abstract syntax is augmented with a *typeWrong* component:

$$\begin{aligned} TYPE ::= & \dots \\ & | \textit{typeWrong} \end{aligned}$$

For this non-standard interpretation, the type environment describes the mapping from identifiers to their types:

$$Env_T == NAME \mapsto TYPE$$

### 7.5 Initialization-before-use semantics

A use value is either *checkWrong* (for a variable whose value is used before being initialized, or an expression that uses a *checkWrong* variable in a subexpression) or *checkOK* (for a variable that has been initialized before being used, or an expression that uses only *checkOK* variables in its subexpressions).

The *Store<sub>U</sub>* is the mapping from store locations to the current use state of the variable stored there:

$$Store_U == Locn \mapsto CHECK$$

#### 7.5.1 The initialization-before-use state

The *State<sub>U</sub>* has two components, the *Store<sub>U</sub>* and a tag noting the check status. This tag propagates through the definition and becomes the final check result:

$$State_U == Store_U \times CHECK$$

The generic Z functions *first* and *second* extract components of an ordered pair. The query functions *storeOf<sub>U</sub>* and *checkOf<sub>U</sub>* are more meaningful names for these, for use when extracting the store and check components of the state pair:

$$\begin{aligned} \text{storeOf}_U &== \text{first}[\text{Store}_U, \text{CHECK}] \\ \text{checkOf}_U &== \text{second}[\text{Store}_U, \text{CHECK}] \end{aligned}$$

$\boxplus_v$  updates the store component of a use state:

$$\left| \begin{array}{l} \_ \boxplus_v \_ : \text{State}_U \times \text{Store}_U \longrightarrow \text{State}_U \\ \hline \forall \varsigma_{v1}, \varsigma_{v2} : \text{Store}_U; c_1 : \text{CHECK} \bullet \\ \quad (\varsigma_{v1}, c_1) \boxplus_v \varsigma_{v2} = (\varsigma_{v1} \oplus \varsigma_{v2}, c_1) \end{array} \right.$$

*updateUse<sub>U</sub>* changes the use check component of the store to the worse of its current value and that of the input:

$$\left| \begin{array}{l} \text{updateUse}_U : \text{CHECK} \longrightarrow \text{State}_U \longrightarrow \text{State}_U \\ \hline \forall c_1, c_2 : \text{CHECK}; \varsigma_v : \text{Store}_U \bullet \\ \quad \text{updateUse}_U c_1(\varsigma_v, c_2) = (\varsigma_v, c_1 \boxtimes c_2) \end{array} \right.$$

### 7.5.2 The initialization-before-use environment

The use environment is a mapping from names (identifiers) to what they denote, locations:

$$\text{Env}_U == \text{NAME} \rightsquigarrow \text{Locn}$$

The mapping is an injective (one-to-one) function, which ensures that no two names map to the same location.

### 7.5.3 Worse use store

The function *worseStore* takes two use stores,  $\varsigma_{v1}$  and  $\varsigma_{v2}$ , and returns a use store that combines the worst properties of each.

Where the domains of the two use stores coincide, on  $\text{dom } \varsigma_{v1} \cap \text{dom } \varsigma_{v2}$ , *worseStore* is defined by the check status combining function,  $\boxtimes$ .

Where the domains do not overlap, on  $\text{dom } \varsigma_{v1} \triangleleft \varsigma_{v2}$  and  $\text{dom } \varsigma_{v2} \triangleleft \varsigma_{v1}$ , it means that one store has a location that maps to some check status, and the other does not have that location. If that unmatched location maps to *checkWrong*, the worse behaviour is to continue to map to *checkWrong*. If that unmatched location maps to *checkOK* (if, for example, a variable is initialized in one branch of a choice but not in the other), the worse behaviour is to remove the location from the domain

of the result; it is worse to have a variable uninitialized than set to something. In either case, this can be achieved by range restricting the non-overlapping parts of the stores to *checkWrong*:

$$\left| \begin{array}{l} \text{worseStore} : \text{Store}_U \times \text{Store}_U \longrightarrow \text{Store}_U \\ \hline \forall \varsigma_{v_1}, \varsigma_{v_2} : \text{Store}_U \bullet \\ \text{worseStore}(\varsigma_{v_1}, \varsigma_{v_2}) = \\ \quad \{ \lambda : \text{Locn} \mid \lambda \in \text{dom } \varsigma_{v_1} \cap \text{dom } \varsigma_{v_2} \bullet \lambda \mapsto (\varsigma_{v_1} \lambda \boxtimes \varsigma_{v_2} \lambda) \} \\ \quad \cup \text{dom } \varsigma_{v_2} \triangleleft \varsigma_{v_1} \triangleright \{ \text{checkWrong} \} \\ \quad \cup \text{dom } \varsigma_{v_1} \triangleleft \varsigma_{v_2} \triangleright \{ \text{checkWrong} \} \end{array} \right.$$

$\triangleright$  is  $Z$ 's range restriction: the relation  $S \triangleright r$  is that subset of the relation  $S$  that has its range restricted to the elements in  $r$ .  $\triangleleft$  is  $Z$ 's domain anti-restriction: the relation  $d \triangleleft S$  is that subset of the relation  $S$  that has its domain restricted to the elements not in  $d$ . For example,

$$\begin{aligned} \{a \mapsto 1, b \mapsto 2, c \mapsto 3\} \triangleright \{1, 2\} &= \{a \mapsto 1, b \mapsto 2\} \\ \{a\} \triangleleft \{a \mapsto 1, b \mapsto 2, c \mapsto 3\} &= \{b \mapsto 2, c \mapsto 3\} \\ \{a\} \triangleleft \{a \mapsto 1, b \mapsto 2, c \mapsto 3\} \triangleright \{1, 2\} &= \{b \mapsto 2\} \end{aligned}$$

The definition of *worseStore* is one of the more complicated-looking definitions, so, to see how it works, consider two stores:

$$\begin{aligned} \varsigma_{v_1} &= \{ \lambda_1 \mapsto \text{checkOK}, \lambda_2 \mapsto \text{checkWrong}, \lambda_3 \mapsto \text{checkOK} \} \\ \varsigma_{v_2} &= \{ \lambda_2 \mapsto \text{checkOK}, \lambda_3 \mapsto \text{checkOK}, \lambda_4 \mapsto \text{checkWrong} \} \end{aligned}$$

The overlapping domain is  $\text{dom } \varsigma_{v_1} \cap \text{dom } \varsigma_{v_2} = \{\lambda_2, \lambda_3\}$ .  $\lambda_2$  maps to *checkWrong* in one store, *checkOK* in the other, and so to *checkWrong* in the composed store.  $\lambda_3$  will continue mapping to *checkOK*.

$\varsigma_{v_1}$  has one location not in  $\varsigma_{v_2}$ ,  $\lambda_1$ . This maps to *checkOK*, so will not appear in the combined store:

$$\begin{aligned} \text{dom } \varsigma_{v_2} \triangleleft \varsigma_{v_1} &= \{ \lambda_1 \mapsto \text{checkOK} \} \\ \text{dom } \varsigma_{v_2} \triangleleft \varsigma_{v_1} \triangleright \{ \text{checkWrong} \} &= \emptyset \end{aligned}$$

$\varsigma_{v_2}$  has an unmatched location,  $\lambda_4$ . It maps to *checkWrong*, which carries over:

$$\begin{aligned} \text{dom } \varsigma_{v_1} \triangleleft \varsigma_{v_2} &= \{ \lambda_4 \mapsto \text{checkWrong} \} \\ \text{dom } \varsigma_{v_1} \triangleleft \varsigma_{v_2} \triangleright \{ \text{checkWrong} \} &= \{ \lambda_4 \mapsto \text{checkWrong} \} \end{aligned}$$

So the worse store that results from combining these is

$$\begin{aligned} \text{worseStore}(\varsigma_{v_1}, \varsigma_{v_2}) &= \{ \lambda_2 \mapsto \text{checkWrong}, \lambda_3 \mapsto \text{checkOK} \} \cup \emptyset \cup \{ \lambda_4 \mapsto \text{checkWrong} \} \\ &= \{ \lambda_2 \mapsto \text{checkWrong}, \lambda_3 \mapsto \text{checkOK}, \lambda_4 \mapsto \text{checkWrong} \} \end{aligned}$$

### 7.5.4 Worse use state

The function *worseState* takes two use states and returns a use state that combines the *worst* properties of each:

$$\frac{\text{worseState} : \text{State}_U \times \text{State}_U \rightarrow \text{State}_U}{\forall \varsigma_{v1}, \varsigma_{v2} : \text{Store}_U; c_1, c_2 : \text{CHECK} \bullet \\ \text{worseState}((\varsigma_{v1}, c_1), (\varsigma_{v2}, c_2)) = (\text{worseStore}(\varsigma_{v1}, \varsigma_{v2}), c_1 \boxtimes c_2)}$$

## 7.6 Dynamic semantics

The *Store* is modelled as a mapping from store locations to values:

$$\text{Store} == \text{Locn} \leftrightarrow \text{VALUE}$$

### 7.6.1 Input and output

Input and output are modelled as lists of integers:

$$\begin{aligned} \text{Input} &== \text{seq Integer} \\ \text{Output} &== \text{seq Integer} \end{aligned}$$

#### 7.6.1.1 The dynamic state

The state of a computation has three components, the store mapping, the input, and the output:

$$\text{State} == \text{Store} \times \text{Input} \times \text{Output}$$

Z has no generic functions analogous to its *first* and *second* to extract components of an ordered triple. The query functions *storeOf* and *outOf* need to be defined to extract the store and output sequence components of the state tuple:

$$\frac{\begin{array}{l} \text{storeOf} : \text{State} \rightarrow \text{Store} \\ \text{outOf} : \text{State} \rightarrow \text{Output} \end{array}}{\forall \varsigma : \text{Store}; in : \text{Input}; out : \text{Output} \bullet \\ \text{storeOf}(\varsigma, in, out) = \varsigma \\ \wedge \text{outOf}(\varsigma, in, out) = out}$$

$\boxplus$  updates the store component of a dynamic state:

$$\frac{\_ \boxplus \_ : \text{State} \times \text{Store} \rightarrow \text{State}}{\forall \varsigma_1, \varsigma_2 : \text{Store}; in : \text{Input}; out : \text{Output} \bullet \\ (\varsigma_1, in, out) \boxplus \varsigma_2 = (\varsigma_1 \oplus \varsigma_2, in, out)}$$

### 7.6.1.2 The dynamic environment

The environment is a mapping from names (identifiers) to what they denote, locations:

$$Env == NAME \mapsto Locn$$

The mapping is an injective (one-to-one) function, which ensures that no two names map to the same location.

## 7.7 Aside—using generic definitions

Tosca's syntax includes sequences of declarations and sequences of commands. The meaning of a sequence of such constructs is related to the meaning of a single construct in a uniform manner; all the specifications look boringly similar (see, for example, section 8.2.2).

Thus, it might be thought appropriate to define a generic  $Z$  function that maps a meaning function for a single construct to the corresponding meaning function for a sequence of constructs, and instantiate this generic with various actual types when used. But one of the motivations for specifying and building a high integrity compiler in the manner being described here is that the translation from the formal  $Z$  specification to the Prolog implementation is as clear as possible; the use of some sorts of generic function can obscure this translation. So the Tosca specification has the definition of each separate function written out fully. To illustrate this point further, a generic form for sequences of declarations is given below, for comparison with the explicit forms given in section 8.2.2.

The meaning of a declaration in all four of Tosca's semantics is an appropriate environment change (expressed as a mapping from environment to environment). The meaning of a sequence of declarations is also an environment change. An empty sequence has no effect on the environment, and hence its meaning is the identity function. A sequence consisting of a single declaration has the same meaning as that declaration. The meaning of a longer sequence can be split up as the meaning of shorter subsequences, joined together by functional composition. So an appropriate definition, generic in the environment variable, that maps the meaning function for a single declaration to one for a sequence of declarations, is

$[E]$ <hr style="border: none; border-top: 3px double #000; margin-bottom: 5px;"/> $SEQ : (DECL \mapsto E \mapsto E) \longrightarrow (seq\ DECL \mapsto E \mapsto E)$ <hr style="border: none; border-top: 1px solid #000; margin-bottom: 5px;"/> $\forall \delta : DECL; \Delta_1, \Delta_2 : seq_1\ DECL; \mathcal{F} : DECL \mapsto E \mapsto E \bullet$ $(SEQ\ \mathcal{F})\langle \rangle = id\ E$ $\wedge (SEQ\ \mathcal{F})\langle \delta \rangle = \mathcal{F}\ \delta$ $\wedge (SEQ\ \mathcal{F})\langle \Delta_1 \wedge \Delta_2 \rangle = (SEQ\ \mathcal{F})\Delta_1 \circ (SEQ\ \mathcal{F})\Delta_2$
---



Each of the four meaning functions for sequences of declarations could be concisely defined, by instantiating  $SEQ$  with the appropriate environment, and applying it to the appropriate meaning function for single declarations, thus

$$\mathcal{D}_D^* == SEQ[Env_D] \mathcal{D}_D$$

$$\mathcal{T}_D^* == SEQ[Env_T] \mathcal{T}_D$$

$$\mathcal{U}_D^* == SEQ[Env_U] \mathcal{U}_D$$

$$\mathcal{M}_D^* == SEQ[Env] \mathcal{M}_D$$

However, the link to the Prolog implementation would be less clear in this form. The style of this particular Z specification is guided by a balance between clarity of specification, ease of proof, and clarity of translation into Prolog. If the same specification were written for a different reason, it might well be written in a different style.

# Tosca—Semantics

## 8.1 Introduction

This chapter specifies the semantics of Tosca. There is a separate section for each syntactic category: declarations, operators, expressions, commands, and program. For each separate construct in each syntactic category, its abstract syntax is repeated as a reminder, then the four semantics are given in order: declaration-before-use, type-checking, initialization-before-use, and dynamic. The following chapter uses these definitions to calculate the meanings of the example ‘square’ program.

## 8.2 Declarations

### 8.2.1 Variable declarations

$$DECL ::= \text{declVar}\langle\langle NAME \times TYPE \rangle\rangle$$

#### 8.2.1.1 Variable declaration, declaration-before-use semantics

The meaning function  $\mathcal{D}_D[\ ]$  takes a declaration and name environment, and gives a new name environment, with the declaration added. (A similar function is defined for declaration lists.)

If a new variable is being declared, it is added to the name environment as okay. If a variable with the same name has already been declared (either with the same type, or a different type) it is flagged as wrong:

$$\frac{\mathcal{D}_D[-] : DECL \rightarrow Env_D \rightarrow Env_D}{\mathcal{D}_D[\text{declVar}(\xi, \tau)] \rho_\delta = \begin{array}{l} \text{if } \xi \in \text{dom } \rho_\delta \\ \text{then } \rho_\delta \oplus \{\xi \mapsto \text{checkWrong}\} \\ \text{else } \rho_\delta \oplus \{\xi \mapsto \text{checkOK}\} \end{array}}$$

### 8.2.1.2 Variable declaration, type-checking semantics

The meaning function  $\mathcal{T}_D[-]$  takes a declaration and type environment, and gives a new type environment, with the declaration added. A declared variable is added to the type environment with its type:

$$\frac{\mathcal{T}_D[-] : DECL \mapsto Env_T \rightarrow Env_T}{\mathcal{T}_D[\text{declVar}(\xi, \tau)] \rho_\tau = \rho_\tau \oplus \{\xi \mapsto \tau\}}$$

Note that  $\xi$  is not in the domain of  $\rho_\tau$ , because variables declared more than once are trapped by the declaration-before-use semantics. So the definition could equivalently be written as  $\rho_\tau \cup \{\xi \mapsto \tau\}$ . The use of the  $\oplus$  operator merely serves to highlight that fact that the final environment is also a function.

### 8.2.1.3 Variable declaration, initialization-before-use semantics

A declaration changes the environment by adding a new (name, location) pair. The declaration meaning function  $\mathcal{U}_D[-]$  maps a declaration to a function that describes the change in the environment caused by the declaration.

A declared variable is added to the environment by mapping it to a previously unallocated location:

$$\frac{\mathcal{U}_D[-] : DECL \mapsto Env_U \rightarrow Env_U}{\begin{array}{l} \exists \lambda : Locn \mid \lambda \notin \text{ran } \rho_v \bullet \\ \mathcal{U}_D[\text{declVar}(\xi, \tau)] \rho_v = \rho_v \oplus \{\xi \mapsto \lambda\} \end{array}}$$

### 8.2.1.4 Variable declaration, dynamic semantics

A declaration changes the environment by adding a new (name, location) pair. The declaration meaning function  $\mathcal{M}_D[-]$  maps a declaration to a function that describes the change in the environment caused by the declaration.

A declared variable is added to the environment by mapping its name to an unallocated memory location:

$$\frac{\mathcal{M}_D[-] : DECL \mapsto Env \rightarrow Env}{\begin{array}{l} \exists \lambda : Locn \mid \lambda \notin \text{ran } \rho \bullet \\ \mathcal{M}_D[\text{declVar}(\xi, \tau)] \rho = \rho \oplus \{\xi \mapsto \lambda\} \end{array}}$$

Since a declaration does not change the state,  $\lambda$  is not in the domain of *Store* yet. The variable has been declared, but as yet has no associated value.

The choice of the memory location  $\lambda$  is specified only loosely. This permits any suitable allocation strategy to be chosen on implementation, for example, allocation to a register. Later, it is chosen in such a way as to simplify the correctness proofs.

Notice that the type of the variable,  $\tau$ , is not needed here: it is used only in the static type semantics checks.

## 8.2.2 Multiple declarations

seq *DECL*

The definition of the meanings of multiple declarations is an inductive extension of the meaning of a single declaration. An empty declaration list has no effect on the environment. Checking a list with one element is done by checking that element. Checking a list of declarations composed of two sublists is done by checking the second sublist in the environment that results from checking the first sublist.

All the definitions below look very similar. See section 7.7 for a discussion of this point.

### 8.2.2.1 Multiple declarations, declaration-before-use semantics

$$\left| \begin{array}{l} \mathcal{D}_{D^*}[\_ ] : \text{seq } DECL \rightarrow Env_D \rightarrow Env_D \\ \hline \mathcal{D}_{D^*}[\langle \rangle] = \text{id } Env_D \\ \mathcal{D}_{D^*}[\langle \delta \rangle] = \mathcal{D}_D[\delta] \\ \mathcal{D}_{D^*}[\Delta_1 \wedge \Delta_2] = \mathcal{D}_{D^*}[\Delta_2] \circ \mathcal{D}_{D^*}[\Delta_1] \end{array} \right.$$

### 8.2.2.2 Multiple declarations, type-checking semantics

$$\left| \begin{array}{l} \mathcal{T}_{D^*}[\_ ] : \text{seq } DECL \leftrightarrow Env_T \rightarrow Env_T \\ \hline \mathcal{T}_{D^*}[\langle \rangle] = \text{id } Env_T \\ \mathcal{T}_{D^*}[\langle \delta \rangle] = \mathcal{T}_D[\delta] \\ \mathcal{T}_{D^*}[\Delta_1 \wedge \Delta_2] = \mathcal{T}_{D^*}[\Delta_2] \circ \mathcal{T}_{D^*}[\Delta_1] \end{array} \right.$$

### 8.2.2.3 Multiple declarations, initialization-before-use semantics

$$\left| \begin{array}{l} \mathcal{U}_{D^*}[\_ ] : \text{seq } DECL \leftrightarrow Env_U \rightarrow Env_U \\ \hline \mathcal{U}_{D^*}[\langle \rangle] = \text{id } Env_U \\ \mathcal{U}_{D^*}[\langle \delta \rangle] = \mathcal{U}_D[\delta] \\ \mathcal{U}_{D^*}[\Delta_1 \wedge \Delta_2] = \mathcal{U}_{D^*}[\Delta_2] \circ \mathcal{U}_{D^*}[\Delta_1] \end{array} \right.$$

#### 8.2.2.4 Multiple declarations, dynamic semantics

$$\left| \begin{array}{l} \mathcal{M}_{D^*}[-] : \text{seq } DECL \mapsto Env \rightarrow Env \\ \hline \mathcal{M}_{D^*}[\langle \rangle] = \text{id } Env \\ \mathcal{M}_{D^*}[\langle \delta \rangle] = \mathcal{M}_D[\delta] \\ \mathcal{M}_{D^*}[\Delta_1 \wedge \Delta_2] = \mathcal{M}_{D^*}[\Delta_2] \circ \mathcal{M}_{D^*}[\Delta_1] \end{array} \right.$$

### 8.3 Operators

$UNY\_ARITH\_OP ::= \text{negate}$

$UNY\_LOGIC\_OP ::= \text{not}$

$UNY\_OP ::= \text{unyArithOp}\langle\langle UNY\_ARITH\_OP \rangle\rangle$   
 $\quad | \text{unyLogicOp}\langle\langle UNY\_LOGIC\_OP \rangle\rangle$

$BIN\_ARITH\_OP ::= \text{plus} \mid \text{minus}$

$BIN\_COMP\_OP ::= \text{less} \mid \text{greater} \mid \text{equal}$

$BIN\_LOGIC\_OP ::= \text{or} \mid \text{and}$

$BIN\_OP ::= \text{binArithOp}\langle\langle BIN\_ARITH\_OP \rangle\rangle$   
 $\quad | \text{binCompOp}\langle\langle BIN\_COMP\_OP \rangle\rangle$   
 $\quad | \text{binLogicOp}\langle\langle BIN\_LOGIC\_OP \rangle\rangle$

#### 8.3.1 Operators, declaration-before-use semantics

No declaration-before-use semantics needs to be defined for operators. All the necessary checking is done in the expressions, because applying an operator to them cannot introduce a further error in this semantics.

#### 8.3.2 Operators, type-checking semantics

The meaning functions  $\mathcal{T}_U[\ ]$  and  $\mathcal{T}_B[\ ]$  map unary and binary operators to functions from the types of the arguments to the types of the results.

Arithmetic operators take **integer** arguments and return **integer** results; comparison operators take **integers** and return **booleans**; logical operators take **booleans** and return **booleans**:

$\mathcal{T}_U[-] : UNY\_OP \rightarrow TYPE \rightarrow TYPE$	$\mathcal{T}_U[\text{unyArithOp } \psi_\alpha]\tau =$ $\quad \text{if } \tau = \text{integer} \text{ then integer else } typeWrong$ $\mathcal{T}_U[\text{unyLogicOp } \psi_\lambda]\tau =$ $\quad \text{if } \tau = \text{boolean} \text{ then boolean else } typeWrong$
$\mathcal{T}_B[-] : BIN\_OP \rightarrow TYPE \times TYPE \rightarrow TYPE$	$\mathcal{T}_B[\text{binArithOp } \omega_\alpha](\tau_1, \tau_2) =$ $\quad \text{if } (\tau_1 = \text{integer} \wedge \tau_2 = \text{integer}) \text{ then integer else } typeWrong$ $\mathcal{T}_B[\text{binCompOp } \omega_\chi](\tau_1, \tau_2) =$ $\quad \text{if } (\tau_1 = \text{integer} \wedge \tau_2 = \text{integer}) \text{ then boolean else } typeWrong$ $\mathcal{T}_B[\text{binLogicOp } \omega_\lambda](\tau_1, \tau_2) =$ $\quad \text{if } (\tau_1 = \text{boolean} \wedge \tau_2 = \text{boolean}) \text{ then boolean else } typeWrong$

### 8.3.3 Operators, initialization-before-use semantics

No initialization-before-use semantics needs to be defined for operators. All the necessary checking is done in the expressions, because applying an operator to them cannot introduce a further error in this semantics.

### 8.3.4 Operators, dynamic semantics

An operator produces a new value from one or two other values. The dynamic meaning functions  $\mathcal{M}_U[-]$  and  $\mathcal{M}_B[-]$  take unary and binary operators and map them to functions between values.

Tosca operators are defined in terms of the corresponding mathematical operators:

$\mathcal{M}_U[-] : UNY\_OP \rightarrow VALUE \rightarrow VALUE$	$\forall n : Integer \bullet$ $\quad \mathcal{M}_U[\text{unyArithOp negate}](int_v n) = int_v(-n)$ $\forall b : Boolean \bullet$ $\quad \mathcal{M}_U[\text{unyLogicOp not}](bool_v b) = bool_v(\text{if } b = \top \text{ then } \text{F} \text{ else } \top)$
--	---

The functions  $int_v$  and  $bool_v$  are needed to map integers and booleans to *VALUES*. In  $\mathbb{Z}$ , logical operators can be applied only to predicates, not to values, so unfortunately it is not possible to write the meaning of `unyLogicOp` as  $bool_v(\neg b)$ :

$\mathcal{M}_B \llbracket - \rrbracket : BIN\_OP \rightarrow VALUE \times VALUE \rightarrow VALUE$ <p><math>\forall b_1, b_2 : Boolean \bullet</math></p> $\mathcal{M}_B \llbracket binLogicOp or \rrbracket (bool_v b_1, bool_v b_2) =$ $bool_v(\text{if } b_1 = \top \vee b_2 = \top \text{ then } \top \text{ else } \text{F})$ $\wedge \mathcal{M}_B \llbracket binLogicOp and \rrbracket (bool_v b_1, bool_v b_2) =$ $bool_v(\text{if } b_1 = b_2 = \top \text{ then } \top \text{ else } \text{F})$ <p><math>\forall n_1, n_2 : Integer \bullet</math></p> $\mathcal{M}_B \llbracket binArithOp plus \rrbracket (int_v n_1, int_v n_2) = int_v(n_1 + n_2)$ $\wedge \mathcal{M}_B \llbracket binArithOp minus \rrbracket (int_v n_1, int_v n_2) = int_v(n_1 - n_2)$ $\wedge \mathcal{M}_B \llbracket binCompOp less \rrbracket (int_v n_1, int_v n_2) =$ $bool_v(\text{if } n_1 < n_2 \text{ then } \top \text{ else } \text{F})$ $\wedge \mathcal{M}_B \llbracket binCompOp greater \rrbracket (int_v n_1, int_v n_2) =$ $bool_v(\text{if } n_1 > n_2 \text{ then } \top \text{ else } \text{F})$ $\wedge \mathcal{M}_B \llbracket binCompOp equal \rrbracket (int_v n_1, int_v n_2) =$ $bool_v(\text{if } n_1 = n_2 \text{ then } \top \text{ else } \text{F})$
---

Notice that the effect of arithmetic overflow is *undefined*. For example, if  $n_1 + n_2 > maxInt$ , then it is not in the domain of the function  $int_v$ , and so the result is not defined. Any implementation is permitted. For example, an interpreter might print an error message, or perform extended precision arithmetic; a compiler might generate code to halt the processor, or perform modulo arithmetic. The compiler correctness proofs apply only to the defined case, where overflow does not occur. Then, for each safety critical application written in Tosca (or any other language!), there needs to be a proof that its arithmetic never overflows.

## 8.4 Expressions

### 8.4.1 Meaning functions

The meaning functions for expressions are declared in this section, to show the types of the arguments and results. They are defined in the following sections, inductively over the structure of expressions.

#### 8.4.1.1 Expression meaning function, declaration-before-use semantics

An expression is checked in the current environment. The meaning function  $\mathcal{D}_E \llbracket \cdot \rrbracket$  maps an expression to its declaration status, in the context of the name environment:

$$| \mathcal{D}_E[-] : \text{EXPR} \rightarrow \text{Env}_D \rightarrow \text{CHECK}$$

#### 8.4.1.2 Expression meaning function, type-checking semantics

The meaning function  $\mathcal{T}_E[-]$  maps an expression to its type, in the context of the type environment:

$$| \mathcal{T}_E[-] : \text{EXPR} \mapsto \text{Env}_T \mapsto \text{TYPE}$$

#### 8.4.1.3 Expression meaning function, initialization-before-use semantics

The meaning function  $\mathcal{U}_E[-]$  takes an expression, a use environment and state, and gives a possibly modified state (which occurs if an uninitialized variable is used in the expression):

$$| \mathcal{U}_E[-] : \text{EXPR} \mapsto \text{Env}_U \mapsto \text{State}_U \mapsto \text{State}_U$$

#### 8.4.1.4 Expression meaning function, dynamic semantics

An expression produces a value that depends on the current state. The meaning function  $\mathcal{M}_E[-]$  maps an expression to the value it denotes in the context of an environment and state:

$$| \mathcal{M}_E[-] : \text{EXPR} \mapsto \text{Env} \mapsto \text{State} \mapsto \text{VALUE}$$

Finding the meaning of an expression does not change the state. Hence Tosca expressions have no side effects.

### 8.4.2 Constant

$$\text{EXPR} ::= \text{const}\langle\langle \text{VALUE} \rangle\rangle$$

#### 8.4.2.1 Constant, declaration-before-use semantics

An expression consisting of a constant is always okay:

$$| \mathcal{D}_E[\text{const } \chi] \rho_\delta = \text{checkOK}$$

#### 8.4.2.2 Constant, type-checking semantics

The type of an expression consisting of a constant is **boolean** for  $\top$  and  $\text{F}$ , and **integer** for a number:

$$| \mathcal{T}_E[\text{const } \chi] \rho_\tau = \text{if } \chi \in \text{ran } \text{bool}_v \text{ then boolean else integer}$$



### 8.4.2.3 Constant, initialization-before-use semantics

An expression consisting of a constant is *checkOK*, and leaves the state unchanged:

$$\left| \mathcal{U}_E[\text{const } \chi] \rho_v = \text{id } State_U \right.$$

### 8.4.2.4 Constant, dynamic semantics

The meaning of a constant is just the constant's actual mathematical value:

$$\left| \mathcal{M}_E[\text{const } \chi] \rho \sigma = \chi \right.$$

Note that the  $\chi$  inside the brackets represents the syntactic literal constant, and the one outside the brackets represents its mathematical value.

## 8.4.3 Named variable

$$EXPR ::= \text{var} \langle \langle NAME \rangle \rangle$$

### 8.4.3.1 Named variable, declaration-before-use semantics

An expression consisting of a variable's name is okay if the name has been declared:

$$\left| \mathcal{D}_E[\text{var } \xi] \rho_\delta = \text{if } \xi \in \text{dom } \rho_\delta \text{ then } checkOK \text{ else } checkWrong \right.$$

### 8.4.3.2 Named variable, type-checking semantics

The type of an expression consisting of a variable's name is given by the type environment function:

$$\left| \mathcal{T}_E[\text{var } \xi] \rho_\tau = \rho_\tau[\xi] \right.$$

### 8.4.3.3 Named variable, initialization-before-use semantics

The variable in the expression will either be uninitialized (so not in the domain of the store) or set to something (either *checkWrong* or *checkOK*).

If the variable is uninitialized, then the new store is modified to set the identifier to *checkWrong*, and the use value of the state becomes *checkWrong*.

If the variable is in the domain of the store, the store is not changed, and the state's use value is set to the worse of its previous value and that of the variable.

This is the only kind of *expression* that directly changes the store (the assignment command can also change the store):

$$\left| \begin{array}{l} \mathcal{U}_E[\text{var } \xi] \rho_v(\varsigma_v, use) = \\ \quad (\text{let } \lambda == \rho_v[\xi] \bullet \\ \quad \quad \text{if } \lambda \in \text{dom } \varsigma_v \\ \quad \quad \text{then } (\varsigma_v, use \boxtimes \varsigma_v \lambda) \\ \quad \quad \text{else } (\varsigma_v \oplus \{\lambda \mapsto checkWrong\}, checkWrong)) \end{array} \right.$$

#### 8.4.3.4 Named variable, dynamic semantics

The meaning of an expression consisting of a variable's name is the variable's current value. This is found by first using the environment's variable function to find the memory location, then using the state's store function to get the corresponding value:

$$\mid \mathcal{M}_E[\text{var } \xi] \rho \sigma = (\text{storeOf } \sigma)(\rho[\xi])$$

#### 8.4.4 Unary expression

$$EXPR ::= \text{unExpr} \langle\langle UNY\_OP \times EXPR \rangle\rangle$$

##### 8.4.4.1 Unary expression, declaration-before-use semantics

A unary expression declaration checks the same as its component expression:

$$\mid \mathcal{D}_E[\text{unExpr}(\psi, \epsilon)] = \mathcal{D}_E[\epsilon]$$

##### 8.4.4.2 Unary expression, type-checking semantics

The type of a unary expression is determined by the type of the component expression, and the definition of  $\mathcal{T}_U$  given above:

$$\mid \mathcal{T}_E[\text{unExpr}(\psi, \epsilon)] \rho_\tau = \mathcal{T}_U[\psi](\mathcal{T}_E[\epsilon] \rho_\tau)$$

##### 8.4.4.3 Unary expression, initialization-before-use semantics

A unary expression use checks the same as its component expression:

$$\mid \mathcal{U}_E[\text{unExpr}(\psi, \epsilon)] = \mathcal{U}_E[\epsilon]$$

##### 8.4.4.4 Unary expression, dynamic semantics

The meaning of a unary operator applied to a subexpression is the corresponding mathematical operator applied to the value that the subexpression denotes:

$$\mid \mathcal{M}_E[\text{unExpr}(\psi, \epsilon)] \rho \sigma = \mathcal{M}_U[\psi](\mathcal{M}_E[\epsilon] \rho \sigma)$$

#### 8.4.5 Binary expression

$$EXPR ::= \text{binExpr} \langle\langle EXPR \times BIN\_OP \times EXPR \rangle\rangle$$

#### 8.4.5.1 Binary expression, declaration-before-use semantics

A binary expression is *checkOK* if both the subexpressions are *checkOK*:

$$\left| \mathcal{D}_E[\text{binExpr}(\epsilon_1, \omega, \epsilon_2)] \rho_\delta = \mathcal{D}_E[\epsilon_1] \rho_\delta \bowtie \mathcal{D}_E[\epsilon_2] \rho_\delta \right.$$

#### 8.4.5.2 Binary expression, type-checking semantics

The type of a binary operator applied to two subexpressions is determined by the types of the subexpressions, and the definition of  $\mathcal{T}_B$  given above:

$$\left| \mathcal{T}_E[\text{binExpr}(\epsilon_1, \omega, \epsilon_2)] \rho_\tau = \mathcal{T}_B[\omega](\mathcal{T}_E[\epsilon_1] \rho_\tau, \mathcal{T}_E[\epsilon_2] \rho_\tau) \right.$$

#### 8.4.5.3 Binary expression, initialization-before-use semantics

The use of a binary operator applied to two expressions is *checkOK* only if both expressions are *checkOK*. Notice that the store can be changed if either subexpression uses an uninitialized variable:

$$\left| \mathcal{U}_E[\text{binExpr}(\epsilon_1, \omega, \epsilon_2)] \rho_v \sigma_v = \text{worseState}(\mathcal{U}_E[\epsilon_1] \rho_v \sigma_v, \mathcal{U}_E[\epsilon_2] \rho_v \sigma_v) \right.$$

#### 8.4.5.4 Binary expression, dynamic semantics

The value of the expression consisting of a binary operator applied to two subexpressions is the corresponding mathematical operator applied to the values of the subexpressions (remember there are no side effects, so evaluating the first subexpression changes neither the environment nor the store):

$$\left| \mathcal{M}_E[\text{binExpr}(\epsilon_1, \omega, \epsilon_2)] \rho \sigma = \mathcal{M}_B[\omega](\mathcal{M}_E[\epsilon_1] \rho \sigma, \mathcal{M}_E[\epsilon_2] \rho \sigma) \right.$$

## 8.5 Commands

### 8.5.1 Meaning functions

The meaning functions for commands are declared in this section, to show the types of the arguments and results. They are defined in the following sections, inductively over the structure of commands.

#### 8.5.1.1 Command meaning functions, declaration-before-use semantics

A command is checked in the current environment. The meaning function  $\mathcal{D}_C[\ ]$  maps a command to its check status, in the context of the name environment. Similarly for lists of commands:

$$\left| \begin{array}{l} \mathcal{D}_C[\ ] : \text{CMD} \rightarrow \text{Env}_D \rightarrow \text{CHECK} \\ \mathcal{D}_{C^*}[\ ] : \text{seq CMD} \rightarrow \text{Env}_D \rightarrow \text{CHECK} \end{array} \right.$$

### 8.5.1.2 Command meaning functions, type-checking semantics

The meaning function  $\mathcal{T}_C[\![-]\!]$  takes a command and type environment, and gives *checkOK* or *checkWrong*, depending on whether the command is well-typed or not. Similarly for lists of commands:

$$\left| \begin{array}{l} \mathcal{T}_C[\![-]\!] : CMD \rightarrow Env_T \rightarrow CHECK \\ \mathcal{T}_{C^*}[\![-]\!] : \text{seq } CMD \rightarrow Env_T \rightarrow CHECK \end{array} \right.$$

### 8.5.1.3 Command meaning functions, initialization-before-use semantics

The meaning function  $\mathcal{U}_C[\![-]\!]$  takes a command, use environment and store, and gives the new store that results from checking the command. Similarly for lists of commands:

$$\left| \begin{array}{l} \mathcal{U}_C[\![-]\!] : CMD \rightarrow Env_U \rightarrow State_U \rightarrow State_U \\ \mathcal{U}_{C^*}[\![-]\!] : \text{seq } CMD \rightarrow Env_U \rightarrow State_U \rightarrow State_U \end{array} \right.$$

### 8.5.1.4 Command meaning functions, dynamic semantics

A command causes a state change. The command meaning function  $\mathcal{M}_C[\![-]\!]$  maps a command to the relevant state transition function, in the context of an environment. Similarly for lists of commands:

$$\left| \begin{array}{l} \mathcal{M}_C[\![-]\!] : CMD \rightarrow Env \rightarrow State \rightarrow State \\ \mathcal{M}_{C^*}[\![-]\!] : \text{seq } CMD \rightarrow Env \rightarrow State \rightarrow State \end{array} \right.$$

## 8.5.2 Multiple commands

seq *CMD*

The definitions below look very similar. See section 7.7 for a discussion of this point.

### 8.5.2.1 Multiple commands, declaration-before-use semantics

An empty command list declaration checks okay. A command list with a single command checks the same as that command. Checking a list of commands composed of two sublists is done by checking each sublist, and taking the worse result:

$$\left| \begin{array}{l} \mathcal{D}_{C^*}[\![\langle \rangle]\!] \rho_\delta = \text{checkOK} \\ \mathcal{D}_{C^*}[\![\langle \gamma \rangle]\!] \rho_\delta = \mathcal{D}_C[\![\gamma]\!] \rho_\delta \\ \mathcal{D}_{C^*}[\![\Gamma_1 \wedge \Gamma_2]\!] \rho_\delta = (\mathcal{D}_{C^*}[\![\Gamma_1]\!] \rho_\delta) \bowtie (\mathcal{D}_{C^*}[\![\Gamma_2]\!] \rho_\delta) \end{array} \right.$$

### 8.5.2.2 Multiple commands, type-checking semantics

An empty command list type checks okay. A command list with a single command type checks the same as that command. Type checking a list of commands composed of two sublists is done by checking each sublist, and taking the worse result:

$$\left| \begin{array}{l} \mathcal{T}_{C^*}[\langle \rangle] \rho_\tau = \text{checkOK} \\ \mathcal{T}_{C^*}[\langle \gamma \rangle] \rho_\tau = \mathcal{T}_C[\gamma] \rho_\tau \\ \mathcal{T}_{C^*}[\Gamma_1 \hat{\wedge} \Gamma_2] \rho_\tau = \mathcal{T}_{C^*}[\Gamma_1] \rho_\tau \bowtie \mathcal{T}_{C^*}[\Gamma_2] \rho_\tau \end{array} \right.$$

### 8.5.2.3 Multiple commands, initialization-before-use semantics

An empty command list has no effect on the use state. A command list with a single command type checks the same as that command. Use checking a list of commands composed of two sublists is done by checking the second sublist in the use state that results from checking the first sublist:

$$\left| \begin{array}{l} \mathcal{U}_{C^*}[\langle \rangle] \rho_v = \text{id State}_U \\ \mathcal{U}_{C^*}[\langle \gamma \rangle] \rho_v = \mathcal{U}_C[\gamma] \rho_v \\ \mathcal{U}_{C^*}[\Gamma_1 \hat{\wedge} \Gamma_2] \rho_v = (\mathcal{U}_{C^*}[\Gamma_2] \rho_v) \circ (\mathcal{U}_{C^*}[\Gamma_1] \rho_v) \end{array} \right.$$

### 8.5.2.4 Multiple commands, dynamic semantics

An empty command list has no effect on the state. A command list with a single command has the same effect as that command. Evaluating a list of commands composed of two sublists is done by evaluating the second sublist in the state that results from evaluating the first sublist:

$$\left| \begin{array}{l} \mathcal{M}_{C^*}[\langle \rangle] \rho = \text{id State} \\ \mathcal{M}_{C^*}[\langle \gamma \rangle] \rho = \mathcal{M}_C[\gamma] \rho \\ \mathcal{M}_{C^*}[\Gamma_1 \hat{\wedge} \Gamma_2] \rho = (\mathcal{M}_{C^*}[\Gamma_2] \rho) \circ (\mathcal{M}_{C^*}[\Gamma_1] \rho) \end{array} \right.$$

## 8.5.3 Block

$$CMD ::= \text{block}\langle\langle \text{seq}_1 CMD \rangle\rangle$$

### 8.5.3.1 Block, declaration-before-use semantics

A block's declaration check status is the result of checking the sequence of body commands:

$$\left| \mathcal{D}_C[\text{block } \Gamma] \rho_\delta = \mathcal{D}_{C^*}[\Gamma] \rho_\delta \right.$$

**8.5.3.2 Block, type-checking semantics**

A block's type check status is the result of type checking the sequence of body commands:

$$\mid \mathcal{T}_C[\text{block } \Gamma] \rho_\tau = \mathcal{T}_{C^*}[\Gamma] \rho_\tau$$

**8.5.3.3 Block, initialization-before-use semantics**

A block's use check status is the result of use checking the sequence of body commands:

$$\mid \mathcal{U}_C[\text{block } \Gamma] \rho_v = \mathcal{U}_{C^*}[\Gamma] \rho_v$$

**8.5.3.4 Block, dynamic semantics**

A block's dynamic meaning is the meaning of the sequence of body commands:

$$\mid \mathcal{M}_C[\text{block } \Gamma] \rho = \mathcal{M}_{C^*}[\Gamma] \rho$$

**8.5.4 Skip**

*CMD* ::= skip

**8.5.4.1 Skip, declaration-before-use semantics**

The skip statement always checks okay:

$$\mid \mathcal{D}_C[\text{skip}] \rho_\delta = \text{checkOK}$$

**8.5.4.2 Skip, type-checking semantics**

The skip statement always type checks *checkOK*:

$$\mid \mathcal{T}_C[\text{skip}] \rho_\tau = \text{checkOK}$$

**8.5.4.3 Skip, initialization-before-use semantics**

The skip statement does nothing; it leaves the store unchanged:

$$\mid \mathcal{U}_C[\text{skip}] \rho_v = \text{id } \text{State}_V$$

**8.5.4.4 Skip, dynamic semantics**

The skip statement does nothing; it leaves the state unchanged:

$$\mid \mathcal{M}_C[\text{skip}] \rho = \text{id } \text{State}$$

### 8.5.5 Assignment

$CMD ::= \text{assign}\langle\langle NAME \times EXPR \rangle\rangle$

#### 8.5.5.1 Assignment, declaration-before-use semantics

Assignment checks *checkOK* if the target variable has been declared and if the source expression checks okay:

$$\left| \begin{array}{l} \mathcal{D}_C[\text{assign}(\xi, \epsilon)] \rho_\delta = \\ \quad (\text{if } \xi \in \text{dom } \rho_\delta \text{ then } \text{checkOK} \text{ else } \text{checkWrong}) \\ \quad \bowtie \mathcal{D}_E[\epsilon] \rho_\delta \end{array} \right.$$

#### 8.5.5.2 Assignment, type-checking semantics

Assignment type checks *checkOK* if the target variable and the source expression have the same type, and that type is not wrong:

$$\left| \begin{array}{l} \mathcal{T}_C[\text{assign}(\xi, \epsilon)] \rho_\tau = \\ \quad \text{if } \rho_\tau[\xi] = \mathcal{T}_E[\epsilon] \rho_\tau \neq \text{typeWrong} \text{ then } \text{checkOK} \text{ else } \text{checkWrong} \end{array} \right.$$

Notice that it would not be correct simply to check that the target and source have the same types, in case they were both *typeWrong*.

#### 8.5.5.3 Assignment, initialization-before-use semantics

The final use store depends on the use state of the variable in the store resulting from evaluating the expression (using this intermediate store can catch usage like  $\mathbf{x} := (\mathbf{x}+1)$ , where  $\mathbf{x}$  has not previously been initialized).

If the variable has not yet been used (either properly or improperly, possibly in the expression), its use becomes *checkOK*, otherwise its use is left unchanged. Notice this is the case whether the expression is *checkOK* or *checkWrong*—this semantics does not worry if a variable has been set to a *checkWrong* expression, it just notes what variables are used before they are set to anything at all. In the above example,  $\mathbf{x}$  is set to *checkWrong* on evaluating the expression, and hence remains wrong on evaluating the assignment:

$$\left| \begin{array}{l} \mathcal{U}_C[\text{assign}(\xi, \epsilon)] \rho_v \sigma_v = \\ \quad (\text{let } \lambda == \rho_v[\xi]; \sigma_{v1} == \mathcal{U}_E[\epsilon] \rho_v \sigma_v \bullet \\ \quad \quad \text{if } \lambda \in \text{dom}(\text{storeOf}_U \sigma_v) \\ \quad \quad \text{then } \sigma_{v1} \\ \quad \quad \text{else } \sigma_{v1} \boxplus_v \{ \lambda \mapsto \text{checkOK} \} ) \end{array} \right.$$

#### 8.5.5.4 Assignment, dynamic semantics

Assignment updates the store. It modifies the value held in the target variable's store location to that of the value of the source expression:

$$\left| \mathcal{M}_C[\text{assign}(\xi, \epsilon)] \rho \sigma = \sigma \boxplus \{\rho[\xi] \mapsto \mathcal{M}_E[\epsilon] \rho \sigma\}$$

#### 8.5.6 Choice

$$CMD ::= \text{choice}(\langle \langle \text{EXPR} \times \text{CMD} \times \text{CMD} \rangle \rangle)$$

##### 8.5.6.1 Choice, declaration-before-use semantics

The choice command checks *checkOK* only if the expression and both subcommands check *checkOK*:

$$\left| \mathcal{D}_C[\text{choice}(\epsilon, \gamma_1, \gamma_2)] \rho_\delta = \mathcal{D}_E[\epsilon] \rho_\delta \bowtie \mathcal{D}_C[\gamma_1] \rho_\delta \bowtie \mathcal{D}_C[\gamma_2] \rho_\delta$$

##### 8.5.6.2 Choice, type-checking semantics

The choice command type checks *checkOK* only if the expression has type *boolean* and both subcommands type check *checkOK*:

$$\left| \begin{array}{l} \mathcal{T}_C[\text{choice}(\epsilon, \gamma_1, \gamma_2)] \rho_\tau = \\ \quad (\text{if } \mathcal{T}_E[\epsilon] \rho_\tau = \text{boolean} \text{ then } \text{checkOK} \text{ else } \text{checkWrong}) \\ \quad \bowtie \mathcal{T}_C[\gamma_1] \rho_\tau \\ \quad \bowtie \mathcal{T}_C[\gamma_2] \rho_\tau \end{array}$$

##### 8.5.6.3 Choice, initialization-before-use semantics

If an uninitialized variable is used in one branch of a choice, it should be mapped to *checkWrong*. But if a variable is initialized in one branch of a choice, it should not be *checkOK*; that branch might never be executed.

For example, if none of **x**, **y** and **z** have been previously initialized, then after the command

```

if expr then
  begin
    x := 0;
    z := 1;
  end
else
  begin
    x := 1;
    y := 1;
    z := (z+1);
  end

```



we would want  $\mathbf{x}$  to be set to *checkOK* and the others to be set to *checkWrong*,  $\mathbf{y}$  because it is initialized in one branch but not the other, and  $\mathbf{z}$  because it is initialized in one branch but set to something uninitialized in the other.

The new use state is determined by the worse of the two subcommands, evaluated in the possibly changed use state of the expression (which can occur if an uninitialized variable is used in the expression):

$$\left| \begin{array}{l} \mathcal{U}_C[\text{choice}(\epsilon, \gamma_1, \gamma_2)] \rho_v \sigma_v = \\ \quad (\text{let } \sigma_{v_1} == \mathcal{U}_E[\epsilon] \rho_v \sigma_v \bullet \\ \quad \text{worseState}(\mathcal{U}_C[\gamma_1] \rho_v \sigma_{v_1}, \mathcal{U}_C[\gamma_2] \rho_v \sigma_{v_1})) \end{array} \right.$$

Notice that the same environment is used in both branches of the choice.

#### 8.5.6.4 Choice, dynamic semantics

The meaning of the conditional choice command depends on the value of the test expression. If the value of the expression is  $\top$ , the *then* command is applied; if the value of the expression is  $\text{F}$ , the *else* command is applied:

$$\left| \begin{array}{l} \mathcal{M}_C[\text{choice}(\epsilon, \gamma_1, \gamma_2)] \rho \sigma = \\ \quad \text{if } \mathcal{M}_E[\epsilon] \rho \sigma = \text{bool}_v \top \text{ then } \mathcal{M}_C[\gamma_1] \rho \sigma \text{ else } \mathcal{M}_C[\gamma_2] \rho \sigma \end{array} \right.$$

#### 8.5.7 Loop

$$CMD ::= \text{loop}\langle\langle EXPR \times CMD \rangle\rangle$$

##### 8.5.7.1 Loop, declaration-before-use semantics

The loop command checks *checkOK* only if both the expression and the subcommand check *checkOK*:

$$\left| \mathcal{D}_C[\text{loop}(\epsilon, \gamma)] \rho_\delta = \mathcal{D}_E[\epsilon] \rho_\delta \bowtie \mathcal{D}_C[\gamma] \rho_\delta \right.$$

##### 8.5.7.2 Loop, type-checking semantics

The loop command type checks *checkOK* only if the expression has type *boolean* and the subcommand type checks *checkOK*:

$$\left| \begin{array}{l} \mathcal{T}_C[\text{loop}(\epsilon, \gamma)] \rho_\tau = \\ \quad (\text{if } \mathcal{T}_E[\epsilon] \rho_\tau = \text{boolean} \text{ then } \text{checkOK} \text{ else } \text{checkWrong}) \\ \quad \bowtie \mathcal{T}_C[\gamma] \rho_\tau \end{array} \right.$$

### 8.5.7.3 Loop, initialization-before-use semantics

If an uninitialized variable is used in a loop, it should be mapped to *checkWrong*. But if a variable is initialized in a loop, it should not be mapped to *checkOK*: the loop might never be executed. For example, in the program

```

x : int; y : int; z : int;
while expr do
  begin
    z := x;
    y := 1;
  end

```

the uninitialized **x** might be used, because the loop body might be executed, and so it should be mapped to *checkWrong*. But the initialization of **y** might not occur, because the loop body might not be executed, and so it should not be mapped to *checkOK*.

The new use state is determined by the worse of the original state and the one resulting from the body of the command, remembering that the expression (which is guaranteed to be evaluated at least once) might change the state:

$$\left| \begin{array}{l} \mathcal{U}_C[\llbracket \text{loop}(\epsilon, \gamma) \rrbracket] \rho_v \sigma_v = \\ \quad (\text{let } \sigma_{v1} == \mathcal{U}_E[\llbracket \epsilon \rrbracket] \rho_v \sigma_v \bullet \\ \quad \quad \text{worseState}(\sigma_{v1}, \mathcal{U}_C[\llbracket \gamma \rrbracket] \rho_v \sigma_{v1})) \end{array} \right.$$

### 8.5.7.4 Loop, dynamic semantics

The meaning of the while loop command depends on the value of the test expression. If the value of the expression is  $\top$ , the body command is applied, and then the whole while command is reapplied, to the now modified state. If the value of the expression is  $\text{F}$ , nothing is done:

$$\left| \begin{array}{l} \mathcal{M}_C[\llbracket \text{loop}(\epsilon, \gamma) \rrbracket] \rho \sigma = \\ \quad \text{if } \mathcal{M}_E[\llbracket \epsilon \rrbracket] \rho \sigma = \text{bool}_v \top \\ \quad \text{then } \mathcal{M}_C[\llbracket \text{loop}(\epsilon, \gamma) \rrbracket] \rho (\mathcal{M}_C[\llbracket \gamma \rrbracket] \rho \sigma) \\ \quad \text{else } \sigma \end{array} \right.$$

The definitions of the other Tosca constructs are recursive, but always in terms of their simpler components, and so the recursion terminates. The recursive definition of *loop*, however, is given in terms of itself, and so does not necessarily terminate. It has a solution only if the value of the test becomes false in some state that occurs during the evaluation, otherwise it corresponds to an ‘infinite loop’. See Appendix B for further discussion of this point.

### 8.5.8 Input

$CMD ::= \text{input}\langle\langle NAME \rangle\rangle$

#### 8.5.8.1 Input, declaration-before-use semantics

Input checks *checkOK* if the name has been declared:

$$\left| \begin{array}{l} \mathcal{D}_C[\text{input } \xi] \rho_\delta = \\ \quad \text{if } \xi \in \text{dom } \rho_\delta \text{ then } \text{checkOK} \text{ else } \text{checkWrong} \end{array} \right.$$

#### 8.5.8.2 Input, type-checking semantics

Input type checks *checkOK* if the identifier is an *integer* (input can only be integers in Tosca):

$$\left| \begin{array}{l} \mathcal{T}_C[\text{input } \xi] \rho_\tau = \\ \quad \text{if } \rho_\tau[\xi] = \text{integer} \text{ then } \text{checkOK} \text{ else } \text{checkWrong} \end{array} \right.$$

#### 8.5.8.3 Input, initialization-before-use semantics

The new use state on input depends on the use state of the variable. If it has not yet been used (either properly or improperly), its use becomes *checkOK*, otherwise its use is left unchanged:

$$\left| \begin{array}{l} \mathcal{U}_C[\text{input } \xi] \rho_v \sigma_v = \\ \quad (\text{let } \lambda == \rho_v[\xi] \bullet \\ \quad \quad \text{if } \lambda \in \text{dom}(\text{storeOf}_U \sigma_v) \\ \quad \quad \text{then } \sigma_v \\ \quad \quad \text{else } \sigma_v \boxplus_v \{ \lambda \mapsto \text{checkOK} \}) \end{array} \right.$$

#### 8.5.8.4 Input, dynamic semantics

Input removes an integer from the input list, and assigns it to the variable:

$$\left| \begin{array}{l} \mathcal{M}_C[\text{input } \xi] \rho (\varsigma, \langle v \rangle \frown in, out) = \\ \quad (\varsigma \oplus \{ \rho[\xi] \mapsto \text{int}_v v \}, in, out) \end{array} \right.$$

The function  $\text{int}_v$  maps the integer to a *VALUE*.

### 8.5.9 Output

$CMD ::= \text{output}\langle\langle EXPR \rangle\rangle$

**8.5.9.1 Output, declaration-before-use semantics**

Output type checks *checkOK* if the expression checks okay:

$$\left| \mathcal{D}_C[\text{output } \epsilon] \rho_\delta = \mathcal{D}_E[\epsilon] \rho_\delta \right.$$

**8.5.9.2 Output, type-checking semantics**

Output type checks *checkOK* if the expression has type *integer*:

$$\left| \begin{array}{l} \mathcal{T}_C[\text{output } \epsilon] \rho_\tau = \\ \text{if } \mathcal{T}_E[\epsilon] \rho_\tau = \text{integer} \text{ then } \text{checkOK} \text{ else } \text{checkWrong} \end{array} \right.$$

**8.5.9.3 Output, initialization-before-use semantics**

The new use store produced by output depends on that of the expression:

$$\left| \mathcal{U}_C[\text{output } \epsilon] = \mathcal{U}_E[\epsilon] \right.$$

**8.5.9.4 Output, dynamic semantics**

Output appends the value of the expression to the output list:

$$\left| \begin{array}{l} \mathcal{M}_C[\text{output } \epsilon] \rho(\varsigma, in, out) = \\ (\varsigma, in, out \hat{\sim} \langle int_v \sim (\mathcal{M}_E[\epsilon] \rho(\varsigma, in, out)) \rangle) \end{array} \right.$$

The function  $int_v \sim$  maps the *VALUE* of the expression to an integer.

**8.6 Program**

$$PROG ::= \text{Tosca} \langle \langle \text{seq } DECL \times CMD \rangle \rangle$$

**8.6.1 Program, declaration-before-use semantics**

The declaration meaning of a program maps it to a declaration check. A program's check status is the result of checking the body command in the environment of the declarations:

$$\left| \begin{array}{l} \mathcal{D}_P[-] : PROG \rightarrow CHECK \\ \hline \mathcal{D}_P[\text{Tosca}(\Delta, \gamma)] = \mathcal{D}_C[\gamma](\mathcal{D}_{D^*}[\Delta] \emptyset) \end{array} \right.$$

A program checks okay if its check value is *checkOK*. So the condition for the rest of the semantics to be defined is

$$\mathcal{D}_P[\text{Tosca}(\Delta, \gamma)] = \text{checkOK}$$

### 8.6.2 Program, type-checking semantics

The type meaning of a program maps it to a type check. The command is checked in the environment of the declarations:

$$\left| \begin{array}{l} \mathcal{T}_P[-] : \text{PROG} \leftrightarrow \text{CHECK} \\ \hline \mathcal{D}_P[\text{Tosca}(\Delta, \gamma)] = \text{checkOK} \Rightarrow \\ \mathcal{T}_P[\text{Tosca}(\Delta, \gamma)] = \mathcal{T}_C[\gamma](\mathcal{T}_{D^*}[\Delta]\emptyset) \end{array} \right.$$

A program type checks okay if its type check value is *checkOK*. So the condition for the initialization before use semantics to be defined is

$$\mathcal{T}_P[\text{Tosca}(\Delta, \gamma)] = \text{checkOK}$$

### 8.6.3 Program, initialization-before-use semantics

The use meaning of a program maps it to a use check. Use checking a program means checking the command in the environment of the declarations:

$$\left| \begin{array}{l} \mathcal{U}_P[-] : \text{PROG} \leftrightarrow \text{CHECK} \\ \hline \mathcal{D}_P[\text{Tosca}(\Delta, \gamma)] = \text{checkOK} \\ \wedge \mathcal{T}_P[\text{Tosca}(\Delta, \gamma)] = \text{checkOK} \Rightarrow \\ \mathcal{U}_P[\text{Tosca}(\Delta, \gamma)] = \text{checkOf}_U(\mathcal{U}_C[\gamma](\mathcal{U}_{D^*}[\Delta]\emptyset)(\emptyset, \text{checkOK})) \end{array} \right.$$

A program use checks okay if its value is *checkOK*. So the condition for the dynamic semantics to be defined is

$$\mathcal{U}_P[\text{Tosca}(\Delta, \gamma)] = \text{checkOK}$$

### 8.6.4 Program, dynamic semantics

A program maps its input to its output. The meaning of a program is simply this mapping. Its body command is executed in the environment and state of the declarations:

$$\left| \begin{array}{l} \mathcal{M}_P[-] : \text{PROG} \leftrightarrow \text{Input} \leftrightarrow \text{Output} \\ \hline \mathcal{D}_P[\text{Tosca}(\Delta, \gamma)] = \text{checkOK} \\ \wedge \mathcal{T}_P[\text{Tosca}(\Delta, \gamma)] = \text{checkOK} \\ \wedge \mathcal{U}_P[\text{Tosca}(\Delta, \gamma)] = \text{checkOK} \Rightarrow \\ \mathcal{M}_P[\text{Tosca}(\Delta, \gamma)]i = \\ \text{outOf}(\mathcal{M}_C[\gamma](\mathcal{M}_{D^*}[\Delta]\emptyset)(\emptyset, i, \langle \rangle)) \end{array} \right.$$

Notice that, even if a program passes all its static checks, it may still have errors. It may fail to terminate because of a non-terminating loop, or may fail because of insufficient input.

## 8.7 Freedom in the definitions

The more programming errors that can be discovered statically, the better. Each of these checks could be specified as a separate static semantics, or added as an extra condition in an existing semantics. There is a great deal of freedom in how many of these various semantics are specified, and in how the checks are distributed between the semantics. Some conditions might have been placed in different semantics, or dropped altogether; other conditions might have been added. Clarity of specification and separation of concerns should be a goal: not only should this make the specification easier to understand, but it should make the effect of the restrictions easier to predict.

A couple of places where the definitions might have been different are discussed below.

### 8.7.1 Simpler checking

It might seem unnecessary to keep checking that variables are initialized before use once an error has been found. For example, the definition for commands could be modified to

$$\left| \begin{array}{l} \mathcal{U}_C \llbracket \gamma \rrbracket \rho_v(\varsigma_v, use) = \\ \quad \mathbf{if} \ use = \mathit{checkWrong} \\ \quad \mathbf{then} \ (\varsigma_v, use) \\ \quad \mathbf{else} \ \text{as before} \dots \end{array} \right.$$

The meaning of an expression might similarly be simplified not to update the store, but merely to propagate a use result.

This conceptually simpler definition results in a check result that is *checkWrong* or *checkOK*, but it gives no indication of *which* variables are being improperly used. The more complicated definition actually used results in a final state that includes information about *all* the improperly used variables. There is a trade-off between complexity of the static semantics definitions and the amount of useful information available in the final state.

### 8.7.2 Declared without use

Should a variable that is declared but never used be flagged? The current definition of the various static semantics does not define it to be an error. However, for a safety critical language, it might be wise to take a stricter view, and define a static semantics to check for this case. If it were thought to be important to know if variables were not used, but that such a case should not be counted as an error, then a new check status such as *checkWarn* could be introduced.

# Calculating the Meanings of Programs

## 9.1 Incorrect programs

In order to understand how the various static semantics can be used as checks, let's calculate the static meanings of various short programs that have errors in them.

### 9.1.1 A variable not declared

Consider the complete Tosca program

```
x := true
```

Notice that **x** has not been declared. The abstract syntax form is

$$declErr == \text{Tosca}(\langle \rangle, \text{assign}(x, \text{const}(bool_v \top)))$$

and the declaration-before-use meaning is

$$\begin{aligned}
 \mathcal{D}_P[declErr] &= \mathcal{D}_P[\text{Tosca}(\langle \rangle, \text{assign}(x, \text{const}(bool_v \top)))] \\
 &= \mathcal{D}_C[\text{assign}(x, \text{const}(bool_v \top))](\mathcal{D}_{D^*}[\langle \rangle] \emptyset) \\
 &= \mathcal{D}_C[\text{assign}(x, \text{const}(bool_v \top))](\text{id } Env_D \emptyset) \\
 &= \mathcal{D}_C[\text{assign}(x, \text{const}(bool_v \top))] \emptyset \\
 &= checkWrong \bowtie \mathcal{D}_E[\text{const}(bool_v \top)] \emptyset \\
 &= checkWrong
 \end{aligned}$$

□

### 9.1.2 A type error

Consider the complete Tosca program

```
int x ;
x := true
```

Now  $x$  has been declared, but is assigned to an expression of the wrong type. The abstract syntax form is

$$typeErr == \text{Tosca}(\langle \text{declVar}(x, \text{integer}) \rangle, \text{assign}(x, \text{const}(\text{bool}_v \top)))$$

The declaration-before-use meaning is *checkOK*, and the type checking meaning is

$$\begin{aligned} \mathcal{T}_P[\text{typeErr}] &= \mathcal{T}_P[\text{Tosca}(\langle \text{declVar}(x, \text{integer}) \rangle, \text{assign}(x, \text{const}(\text{bool}_v \top)))] \\ &= \mathcal{T}_C[\text{assign}(x, \text{const}(\text{bool}_v \top))](\mathcal{T}_D^*[\langle \text{declVar}(x, \text{integer}) \rangle](\langle \rangle)) \\ &= \mathcal{T}_C[\text{assign}(x, \text{const}(\text{bool}_v \top))]\{x \mapsto \text{integer}\} \\ &= \text{if } \text{integer} = \mathcal{T}_E[\text{const}(\text{bool}_v \top)]\{x \mapsto \text{integer}\} \\ &\quad \text{then } \text{checkOK} \\ &\quad \text{else } \text{checkWrong} \\ &= \text{if } \text{integer} = \text{boolean} \text{ then } \text{checkOK} \text{ else } \text{checkWrong} \\ &= \text{checkWrong} \end{aligned}$$

□

### 9.1.3 Use without initialization

Consider the complete Tosca program

```
int x ;
output x
```

$x$  has been declared, and is of the right type to be used in an **output**, but has not been assigned a value before it is used. The abstract syntax form is

$$useErr == \text{Tosca}(\langle \text{declVar}(x, \text{integer}) \rangle, \text{output}(\text{var } x))$$

The declaration-before-use meaning, and type checking meaning, are both *checkOK*, and the use checking meaning is

$$\begin{aligned} \mathcal{U}_P[\text{useErr}] &= \mathcal{U}_P[\text{Tosca}(\langle \text{declVar}(x, \text{integer}) \rangle, \text{output}(\text{var } x))] \\ &= \text{checkOfU}(\mathcal{U}_C[\text{output}(\text{var } x)]) \\ &\quad (\mathcal{U}_D^*[\langle \text{declVar}(x, \text{integer}) \rangle](\emptyset)(\emptyset, \text{checkOK})) \end{aligned}$$



$$\begin{aligned}
&= \text{checkOf}_U(\mathcal{U}_C[\text{output}(\text{var } x)]\{x \mapsto \lambda_1\}(\emptyset, \text{checkOK})) \\
&= \text{checkOf}_U(\mathcal{U}_E[\text{var } x]\{x \mapsto \lambda_1\}(\emptyset, \text{checkOK})) \\
&= \text{checkOf}_U(\{\lambda_1 \mapsto \text{checkWrong}\}, \text{checkWrong}) \\
&= \text{checkWrong}
\end{aligned}$$

□

## 9.2 The ‘square’ program

Now let’s consider a bigger, and correct, example—the ‘square’ program introduced in Chapter 5.

### 9.2.1 Some convenient abbreviations

Before we start calculating the various static and dynamic meanings of the ‘square’ program, it is convenient to define some abbreviations.

Let  $\Delta_{all}$  be the list of all the declarations:

$$\Delta_{all} == \langle \text{declVar}(n, \text{integer}), \text{declVar}(sq, \text{integer}), \text{declVar}(\text{limit}, \text{integer}) \rangle$$

Let  $\gamma_{loop}$  be the body of the while loop:

$$\begin{aligned}
\gamma_{loop} == & \\
& \text{block} \langle \text{assign}(sq, \\
& \quad \text{binExpr}(\text{binExpr}(\text{var } sq, \text{plus}, \text{const}(\text{int}_v 1)), \\
& \quad \quad \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n)), \\
& \quad \text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const}(\text{int}_v 1))), \\
& \quad \text{output}(\text{var } sq) \rangle
\end{aligned}$$

Let  $\gamma_{body}$  be the body of the block:

$$\begin{aligned}
\gamma_{body} == & \\
& \text{block} \langle \text{assign}(n, \text{const}(\text{int}_v 1)), \text{assign}(sq, \text{const}(\text{int}_v 1)), \\
& \quad \text{input } \text{limit}, \text{output}(\text{var } sq), \\
& \quad \text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } \text{limit}), \gamma_{loop}) \rangle
\end{aligned}$$

So the example program is simply

$$\text{square} = \text{Tosca}(\Delta_{all}, \gamma_{body})$$

### 9.2.2 The ‘square’ program, declaration-before-use semantics

The declaration-before-use meaning of the square program is

$$\begin{aligned} & \mathcal{D}_P[\text{square}] \\ &= \mathcal{D}_P[\text{Tosca}(\Delta_{all}, \gamma_{body})] \end{aligned}$$

Using the definition of  $\mathcal{D}_P[\_]$  from section 8.6.1, this becomes

$$= \mathcal{D}_C[\gamma_{body}](\mathcal{D}_{D^*}[\Delta_{all}]\emptyset)$$

Expanding the definition of  $\Delta_{all}$  gives

$$= \mathcal{D}_C[\gamma_{body}](\mathcal{D}_{D^*}[\langle \text{declVar}(n, \text{integer}), \\ \text{declVar}(sq, \text{integer}), \text{declVar}(limit, \text{integer}) \rangle]\emptyset)$$

Writing the list as a concatenation of two sublists gives

$$= \mathcal{D}_C[\gamma_{body}](\mathcal{D}_{D^*}[\langle \text{declVar}(n, \text{integer}) \rangle \\ \hat{\ } \langle \text{declVar}(sq, \text{integer}), \text{declVar}(limit, \text{integer}) \rangle]\emptyset)$$

Using the definition of  $\mathcal{D}_{D^*}[\Delta_1 \hat{\ } \Delta_2]$  (section 8.2.2.1) gives

$$= \mathcal{D}_C[\gamma_{body}](\mathcal{D}_{D^*}[\langle \text{declVar}(sq, \text{integer}), \text{declVar}(limit, \text{integer}) \rangle] \\ \circ \mathcal{D}_{D^*}[\langle \text{declVar}(n, \text{integer}) \rangle]\emptyset)$$

Using the definition of  $\mathcal{D}_{D^*}[\langle \delta \rangle]$ , and the definition of composition  $\circ$  gives

$$= \mathcal{D}_C[\gamma_{body}](\mathcal{D}_{D^*}[\langle \text{declVar}(sq, \text{integer}), \text{declVar}(limit, \text{integer}) \rangle] \\ (\mathcal{D}_D[\text{declVar}(n, \text{integer})]\emptyset))$$

The meaning of the declaration of  $n$  is to add the name to the environment, mapping it to *checkOK* (section 8.2.1.1):

$$= \mathcal{D}_C[\gamma_{body}](\mathcal{D}_{D^*}[\langle \text{declVar}(sq, \text{integer}), \text{declVar}(limit, \text{integer}) \rangle] \\ \{n \mapsto \text{checkOK}\})$$

Repeating this procedure for the other two declarations yields the full environment:

$$= \mathcal{D}_C[\gamma_{body}]\rho_\delta \\ \text{where } \rho_\delta == \{n \mapsto \text{checkOK}, sq \mapsto \text{checkOK}, limit \mapsto \text{checkOK}\}$$

Expanding the definition of  $\gamma_{body}$  gives

$$= \mathcal{D}_{C^*}[\langle \text{assign}(n, \text{const}(int_v 1)), \text{assign}(sq, \text{const}(int_v 1)), \\ \text{input } limit, \text{output}(\text{var } sq), \\ \text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } limit), \gamma_{loop}) \rangle]\rho_\delta$$

Using the definition of  $\mathcal{D}_{C^*}[-]$  (section 8.5.1.1) multiple times:

$$\begin{aligned}
&= \mathcal{D}_C[\text{assign}(n, \text{const}(int_v 1))] \rho_\delta \\
&\quad \times \mathcal{D}_C[\text{assign}(sq, \text{const}(int_v 1))] \rho_\delta \\
&\quad \times \mathcal{D}_C[\text{input } limit] \rho_\delta \\
&\quad \times \mathcal{D}_C[\text{output}(\text{var } sq)] \rho_\delta \\
&\quad \times \mathcal{D}_C[\text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } limit), \gamma_{loop})] \rho_\delta
\end{aligned}$$

Both the assignments have their target in the environment, and corresponding expressions are *checkOK*, so both assignments are *checkOK*. The input has its variable in the environment, and the output’s expression is *checkOK*, so both these commands are *checkOK*, too. Thus, the expression becomes

$$= \mathcal{D}_C[\text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } limit), \gamma_{loop})] \rho_\delta$$

Using the meaning of the loop command from section 8.5.7.1:

$$\begin{aligned}
&= \mathcal{D}_E[\text{binExpr}(\text{var } n, \text{less}, \text{var } limit)] \rho_\delta \\
&\quad \times \mathcal{D}_C[\gamma_{loop}] \rho_\delta
\end{aligned}$$

Using the meaning of the binary expression from section 8.4.5.1, and expanding the definition of  $\gamma_{loop}$ :

$$\begin{aligned}
&= \mathcal{D}_E[\text{var } n] \rho_\delta \\
&\quad \times \mathcal{D}_E[\text{var } limit] \rho_\delta \\
&\quad \times \mathcal{D}_C[\text{block}(\text{assign}(sq, \\
&\quad\quad\quad \text{binExpr}(\text{binExpr}(\text{var } sq, \text{plus}, \text{const}(int_v 1)), \\
&\quad\quad\quad \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n))), \\
&\quad\quad\quad \text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const}(int_v 1))), \\
&\quad\quad\quad \text{output}(\text{var } sq))] \rho_\delta
\end{aligned}$$

Both the expressions are *checkOK*. Expanding the block’s command list gives

$$\begin{aligned}
&= \mathcal{D}_C[\text{assign}(sq, \\
&\quad\quad\quad \text{binExpr}(\text{binExpr}(\text{var } sq, \text{plus}, \text{const}(int_v 1)), \\
&\quad\quad\quad \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n))] \rho_\delta \\
&\quad \times \mathcal{D}_C[\text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const}(int_v 1)))] \rho_\delta \\
&\quad \times \mathcal{D}_C[\text{output}(\text{var } sq)] \rho_\delta
\end{aligned}$$

The target of the first assignment is in the environment, so this becomes

$$\begin{aligned}
&= \mathcal{D}_E[\text{binExpr}(\text{binExpr}(\text{var } sq, \text{plus}, \text{const}(int_v 1)), \\
&\quad\quad\quad \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n))] \rho_\delta \\
&\quad \times \mathcal{D}_C[\text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const}(int_v 1)))] \rho_\delta \\
&\quad \times \mathcal{D}_C[\text{output}(\text{var } sq)] \rho_\delta
\end{aligned}$$

$$\begin{aligned}
&= \mathcal{D}_E[\text{binExpr}(\text{var } sq, \text{plus}, \text{const}(\text{int}_v 1))] \rho_\delta \\
&\quad \bowtie \mathcal{D}_E[\text{binExpr}(\text{var } n, \text{plus}, \text{var } n)] \rho_\delta \\
&\quad \bowtie \mathcal{D}_C[\text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const}(\text{int}_v 1)))] \rho_\delta \\
&\quad \bowtie \mathcal{D}_C[\text{output}(\text{var } sq)] \rho_\delta \\
&= \mathcal{D}_E[\text{var } sq] \rho_\delta \\
&\quad \bowtie \mathcal{D}_E[\text{const}(\text{int}_v 1)] \rho_\delta \\
&\quad \bowtie \mathcal{D}_E[\text{var } n] \rho_\delta \\
&\quad \bowtie \mathcal{D}_E[\text{var } n] \rho_\delta \\
&\quad \bowtie \mathcal{D}_C[\text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const}(\text{int}_v 1)))] \rho_\delta \\
&\quad \bowtie \mathcal{D}_C[\text{output}(\text{var } sq)] \rho_\delta
\end{aligned}$$

The four individual expressions are *checkOK*, so

$$\begin{aligned}
&= \mathcal{D}_C[\text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const}(\text{int}_v 1)))] \rho_\delta \\
&\quad \bowtie \mathcal{D}_C[\text{output}(\text{var } sq)] \rho_\delta
\end{aligned}$$

The target of the assignment is in the environment, and the output checks okay, so this becomes

$$\begin{aligned}
&= \mathcal{D}_E[\text{binExpr}(\text{var } n, \text{plus}, \text{const}(\text{int}_v 1))] \rho_\delta \\
&= \mathcal{D}_E[\text{var } n] \rho_\delta \bowtie \mathcal{D}_E[\text{const}(\text{int}_v 1)] \rho_\delta
\end{aligned}$$

Both these expressions are *checkOK*, so we have the final result

$$\begin{aligned}
\mathcal{D}_P[\text{square}] &= \text{checkOK} \\
&\square
\end{aligned}$$

So the ‘square’ program passes the declaration-before-use static check: it makes no use of any variables it has not declared. The other two static semantics can be calculated similarly, both giving *checkOK*.

### 9.2.3 The ‘square’ program, dynamic semantics

Let’s calculate the dynamic, or execution, meaning of the ‘square’ program (which is its output, a sequence of numbers) for an input of 3:

$$\begin{aligned}
&\mathcal{M}_P[\text{square}]\langle 3 \rangle \\
&= \mathcal{M}_P[\text{Tosca}(\Delta_{all}, \gamma_{body})]\langle 3 \rangle \\
&= \text{outOf}(\mathcal{M}_C[\gamma_{body}](\mathcal{M}_{D^*}[\Delta_{all}]\emptyset)(\emptyset, \langle 3 \rangle, \langle \rangle))
\end{aligned}$$

Substituting for  $\Delta_{all}$  gives

$$\begin{aligned}
&= \text{outOf}(\mathcal{M}_C[\gamma_{body}](\mathcal{M}_{D^*}[\langle \text{declVar}(n, \text{integer}), \\
&\quad \text{declVar}(sq, \text{integer}), \text{declVar}(\text{limit}, \text{integer}) \rangle]\emptyset) \\
&\quad (\emptyset, \langle 3 \rangle, \langle \rangle))
\end{aligned}$$

The declaration list can be expanded to give

$$= \text{outOf}(\mathcal{M}_C[\![\gamma_{body}]\!] (\mathcal{M}_D[\![\text{declVar}(limit, \text{integer})]\!] \\ \mathcal{M}_D[\![\text{declVar}(sq, \text{integer})]\!] \mathcal{M}_D[\![\text{declVar}(n, \text{integer})]\!] \emptyset \\ (\emptyset, \langle 3 \rangle, \langle \rangle)))$$

Applying these three declarations to the initially empty environment gives

$$= \text{outOf}(\mathcal{M}_C[\![\gamma_{body}]\!] \rho_f(\emptyset, \langle 3 \rangle, \langle \rangle)) \\ \text{where } \rho_f == \{n \mapsto \lambda_n, sq \mapsto \lambda_{sq}, limit \mapsto \lambda_{limit}\}$$

The command list can now be expanded to give

$$= \text{outOf}((\mathcal{M}_C[\![\text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } limit), \gamma_{loop})]\!] \rho_f) \\ (\mathcal{M}_C[\![\text{output}(\text{var } sq)]\!] \rho_f) \\ (\mathcal{M}_C[\![\text{input } limit]\!] \rho_f) \\ (\mathcal{M}_C[\![\text{assign}(sq, \text{const}(int_v 1))]\!] \rho_f) \\ (\mathcal{M}_C[\![\text{assign}(n, \text{const}(int_v 1))]\!] \rho_f(\emptyset, i, \langle \rangle)))$$

The two assignments update the store so that the location of  $n$  and the location of  $sq$  map to 1. The input command assigns the head of the input sequence to  $limit$ , and modifies the input sequence. The output command then modifies the output sequence:

$$= \text{outOf}(\mathcal{M}_C[\![\text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } limit), \gamma_{loop})]\!] \rho_f \sigma_1) \\ \text{where } \sigma_1 == (\{\lambda_n \mapsto int_v 1, \lambda_{sq} \mapsto int_v 1, \lambda_{limit} \mapsto int_v 3\}, \langle \rangle, \langle 1 \rangle)$$

The meaning of the loop depends on the value of the test. The value of the test is

$$\mathcal{M}_E[\![\text{binExpr}(\text{var } n, \text{less}, \text{var } limit)]\!] \rho_f \sigma_1 \\ = \mathcal{M}_B[\![\text{less}]\!] (\mathcal{M}_E[\![\text{var } n]\!] \rho_f \sigma_1, \mathcal{M}_E[\![\text{var } limit]\!] \rho_f \sigma_1) \\ = \mathcal{M}_B[\![\text{less}]\!] (int_v 1, int_v 3) \\ = \text{bool}_v(\text{if } 1 < 3 \text{ then } \top \text{ else } \text{F}) \\ = \text{bool}_v \top$$

The test is  $\top$ , so the *then* branch of the *loop* definition is used, giving

$$\mathcal{M}_P[\![\text{square}]\!] \langle 3 \rangle \\ = \text{outOf}(\mathcal{M}_C[\![\text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } limit), \gamma_{loop})]\!] \rho_f \\ (\mathcal{M}_C[\![\text{block}(\text{assign}(sq, \\ \text{binExpr}(\text{binExpr}(\text{var } sq, \text{plus}, \text{const}(int_v 1)), \\ \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n))), \\ \text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const}(int_v 1))), \\ \text{output}(\text{var } sq))]\!] \rho_f \sigma_1))$$

Expanding out the command list in the block gives

$$\begin{aligned}
&= \text{outOf}((\mathcal{M}_C[\text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } \textit{limit}), \gamma_{\text{loop}})] \rho_f) \\
&\quad (\mathcal{M}_C[\text{output}(\text{var } sq)] \rho_f) \\
&\quad (\mathcal{M}_C[\text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const}(\textit{int}_v 1)))] \rho_f) \\
&\quad (\mathcal{M}_C[\text{assign}(sq, \\
&\quad \quad \text{binExpr}(\text{binExpr}(\text{var } sq, \text{plus}, \text{const}(\textit{int}_v 1)), \\
&\quad \quad \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n)))] \rho_f \sigma_1))
\end{aligned}$$

The meaning of the assignment to  $sq$  is

$$\begin{aligned}
&\mathcal{M}_C[\text{assign}(sq, \\
&\quad \text{binExpr}(\text{binExpr}(\text{var } sq, \text{plus}, \text{const}(\textit{int}_v 1)), \\
&\quad \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n)))] \rho_f \sigma_1 \\
&= \sigma_1 \boxplus \{ \lambda_{sq} \mapsto \\
&\quad \mathcal{M}_E[\text{binExpr}(\text{binExpr}(\text{var } sq, \text{plus}, \text{const}(\textit{int}_v 1)), \\
&\quad \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n))] \rho_{\text{decl}} \sigma_1 \} \\
&= \sigma_1 \boxplus \{ \lambda_{sq} \mapsto \\
&\quad \mathcal{M}_B[\text{plus}](\mathcal{M}_E[\text{binExpr}(\text{var } sq, \text{plus}, \text{const}(\textit{int}_v 1))] \rho_{\text{decl}} \sigma_1, \\
&\quad \mathcal{M}_E[\text{binExpr}(\text{var } n, \text{plus}, \text{var } n)] \rho_{\text{decl}} \sigma_1) \} \\
&= \sigma_1 \boxplus \{ \lambda_{sq} \mapsto \mathcal{M}_B[\text{plus}](\mathcal{M}_B[\text{plus}](\textit{storeOf } \sigma_1 sq, \textit{int}_v 1), \\
&\quad \mathcal{M}_B[\text{plus}](\textit{storeOf } \sigma_1 n, \textit{storeOf } \sigma_1 n)) \} \\
&= \sigma_1 \boxplus \{ \lambda_{sq} \mapsto \mathcal{M}_B[\text{plus}](\mathcal{M}_B[\text{plus}](\textit{int}_v 1, \textit{int}_v 1), \mathcal{M}_B[\text{plus}](\textit{int}_v 1, \textit{int}_v 1)) \} \\
&= \sigma_1 \boxplus \{ \lambda_{sq} \mapsto \mathcal{M}_B[\text{plus}](\textit{int}_v(1 + 1), \textit{int}_v(1 + 1)) \} \\
&= \sigma_1 \boxplus \{ \lambda_{sq} \mapsto \textit{int}_v 4 \}
\end{aligned}$$

So we now have

$$\begin{aligned}
&\mathcal{M}_P[\text{square}]\langle 3 \rangle \\
&= \text{outOf}((\mathcal{M}_C[\text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } \textit{limit}), \gamma_{\text{loop}})] \rho_f) \\
&\quad (\mathcal{M}_C[\text{output}(\text{var } sq)] \rho_f) \\
&\quad \mathcal{M}_C[\text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const}(\textit{int}_v 1)))] \rho_f (\sigma_1 \boxplus \{ \lambda_{sq} \mapsto \textit{int}_v 4 \}))
\end{aligned}$$

The assignment increments the value of  $n$ , and the output command appends the value of  $sq$  to the output list, leaving us back at the loop:

$$\begin{aligned}
&= \text{outOf}(\mathcal{M}_C[\text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } \textit{limit}), \gamma_{\text{loop}})] \rho_f \sigma_2) \\
&\quad \text{where } \sigma_2 == (\{ \lambda_n \mapsto \textit{int}_v 2, \lambda_{sq} \mapsto \textit{int}_v 4, \lambda_{\textit{limit}} \mapsto \textit{int}_v 3 \}, \langle \rangle, \langle 1, 4 \rangle)
\end{aligned}$$

Evaluating the test in the loop in this new state  $\sigma_2$  gives

$$\begin{aligned}
&\mathcal{M}_E[\text{binExpr}(\text{var } n, \text{less}, \text{var } \textit{limit})] \rho_f \sigma_2 \\
&= \text{bool}_v(\text{if } 2 < 3 \text{ then } \top \text{ else } \text{F}) \\
&= \text{bool}_v \top
\end{aligned}$$

So it’s the *then* branch of the loop definition again. This gives the three commands again: the assignment that changes the value of *sq* to  $((4 + 1) + (2 + 2)) = 9$ , the increment of *n* to 3, and the output of *sq*. This leaves the state as  $\sigma_3$ , where

$$\sigma_3 == (\{\lambda_n \mapsto \text{int}_v 3, \lambda_{sq} \mapsto \text{int}_v 9, \lambda_{limit} \mapsto \text{int}_v 3\}, \langle \rangle, \langle 1, 4, 9 \rangle)$$

and we’re back at the loop again. Evaluating the test in the loop in this new state  $\sigma_3$  gives

$$\begin{aligned} \mathcal{M}_E[\text{binExpr}(\text{var } n, \text{less}, \text{var } limit)] \rho_f \sigma_3 \\ &= \text{bool}_v(\text{if } 3 < 3 \text{ then } \top \text{ else } \text{F}) \\ &= \text{bool}_v \text{F} \end{aligned}$$

so this time the *else* branch in the definition of the loop is used: the identity function.

$$\begin{aligned} \mathcal{M}_P[\text{square}](3) \\ &= \text{outOf}(\text{id State } \sigma_3) \\ &= \text{outOf } \sigma_3 \\ &= \langle 1, 4, 9 \rangle \end{aligned}$$

□

So the meaning of the square program with input of 3 is  $\langle 1, 4, 9 \rangle$ .

This is a lot of effort for such a simple result, and it is not sensible to calculate the meaning of a program in general this way by hand. However, when the specification is translated into Prolog, these are the sort of manipulations being performed by the interpreter. Also, they are sort of manipulations used when calculating the meaning of the compiler templates later on, in order to prove that they are correct.





## **Part III**

# **The Correct Compiler**



# Aida—the Target Language

## 10.1 Introduction

The example target language, Aida (‘An Imaginary Denotational Assembler’), is specified in this chapter. It is closer to a machine language than Tosca, for example, it has low level jump instructions rather than high level structured constructs such as a loop. It can be thought of as the first refinement of the compiler.

Do we have to go through with Aida all that we did with Tosca? Fortunately not. This is the target language, and it is used only in a well-controlled manner. In particular, no equivalent of the use checking or type checking semantics is needed: if the source program is checked, and the translation is done correctly, the target program will be correct, too. Jumps (the dreaded `gotos`) are handled in a controlled fashion.

If we wanted to define a complete language, rather than just what is sufficient for the target of a translation, much more would be needed, analogous to all the static semantics checks defined for Tosca. For example, static semantics would be defined to check that

- the labels referred to in all jumps and `gotos` exist,
- if a value is loaded from a location, then an appropriate value has previously been stored in it,
- a value used to control a conditional jump corresponds to a boolean value.

## 10.2 Abstract syntax

Aida has two syntactic categories: instructions and programs. It also uses labels, which are modelled as numbers:

$$\textit{Label} == \mathbb{N}$$

Aida’s instructions are goto (an unconditional jump to a label), a conditional jump to a label, a label (the target of jumps), an instruction to load a constant value, an instruction to load a value from a location, an instruction to store a value at a location, various unary and binary arithmetic instructions, an input, and an output:

```

INSTR ::= goto⟨⟨Label⟩⟩
         | jump⟨⟨Label⟩⟩
         | label⟨⟨Label⟩⟩
         | loadConst⟨⟨VALUE⟩⟩
         | loadVar⟨⟨Locn⟩⟩
         | store⟨⟨Locn⟩⟩
         | unyOp⟨⟨UNY_OP⟩⟩
         | binOp⟨⟨BIN_OP × Locn⟩⟩
         | input
         | output

```

An Aida program is a sequence of instructions:

```

AIDA_PROG ::= Aida⟨⟨seq INSTR⟩⟩

```

There is no need to define a concrete syntax for Aida: its abstract syntax is simple and concise enough to be understandable. For a real compiler, a concrete syntax would need to be defined in terms of the appropriate assembly language mnemonics.

### 10.3 Aida’s domains

Locations are modelled by numbers as in Tosca. Aida’s store maps locations to the values held there:

$$Store_I == Locn \leftrightarrow VALUE$$

Note that Aida’s store has the same structure as Tosca’s dynamic store. In particular, the range of integers that can be stored in an Aida location is the same as can be stored in a Tosca location. Some low level languages correspond to processors that can store only a restricted range of integers in a single location (for example, 8-bit processors). For such a language,  $Store_I$  is more complicated, with values mapped to multiple locations; the correctness proofs are correspondingly more complicated. These intricacies are not discussed further here.

#### 10.3.1 Aida’s state

Aida’s state consists of its store, and the input and output streams:

$$State_I == Store_I \times Input \times Output$$

Note that Aida's state has the same structure as Tosca's dynamic state. So Tosca's update function  $\boxplus$ , defined in section 7.6.1.1, can also be used to update Aida's state.

Aida has a special 'accumulator' memory location, which will be used to hold the results of arithmetic operations, and also the value used to control conditional jumps:

$$\mid A : \text{Locn}$$

The query functions  $\text{storeOf}_I$  and  $\text{outOf}_I$  return the  $\text{Store}_I$  and  $\text{Output}$  components of the  $\text{State}_I$ :

$$\left| \begin{array}{l} \text{storeOf}_I : \text{State}_I \rightarrow \text{Store}_I \\ \text{outOf}_I : \text{State}_I \rightarrow \text{Output} \end{array} \right. \\ \hline \forall \varsigma_i : \text{Store}_I; \text{in} : \text{Input}; \text{out} : \text{Output} \bullet \\ \quad \text{storeOf}_I(\varsigma_i, \text{in}, \text{out}) = \varsigma_i \\ \quad \wedge \text{outOf}_I(\varsigma_i, \text{in}, \text{out}) = \text{out}$$

### 10.3.2 Continuations and Aida's environment

Because of jumps, the semantics of a language like Aida is not as straightforward as that of one like Tosca. Sequences of commands do not compose in the same way. For example, the meaning of  $\langle \text{goto } \phi, \text{store } \lambda \rangle$  is not the meaning of  $\text{goto } \phi$  followed by the meaning of  $\text{store } \lambda$ ; the goto command somehow bypasses the store command. The conventional way to solve this problem is to use a *continuation* semantics. A continuation is the computation that follows a command if it is not a jump; it is the state transition of the rest of the program from that point on. The meaning of a label is then simply a continuation, and a jump to a label means what is expected: the computation given by the rest of the program from that label onwards. The meaning of a jump is the meaning of the label it jumps to. (See section 10.5 for a small example of continuations.)

Hence, a continuation is a computation, that is, a state transition:

$$\text{Cont} == \text{State}_I \rightarrow \text{State}_I$$

Aida's environment is a mapping from labels to what they denote, which is the computation that follows from jumping to the label. This computation is a continuation:

$$\text{Env}_I == \text{Label} \rightarrow \text{Cont}$$

## 10.4 Aida’s dynamic semantics

### 10.4.1 Meaning functions

The meaning functions for instructions and sequences of instructions are declared here, and defined in the following sections.

An instruction causes a state change. So the instruction meaning function  $\mathcal{M}_I[\_]$  maps an instruction to the relevant state transition function, in the context of an environment and continuation. A similar function is defined for lists of instructions.

$$\left| \begin{array}{l} \mathcal{M}_I[\_] : INSTR \mapsto Env_I \mapsto Cont \mapsto State_I \mapsto State_I \\ \mathcal{M}_{I^*}[\_] : seq\ INSTR \mapsto Env_I \mapsto Cont \mapsto State_I \mapsto State_I \end{array} \right.$$

### 10.4.2 Multiple instructions

The empty list of instructions has no effect. The meaning of the singleton list is the meaning of that instruction. The meaning of a list of instructions composed of two sublists is given by the meaning of the first sublist executed with a continuation given by the second sublist:

$$\left| \begin{array}{l} \mathcal{M}_{I^*}[\langle \rangle] \rho_i \vartheta \sigma_i = \vartheta \sigma_i \\ \mathcal{M}_{I^*}[\langle \iota \rangle] \rho_i \vartheta \sigma_i = \mathcal{M}_I[\iota] \rho_i \vartheta \sigma_i \\ \mathcal{M}_{I^*}[I_1 \wedge I_2] \rho_i \vartheta \sigma_i = \mathcal{M}_{I^*}[I_1] \rho_i (\mathcal{M}_{I^*}[I_2] \rho_i \vartheta) \sigma_i \end{array} \right.$$

### 10.4.3 Goto

A `goto` instruction performs its following computation with the continuation denoted by the label:

$$\left| \mathcal{M}_I[\text{goto } \phi] \rho_i \vartheta \sigma_i = \rho_i[\phi] \sigma_i \right.$$

### 10.4.4 Jump

A `jump` instruction is a conditional `goto`. If the value in the accumulator is `F`, it jumps to the label, performing its following computation with the label’s continuation. Otherwise it proceeds with the original continuation:

$$\left| \begin{array}{l} \mathcal{M}_I[\text{jump } \phi] \rho_i \vartheta \sigma_i = \\ \quad \text{if } storeOf_I \sigma_i A = bool_v\ F \text{ then } \rho_i[\phi] \sigma_i \text{ else } \vartheta \sigma_i \end{array} \right.$$

#### 10.4.5 Load a constant

A `loadConst` instruction loads a constant value into the accumulator:

$$\left| \mathcal{M}_I[\text{loadConst } \chi] \rho_i \vartheta \sigma_i = \vartheta(\sigma_i \boxplus \{A \mapsto \chi\})$$

#### 10.4.6 Load from a memory location

A `loadVar` instruction loads the value stored in a memory location into the accumulator:

$$\left| \mathcal{M}_I[\text{loadVar } \lambda] \rho_i \vartheta \sigma_i = \vartheta(\sigma_i \boxplus \{A \mapsto \text{storeOf}_I \sigma_i \lambda\})$$

#### 10.4.7 Store a value

A `store` instruction stores the value in the accumulator at a memory location:

$$\left| \mathcal{M}_I[\text{store } \lambda] \rho_i \vartheta \sigma_i = \vartheta(\sigma_i \boxplus \{\lambda \mapsto \text{storeOf}_I \sigma_i A\})$$

#### 10.4.8 Unary operator

A `unyOp` instruction changes the value in the accumulator in a way determined by the operator. The unary operators are assumed to have the same meaning as in Tosca.

$$\left| \mathcal{M}_I[\text{unyOp } \psi] \rho_i \vartheta \sigma_i = \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_U[\psi](\text{storeOf}_I \sigma_i A)\})$$

#### 10.4.9 Binary operator

A `binOp` instruction combines the values in the accumulator and a memory location in a way determined by the operator. The resulting value is stored in the accumulator. The binary operators are assumed to have the same meaning as in Tosca:

$$\left| \begin{array}{l} \mathcal{M}_I[\text{binOp}(\omega, \lambda)] \rho_i \vartheta \sigma_i = \\ \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_B[\omega](\text{storeOf}_I \sigma_i A, \text{storeOf}_I \sigma_i \lambda)\}) \end{array}$$

#### 10.4.10 Input

An `input` instruction chops the the head off the input sequence and loads it into the accumulator:

$$\left| \mathcal{M}_I[\text{input}] \rho_i \vartheta(\varsigma_i, \langle n \rangle \hat{\sim} in, out) = \vartheta(\varsigma_i \oplus \{A \mapsto \text{int}_v n\}, in, out)$$

### 10.4.11 Output

An output instruction appends the value in the accumulator to the output sequence:

$$\mathcal{M}_I[\text{output}] \rho_i \vartheta(\varsigma_i, in, out) = \vartheta(\varsigma_i, in, out \hat{\ } \langle int_v \sim(\varsigma_i A) \rangle)$$

### 10.4.12 Labels and a program

A complete Aida program is an **Aida** construct, and maps input to output.

The meanings of the **label** and **Aida** constructs are defined together. In the following, it is assumed that none of the commands in the sequence of instructions  $I_n$  is a **label**. The body instructions are executed in an initial state that is empty, except for the input component:

$$\begin{array}{|l} \mathcal{M}_A[\_ ] : AIDA\_PROG \mapsto Input \mapsto Output \\ \hline \exists \rho_i : Env_I; \vartheta_1, \vartheta_2, \vartheta_3, \dots, \vartheta_n : Cont \mid \\ \quad \rho_i = \{ \phi_1 \mapsto \vartheta_1, \phi_2 \mapsto \vartheta_2, \dots, \phi_n \mapsto \vartheta_n \} \\ \quad \wedge \vartheta_1 = \mathcal{M}_{I^*}[I_1] \rho_i \vartheta_2 \\ \quad \wedge \vartheta_2 = \mathcal{M}_{I^*}[I_2] \rho_i \vartheta_3 \\ \quad \wedge \dots \\ \quad \wedge \vartheta_n = \mathcal{M}_{I^*}[I_n] \rho_i (\text{id } State_I) \bullet \\ \mathcal{M}_A[\text{Aida}(I_0 \hat{\ } \\ \quad \langle \text{label } \phi_1 \rangle \hat{\ } I_1 \hat{\ } \\ \quad \langle \text{label } \phi_2 \rangle \hat{\ } I_2 \hat{\ } \\ \quad \dots \hat{\ } \\ \quad \langle \text{label } \phi_n \rangle \hat{\ } I_n) ] in = \\ \quad outOf_I(\mathcal{M}_{I^*}[I_0] \rho_i \vartheta_1(\emptyset, in, \langle \rangle)) \end{array}$$

## 10.5 A small example

As a small example of how Aida's continuation semantics works, consider the following Aida program:

```
example ==
  Aida⟨loadConst(int_v 0), output,
      goto 1, loadConst(int_v 1), output,
      label 1, loadConst(int_v 2), output⟩
```



Informally, this program outputs 0, jumps over the output of 1, and then outputs 2. This can be shown formally by calculating the meaning of the program with an empty input stream:

$$\begin{aligned}
& \mathcal{M}_A \llbracket \text{example} \rrbracket \langle \rangle \\
&= \text{outOf}_I(\mathcal{M}_{I^*} \llbracket \langle \text{loadConst}(int_v 0), \text{output}, \\
&\quad \text{goto 1, loadConst}(int_v 1), \text{output} \rrbracket \rho_i \vartheta_1(\emptyset, \langle \rangle, \langle \rangle)) \\
&\quad \text{where } \vartheta_1 == \mathcal{M}_{I^*} \llbracket \langle \text{loadConst}(int_v 2), \text{output} \rrbracket \rho_i(\text{id } State_I) \\
&\quad \text{and } \rho_i == \{1 \mapsto \vartheta_1\}
\end{aligned}$$

Splitting the instruction list into two gives

$$\begin{aligned}
&= \text{outOf}_I(\mathcal{M}_{I^*} \llbracket \langle \text{loadConst}(int_v 0) \rangle \hat{\ } \\
&\quad \langle \text{output, goto 1, loadConst}(int_v 1), \text{output} \rrbracket \rho_i \vartheta_1(\emptyset, \langle \rangle, \langle \rangle))
\end{aligned}$$

Using the definition of  $\mathcal{M}_{I^*} \llbracket I_1 \hat{\ } I_2 \rrbracket$  from section 10.4.2 gives

$$\begin{aligned}
&= \text{outOf}_I(\mathcal{M}_{I^*} \llbracket \langle \text{loadConst}(int_v 0) \rangle \rrbracket \rho_i \\
&\quad (\mathcal{M}_{I^*} \llbracket \langle \text{output, goto 1, loadConst}(int_v 1), \text{output} \rrbracket \rho_i \vartheta_1(\emptyset, \langle \rangle, \langle \rangle))
\end{aligned}$$

Using the definition of  $\mathcal{M}_{I^*} \llbracket \langle i \rangle \rrbracket$  from section 10.4.2 gives

$$\begin{aligned}
&= \text{outOf}_I(\mathcal{M}_I \llbracket \text{loadConst}(int_v 0) \rrbracket \rho_i \\
&\quad (\mathcal{M}_{I^*} \llbracket \langle \text{output, goto 1, loadConst}(int_v 1), \text{output} \rrbracket \rho_i \vartheta_1(\emptyset, \langle \rangle, \langle \rangle))
\end{aligned}$$

The `loadConst` instruction loads its value into the accumulator, and proceeds with the current continuation:

$$\begin{aligned}
&= \text{outOf}_I(\mathcal{M}_{I^*} \llbracket \langle \text{output, goto 1, loadConst}(int_v 1), \text{output} \rrbracket \rho_i \vartheta_1 \\
&\quad (\{A \mapsto int_v 0\}, \langle \rangle, \langle \rangle))
\end{aligned}$$

Splitting the instruction sequence and using the definition of  $\mathcal{M}_{I^*} \llbracket - \rrbracket$  again, gives

$$\begin{aligned}
&= \text{outOf}_I(\mathcal{M}_I \llbracket \text{output} \rrbracket \rho_i \\
&\quad (\mathcal{M}_{I^*} \llbracket \langle \text{goto 1, loadConst}(int_v 1), \text{output} \rrbracket \rho_i \vartheta_1(\{A \mapsto int_v 0\}, \langle \rangle, \langle \rangle))
\end{aligned}$$

The `output` instruction appends the value in the accumulator to the output stream, and proceeds with the current continuation:

$$\begin{aligned}
&= \text{outOf}_I(\mathcal{M}_{I^*} \llbracket \langle \text{goto 1, loadConst}(int_v 1), \text{output} \rangle \rrbracket \rho_i \vartheta_1 \\
&\quad (\{A \mapsto int_v 0\}, \langle \rangle, \langle 0 \rangle))
\end{aligned}$$

Splitting the instruction sequence and using the definition of  $\mathcal{M}_{I^*} \llbracket - \rrbracket$  again, gives

$$\begin{aligned}
&= \text{outOf}_I(\mathcal{M}_I \llbracket \text{goto 1} \rrbracket \rho_i \\
&\quad (\mathcal{M}_{I^*} \llbracket \langle \text{loadConst}(int_v 1), \text{output} \rangle \rrbracket \rho_i \vartheta_1(\{A \mapsto int_v 0\}, \langle \rangle, \langle 0 \rangle))
\end{aligned}$$

The meaning of the `goto` instruction is the meaning of the label (section 10.4.3), which is the continuation corresponding to the computation from the position of the label, not from the current position:

$$= \text{outOf}_I(\rho_i[\phi])(\{A \mapsto \text{int}_v 0\}, \langle \rangle, \langle 0 \rangle)$$

Looking up the label in the environment gives the corresponding continuation:

$$= \text{outOf}_I(\vartheta_1(\{A \mapsto \text{int}_v 0\}, \langle \rangle, \langle 0 \rangle))$$

Substituting for the continuation gives

$$= \text{outOf}_I(\mathcal{M}_{I^*}[\langle \text{loadConst}(\text{int}_v 2), \text{output} \rangle] \rho_i(\text{id State}_I))(\{A \mapsto \text{int}_v 0\}, \langle \rangle, \langle 0 \rangle)$$

Using the definition of  $\mathcal{M}_{I^*}[\_]$  from section 10.4.2 multiple times gives

$$= \text{outOf}_I(\mathcal{M}_I[\text{loadConst}(\text{int}_v 2)] \rho_i(\mathcal{M}_I[\text{output}] \rho_i(\text{id State}_I)))(\{A \mapsto \text{int}_v 0\}, \langle \rangle, \langle 0 \rangle)$$

Loading the constant into the accumulator gives

$$= \text{outOf}_I(\mathcal{M}_I[\text{output}] \rho_i(\text{id State}_I))(\{A \mapsto \text{int}_v 2\}, \langle \rangle, \langle 0 \rangle)$$

Outputting the value in the accumulator gives

$$= \text{outOf}_I((\text{id State}_I))(\{A \mapsto \text{int}_v 2\}, \langle \rangle, \langle 0, 2 \rangle)$$

The identity continuation leaves the state unchanged, and hence the meaning of the *example* program is the output component of this state:

$$= \langle 0, 2 \rangle$$

□

This small example shows how continuation semantics enable jumps to be defined. As a larger example, the meaning of the Aida form of the *square* program is calculated later. But first, it has to be compiled. That is the subject of the next chapter.

# The Templates—Operational Semantics

This chapter gives an ‘algorithmic’ style of specification that defines the compiler: it defines what Aida statements are produced for each Tosca fragment. As an algorithm, it is rather overspecified in places. Aida labels are allocated sequentially, but whether the labels to implement a **choice** construct, for example, are allocated before or after any labels needed in its body constructs should make no difference to the correctness of the translation. However, a choice has to be made in the specification below. This freedom is an advantage: the particular choice made (after the body) makes the correctness proofs easier.

All the translations are from a Tosca language construct to a sequence of Aida instructions.

## 11.1 The translation environment

During translation, variable names are allocated memory locations, and labels are allocated to implement jumps in loops and choices.

Labels (which are modelled as numbers) are allocated sequentially, and never reused.

The translation environment is a mapping from variable names to distinct locations (that excludes the accumulator):

$$Env_O == NAME \rightsquigarrow (Locn \setminus \{A\})$$

The location *top* is greater than any allocated memory location (including the accumulator), locations above this are used to store temporary variables (when evaluating nested expressions):

$$\mid top : Locn$$

## 11.2 Declarations

Declarations do not translate to instructions, they just cause modifications to the translation environment:

$$\left| \begin{array}{l} \mathcal{O}_D \langle \_ \rangle : DECL \mapsto Env_O \mapsto Env_O \\ \mathcal{O}_{D^*} \langle \_ \rangle : \text{seq } DECL \mapsto Env_O \mapsto Env_O \end{array} \right.$$

### 11.2.1 Multiple declarations

Translating an empty declaration list has no effect on the environment. Translating a list of a single declaration has the same effect as translating that declaration. Translating a declaration list composed of two sublists is done by translating the first sublist (and thereby changing the environment), then translating the second sublist in the new environment:

$$\left| \begin{array}{l} \mathcal{O}_{D^*} \langle \langle \rangle \rangle \rho_o = \rho_o \\ \mathcal{O}_{D^*} \langle \langle \delta \rangle \rangle \rho_o = \mathcal{O}_D \langle \delta \rangle \rho_o \\ \mathcal{O}_{D^*} \langle \Delta_1 \hat{\ } \Delta_2 \rangle \rho_o = (\mathcal{O}_{D^*} \langle \Delta_2 \rangle \circ \mathcal{O}_{D^*} \langle \Delta_1 \rangle) \rho_o \end{array} \right.$$

### 11.2.2 Variable declaration

A variable declaration modifies the translation environment by mapping the variable to a previously unallocated location. The translation is

$$\left| \begin{array}{l} \exists \lambda : Locn \mid \lambda \notin (\{A\} \cup \text{ran } \rho_o) \bullet \\ \mathcal{O}_D \langle \text{declVar}(\xi, \tau) \rangle \rho_o = \rho_o \oplus \{\xi \mapsto \lambda\} \end{array} \right.$$

## 11.3 Expressions

Translating expressions produces instructions, but does not alter the environment. The translation does need to know the environment, since expressions can reference variables. It also needs to know the location above which it can store temporary variables:

$$\left| \mathcal{O}_E \langle \_ \rangle : EXPR \longrightarrow Env_O \longrightarrow Locn \longrightarrow \text{seq } INSTR \right.$$

The resulting sequence of instructions has the effect of storing the value of the expression in the accumulator.

### 11.3.1 Constant

A constant is stored in the accumulator directly:

$$\left| \mathcal{O}_E \langle \text{const } \chi \rangle \rho_o \lambda = \langle \text{loadConst } \chi \rangle \right.$$

### 11.3.2 Named variable

The value of a variable is loaded into the accumulator:

$$\left| \mathcal{O}_E \langle \text{var } \xi \rangle \rho_o \lambda = \langle \text{loadVar}(\rho_o[[\xi]]) \rangle \right.$$

### 11.3.3 Unary expression

For a unary expression, the body expression is translated (which results in the value of the body expression being stored in the accumulator) then the relevant unary operator is applied to this value:

$$\left| \begin{array}{l} \mathcal{O}_E \langle \text{unyExpr}(\psi, \epsilon) \rangle \rho_o \lambda = \\ \mathcal{O}_E \langle \epsilon \rangle \rho_o \lambda \hat{\ } \langle \text{unyOp } \psi \rangle \end{array} \right.$$

### 11.3.4 Binary expressions

The second subexpression is translated first, and its result stored in a temporary location. The first subexpression is then translated (ensuring any of its sub-subexpressions are stored at higher temporary locations) leaving its result in the accumulator. The appropriate binary operator is then used to combine these two values:

$$\left| \begin{array}{l} \mathcal{O}_E \langle \text{binExpr}(\epsilon_1, \omega, \epsilon_2) \rangle \rho_o \lambda = \\ \mathcal{O}_E \langle \epsilon_2 \rangle \rho_o \lambda \hat{\ } \langle \text{store } \lambda \rangle \\ \hat{\ } \mathcal{O}_E \langle \epsilon_1 \rangle \rho_o(\lambda + 1) \hat{\ } \langle \text{binOp}(\omega, \lambda) \rangle \end{array} \right.$$

## 11.4 Commands

Translating commands does not change the environment. The translation needs to know the next label available to implement loops and choices, and returns a suitably updated label along with the sequence of instructions:

$$\left| \begin{array}{l} \mathcal{O}_C \langle - \rangle : \text{CMD} \mapsto \text{Env}_O \rightarrow \text{Label} \mapsto (\text{Label} \times \text{seq } \text{INSTR}) \\ \mathcal{O}_{C^*} \langle - \rangle : \text{seq } \text{CMD} \mapsto \text{Env}_O \rightarrow \text{Label} \mapsto (\text{Label} \times \text{seq } \text{INSTR}) \end{array} \right.$$

The two components of the result can be extracted using the functions

$$\begin{aligned} \text{labelOf} &== \text{first}[\text{Label}, \text{seq } \text{INSTR}] \\ \text{instrOf} &== \text{second}[\text{Label}, \text{seq } \text{INSTR}] \end{aligned}$$

### 11.4.1 Multiple commands

Translating an empty command list has no effect on the label, and produces no instructions. Translating a list consisting of a single command has the same effect as translating that command. Translating a command list consisting of two sublists is done by translating the first sublist (which may produce instructions and change the label), then translating the second sublist with the new label. The two instruction lists are concatenated:

$$\left| \begin{array}{l} \mathcal{O}_{C^*} \langle \langle \rangle \rangle \rho_o \phi = (\phi, \langle \rangle) \\ \mathcal{O}_{C^*} \langle \langle \gamma \rangle \rangle \rho_o \phi = \mathcal{O}_C \langle \gamma \rangle \rho_o \phi \\ \exists \phi_1, \phi_2 : \text{Label} \mid \\ \quad \phi_1 = \text{labelOf}(\mathcal{O}_{C^*} \langle \Gamma_1 \rangle \rho_o \phi) \\ \quad \wedge \phi_2 = \text{labelOf}(\mathcal{O}_{C^*} \langle \Gamma_2 \rangle \rho_o \phi_1) \bullet \\ \mathcal{O}_{C^*} \langle \Gamma_1 \hat{\wedge} \Gamma_2 \rangle \rho_o \phi = \\ \quad (\phi_2, \\ \quad \quad \text{instrOf}(\mathcal{O}_{C^*} \langle \Gamma_1 \rangle \rho_o \phi) \\ \quad \quad \hat{\wedge} \text{instrOf}(\mathcal{O}_{C^*} \langle \Gamma_2 \rangle \rho_o \phi_1)) \end{array} \right.$$

### 11.4.2 Block

Translating a block command is the same as translating the list of body commands. The translation is

$$\left| \mathcal{O}_C \langle \text{block } \Gamma \rangle \rho_o \phi = \mathcal{O}_{C^*} \langle \Gamma \rangle \rho_o \phi \right.$$

### 11.4.3 Skip

A skip statement does not change the next label, and translates to no instructions:

$$\left| \mathcal{O}_C \langle \text{skip} \rangle \rho_o \phi = (\phi, \langle \rangle) \right.$$

### 11.4.4 Assignment

The expression is translated. This leaves its value in the accumulator, from where it is stored in the relevant name's location. The next label is unchanged:

$$\left| \begin{array}{l} \mathcal{O}_C \langle \text{assign}(\xi, \epsilon) \rangle \rho_o \phi = \\ (\phi, \mathcal{O}_E \langle \epsilon \rangle \rho_o \text{top} \hat{\ } \langle \text{store}(\rho_o \llbracket \xi \rrbracket) \rangle) \end{array} \right.$$

#### 11.4.5 Choice

Two labels are needed to implement a choice construct, for jumping to the else part, and to the end of the choice. The final ‘next label’ is modified by any labels allocated on translating the two subcommands, plus these two labels. So, if before the choice the next label is  $m$ , then the *then* subcommand is translated with this value for the next label. If translating it results in the allocation of  $j$  labels the resulting value of the next label, with which the *else* subcommand is translated, is  $\phi_1 = m + j$ . If translating the *then* subcommand results in  $l$  more labels, after translating it the next label is  $\phi_2 = m + j + l$ . The final value for next label also has the two labels used for implementing the choice construct itself:  $\phi_3 = m + j + l + 2$ .

The translation is

$$\left| \begin{array}{l} \exists \phi_1, \phi_2 : \text{Label} \mid \\ \phi_1 = \text{labelOf}(\mathcal{O}_C \langle \gamma_1 \rangle \rho_o \phi) \\ \wedge \phi_2 = \text{labelOf}(\mathcal{O}_C \langle \gamma_2 \rangle \rho_o \phi_1) \bullet \\ \mathcal{O}_C \langle \text{choice}(\epsilon, \gamma_1, \gamma_2) \rangle \rho_o \phi = \\ (2 + \phi_2, \\ \mathcal{O}_E \langle \epsilon \rangle \rho_o \text{top} \\ \hat{\ } \langle \text{jump } \phi_2 \rangle \\ \hat{\ } \langle \text{instrOf}(\mathcal{O}_C \langle \gamma_1 \rangle \rho_o \phi) \\ \hat{\ } \langle \text{goto}(1 + \phi_2), \text{label } \phi_2 \rangle \\ \hat{\ } \langle \text{instrOf}(\mathcal{O}_C \langle \gamma_2 \rangle \rho_o \phi_1) \\ \hat{\ } \langle \text{label}(1 + \phi_2) \rangle) \end{array} \right.$$

#### 11.4.6 Loop

Two labels are needed to implement a loop construct, for jumping back to the beginning, and to the end of the loop. The final ‘next label’ value is modified by any labels allocated on translating the body command, plus these two labels. So, if before the loop the next label is  $\phi_m$ , then the body command is translated with this label. If translating the body results in the allocation of  $j$  labels the resulting value of the next label is  $\phi_1 = m + j$ . The final result also has the two labels used for implementing the loop construct itself:  $\phi_2 = m + j + 2$ .

The translation is

$$\begin{array}{l}
\exists \phi_1 : \text{Label} \mid \phi_1 = \text{labelOf}(\mathcal{O}_C \langle \gamma \rangle \rho_o \phi) \bullet \\
\mathcal{O}_C \langle \text{loop}(\epsilon, \gamma) \rangle \rho_o \phi = \\
\quad (2 + \phi_1, \\
\quad \quad \langle \text{label } \phi_1 \rangle \\
\quad \quad \hat{\sim} \mathcal{O}_E \langle \epsilon \rangle \rho_o \text{ top} \\
\quad \quad \hat{\sim} \langle \text{jump}(1 + \phi_1) \rangle \\
\quad \quad \hat{\sim} \text{instrOf}(\mathcal{O}_C \langle \gamma \rangle \rho_o \phi) \\
\quad \quad \hat{\sim} \langle \text{goto } \phi_1, \text{label}(1 + \phi_1) \rangle)
\end{array}$$

### 11.4.7 Input

A Tosca input is translated as an Aida input to the accumulator, followed by a store at the relevant name's location. It does not change the next label:

$$\begin{array}{l}
\mathcal{O}_C \langle \text{input } \xi \rangle \rho_o \phi = \\
\quad (\phi, \langle \text{input}, \text{store}(\rho_o \llbracket \xi \rrbracket) \rangle)
\end{array}$$

### 11.4.8 Output

A Tosca output is translated by translating the expression (leaving the result in the accumulator), followed by an Aida output from the accumulator. It does not change the next label:

$$\begin{array}{l}
\mathcal{O}_C \langle \text{output } \epsilon \rangle \rho_o \phi = \\
\quad (\phi, \mathcal{O}_E \langle \epsilon \rangle \rho_o \text{ top} \hat{\sim} \langle \text{output} \rangle)
\end{array}$$

### 11.4.9 A program

A program is translated by translating the declarations in the initially empty environment, and then translating the body command in the declarations' environment:

$$\begin{array}{l}
\mathcal{O}_P \langle \_ \rangle : \text{PROG} \leftrightarrow \text{AIDA\_PROG} \\
\hline
\exists \rho_o : \text{Env}_O \mid \rho_o = \mathcal{O}_{D^*} \langle \Delta \rangle \emptyset \bullet \\
\quad A < \min(\text{ran } \rho_o) \\
\quad \wedge \text{top} > \max(\text{ran } \rho_o) \\
\quad \wedge \mathcal{O}_P \langle \text{Tosca}(\Delta, \gamma) \rangle = \text{Aida}(\text{instrOf}(\mathcal{O}_C \langle \gamma \rangle \rho_o 0))
\end{array}$$

A convenient choice for the location of the accumulator is to be less than any allocated location, where all such locations are given by the range of the translation environment function,  $\text{ran } \rho_o$ . This choice allows higher location values to be used as temporary locations with impunity. The location  $\text{top}$  is defined to be



greater than any allocated memory location (and, hence, necessarily higher than the accumulator).

# The ‘Square’ Example, Compiled

## 12.1 Compiling the example

Translating the example *square* program (Chapter 5) from the high level language Tosca into the low level language Aida (that is, compiling it) gives

$$\begin{aligned} \mathcal{O}_P \langle \textit{square} \rangle \\ = \mathcal{O}_P \langle \textit{Tosca}(\Delta_{all}, \gamma_{body}) \rangle \end{aligned}$$

First, we need to translate the declarations, in an initially empty translation environment, to give the translation environment for commands:

$$\begin{aligned} \rho_o \\ = \mathcal{O}_{D^*} \langle \Delta_{all} \rangle \emptyset \\ = \mathcal{O}_{D^*} \langle \langle \textit{declVar}(n, \textit{integer}), \textit{declVar}(sq, \textit{integer}), \\ \textit{declVar}(limit, \textit{integer}) \rangle \rangle \emptyset \\ = \mathcal{O}_D \langle \textit{declVar}(limit, \textit{integer}) \rangle \\ \quad \mathcal{O}_D \langle \textit{declVar}(sq, \textit{integer}) \rangle \\ \quad \mathcal{O}_D \langle \textit{declVar}(n, \textit{integer}) \rangle \emptyset \end{aligned}$$

Let’s allocate memory locations sequentially, starting at zero, and choose the location of the accumulator to be  $-1$ . (The numbers representing memory locations are written as  $0_\lambda, 1_\lambda, \dots$ , and those representing labels as  $0_\phi, 1_\phi, \dots$ , rather than both as  $0, 1, \dots$ , so that they may more easily be distinguished in this example.)

$$\rho_o = \{n \mapsto 0_\lambda, sq \mapsto 1_\lambda, limit \mapsto 2_\lambda\}$$

Now we can translate the body command in this environment. Setting *top* equal to  $3_\lambda$  (which is greater than the accumulator and all the allocated locations):

$$\begin{aligned}
& \mathcal{O}_P \langle \text{square} \rangle \\
&= \text{Aida}(\text{instrOf}(\mathcal{O}_C \langle \gamma_{\text{body}} \rangle \rho_o 0_\phi)) \\
&= \text{Aida}(\text{instrOf}(\mathcal{O}_{C^*} \langle \text{assign}(n, \text{const } \text{int}_v 1), \text{assign}(sq, \text{const } \text{int}_v 1), \\
&\quad \text{input } \text{limit}, \text{output}(\text{var } sq), \\
&\quad \text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } \text{limit}), \gamma_{\text{loop}}) \rangle \rho_o 0_\phi))
\end{aligned}$$

Translating the first assignment gives

$$\begin{aligned}
& \mathcal{O}_C \langle \text{assign}(n, \text{const } \text{int}_v 1) \rangle \rho_o 0_\phi \\
&= (0_\phi, \mathcal{O}_E \langle \text{const } \text{int}_v 1 \rangle \rho_o 3_\lambda \hat{\ } \langle \text{store } \rho_o \llbracket n \rrbracket \rangle) \\
&= (0_\phi, \langle \text{loadConst } \text{int}_v 1, \text{store } 0_\lambda \rangle)
\end{aligned}$$

This has not changed the next label value, so the translation of the second assignment is

$$\begin{aligned}
& \mathcal{O}_C \langle \text{assign}(sq, \text{const } \text{int}_v 1) \rangle \rho_o 0_\phi \\
&= (0_\phi, \langle \text{loadConst } \text{int}_v 1, \text{store } 1_\lambda \rangle)
\end{aligned}$$

Again, this has not changed the next label value, and the translation of the input proceeds similarly:

$$\begin{aligned}
& \mathcal{O}_C \langle \text{input } \text{limit} \rangle \rho_o 0_\phi \\
&= (0_\phi, \text{input } \hat{\ } \langle \text{store } \rho_o \llbracket \text{limit} \rrbracket \rangle) \\
&= (0_\phi, \langle \text{input}, \text{store } 2_\lambda \rangle)
\end{aligned}$$

The next label value is still  $0_\phi$ . Translating the output command gives

$$\begin{aligned}
& \mathcal{O}_C \langle \text{output}(\text{var } sq) \rangle \rho_o 0_\phi \\
&= (0_\phi, \mathcal{O}_E \langle \text{var } sq \rangle \rho_o 3_\lambda \hat{\ } \langle \text{output} \rangle) \\
&= (0_\phi, \langle \text{loadVar } \rho_o \llbracket sq \rrbracket, \text{output} \rangle) \\
&= (0_\phi, \langle \text{loadVar } 1_\lambda, \text{output} \rangle)
\end{aligned}$$

Putting these translations together gives

$$\begin{aligned}
& \mathcal{O}_P \langle \text{square} \rangle \\
&= \text{Aida}(\langle \text{loadConst } \text{int}_v 1, \text{store } 0_\lambda, \\
&\quad \text{loadConst } \text{int}_v 1, \text{store } 1_\lambda, \\
&\quad \text{input}, \text{store } 2_\lambda, \\
&\quad \text{loadVar } 1_\lambda, \text{output} \rangle \\
&\quad \hat{\ } \text{instrOf}(\mathcal{O}_C \langle \text{loop}(\text{binExpr}(\text{var } n, \text{less}, \text{var } \text{limit}), \gamma_{\text{loop}}) \rangle \rho_o 0_\phi))
\end{aligned}$$

The translation of the loop expression is

$$\begin{aligned}
& \mathcal{O}_E \langle \text{binExpr}(\text{var } n, \text{less}, \text{var } \textit{limit}) \rangle \rho_o 3_\lambda \\
&= \mathcal{O}_E \langle \text{var } \textit{limit} \rangle \rho_o 3_\lambda \hat{\ } \langle \text{store } 3_\lambda \rangle \\
&\quad \hat{\ } \mathcal{O}_E \langle \text{var } n \rangle \rho_o 4_\lambda \hat{\ } \langle \text{binOp}(\text{less}, 3_\lambda) \rangle \\
&= \langle \text{loadVar } 2_\lambda, \text{store } 3_\lambda, \text{loadVar } 0_\lambda, \text{binOp}(\text{less}, 3_\lambda) \rangle
\end{aligned}$$

The translation of the loop body is performed with the current value of the next label,  $0_\phi$ . So

$$\begin{aligned}
& \mathcal{O}_C \langle \gamma_{\text{loop}} \rangle \rho_o 0_\phi \\
&= \mathcal{O}_{C^*} \langle \langle \text{assign}(\textit{sq}, \\
&\quad \text{binExpr}(\text{binExpr}(\text{var } \textit{sq}, \text{plus}, \text{const } \textit{int}_v 1), \\
&\quad \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n))) \\
&\quad \text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const } \textit{int}_v 1)), \\
&\quad \text{output}(\text{var } \textit{sq}) \rangle \rangle \rho_o 0_\phi
\end{aligned}$$

Translating the first assignment, that to  $\textit{sq}$ , gives

$$\begin{aligned}
& \mathcal{O}_C \langle \text{assign}(\textit{sq}, \\
&\quad \text{binExpr}(\text{binExpr}(\text{var } \textit{sq}, \text{plus}, \text{const } \textit{int}_v 1), \\
&\quad \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n))) \rangle \rho_o 0_\phi \\
&= (0_\phi, \\
&\quad \mathcal{O}_E \langle \text{binExpr}(\text{binExpr}(\text{var } \textit{sq}, \text{plus}, \text{const } \textit{int}_v 1), \\
&\quad \text{plus}, \text{binExpr}(\text{var } n, \text{plus}, \text{var } n)) \rangle \rho_o 3_\lambda \\
&\quad \hat{\ } \langle \text{store } \rho_o \llbracket \textit{sq} \rrbracket \rangle ) \\
&= (0_\phi, \\
&\quad \mathcal{O}_E \langle \text{binExpr}(\text{var } \textit{sq}, \text{plus}, \text{const } \textit{int}_v 1) \rangle \rho_o 3_\lambda \\
&\quad \hat{\ } \langle \text{store } 3_\lambda \rangle \\
&\quad \hat{\ } \mathcal{O}_E \langle \text{binExpr}(\text{var } n, \text{plus}, \text{var } n) \rangle \rho_o 4_\lambda \\
&\quad \hat{\ } \langle \text{binOp}(\text{plus}, 3_\lambda), \text{store } 1_\lambda \rangle ) \\
&= (0_\phi, \\
&\quad \mathcal{O}_E \langle \text{var } \textit{sq} \rangle \rho_o 3_\lambda \hat{\ } \langle \text{store } 3_\lambda \rangle \\
&\quad \mathcal{O}_E \langle \text{const } \textit{int}_v 1 \rangle \rho_o 4_\lambda \\
&\quad \hat{\ } \langle \text{binOp}(\text{plus}, 3_\lambda), \text{store } 3_\lambda \rangle \\
&\quad \hat{\ } \mathcal{O}_E \langle \text{var } n \rangle \rho_o 4_\lambda \hat{\ } \langle \text{store } 4_\lambda \rangle \hat{\ } \\
&\quad \mathcal{O}_E \langle \text{var } n \rangle \rho_o 5_\lambda \\
&\quad \hat{\ } \langle \text{binOp}(\text{plus}, 4_\lambda), \text{binOp}(\text{plus}, 3_\lambda), \text{store } 1_\lambda \rangle ) \\
&= (0_\phi, \\
&\quad \langle \text{loadVar } 1_\lambda, \text{store } 3_\lambda, \\
&\quad \text{loadConst } \textit{int}_v 1, \\
&\quad \text{binOp}(\text{plus}, 3_\lambda), \text{store } 3_\lambda, \\
&\quad \text{loadVar } 0_\lambda, \text{store } 4_\lambda, \\
&\quad \text{loadVar } 0_\lambda, \\
&\quad \text{binOp}(\text{plus}, 4_\lambda), \text{binOp}(\text{plus}, 3_\lambda), \text{store } 1_\lambda \rangle )
\end{aligned}$$

Translating the second assignment, to  $n$ , gives

$$\begin{aligned}
& \mathcal{O}_C \langle \text{assign}(n, \text{binExpr}(\text{var } n, \text{plus}, \text{const } \text{int}_v 1)) \rangle 0_\phi 3_\lambda \\
&= (0_\phi, \mathcal{O}_E \langle \text{binExpr}(\text{var } n, \text{plus}, \text{const } \text{int}_v 1) \rangle \rho_o 3_\lambda \hat{\sim} \langle \text{store } \rho_o \llbracket n \rrbracket \rangle) \\
&= (0_\phi, \\
&\quad \mathcal{O}_E \langle \text{var } n \rangle \rho_o 3_\lambda \hat{\sim} \langle \text{store } 3_\lambda \rangle \\
&\quad \hat{\sim} \mathcal{O}_E \langle \text{const } \text{int}_v 1 \rangle \rho_o 4_\lambda \hat{\sim} \langle \text{binOp}(\text{plus}, 3_\lambda), \\
&\quad \text{store } 0_\lambda \rangle) \\
&= (0_\phi, \\
&\quad \langle \text{loadVar } 0_\lambda, \text{store } 3_\lambda, \\
&\quad \text{loadConst } \text{int}_v 1, \text{binOp}(\text{plus}, 3_\lambda), \\
&\quad \text{store } 0_\lambda \rangle)
\end{aligned}$$

Translating the output command:

$$\begin{aligned}
& \mathcal{O}_C \langle \text{output}(\text{var } sq) \rangle \rho_o 0_\phi \\
&= (0_\phi, \langle \text{loadVar } 1_\lambda, \text{output} \rangle)
\end{aligned}$$

Hence, the translation of the complete loop body command is these three sequences of instructions concatenated. The next label value is still 0. The complete compiled version is

$$\begin{aligned}
& \mathcal{O}_P \langle \text{square} \rangle \\
&= \text{Aida}(\langle \text{loadConst } \text{int}_v 1, \text{store } 0_\lambda, \\
&\quad \text{loadConst } \text{int}_v 1, \text{store } 1_\lambda, \\
&\quad \text{input}, \text{store } 2_\lambda, \\
&\quad \text{loadVar } 1_\lambda, \text{output}, \\
&\quad \text{label } 0_\phi, \\
&\quad \text{loadVar } 2_\lambda, \text{store } 3_\lambda, \\
&\quad \text{loadVar } 0_\lambda, \text{binOp}(\text{less}, 3_\lambda), \\
&\quad \text{jump } 1_\phi, \\
&\quad \text{loadVar } 1_\lambda, \text{store } 3_\lambda, \\
&\quad \text{loadConst } \text{int}_v 1, \text{binOp}(\text{plus}, 3_\lambda), \\
&\quad \text{store } 3_\lambda, \\
&\quad \text{loadVar } 0_\lambda, \text{store } 4_\lambda, \\
&\quad \text{loadVar } 0_\lambda, \text{binOp}(\text{plus}, 4_\lambda), \\
&\quad \text{binOp}(\text{plus}, 3_\lambda), \\
&\quad \text{store } 1_\lambda, \\
&\quad \text{loadVar } 0_\lambda, \text{store } 3_\lambda, \\
&\quad \text{loadConst } \text{int}_v 1, \text{binOp}(\text{plus}, 3_\lambda), \\
&\quad \text{store } 0_\lambda, \\
&\quad \text{loadVar } 1_\lambda, \text{output}, \\
&\quad \text{goto } 0_\phi, \\
&\quad \text{label } 1_\phi \rangle)
\end{aligned}$$

□

## 12.2 The meaning after compilation

In this section, the meaning of the Aida version of the *square* program is calculated, for an input of 3. Tracing through this computation demonstrates how the structure of the Aida program mirrors that of the original Tosca program. This is the sort of close correspondence required for compiled code in high integrity applications, that enables it to be validated against the source code.

Naming the sequence of instructions corresponding to the body of the loop as

$$I_{body} ==$$

```

⟨loadVar 1λ, store 3λ,
loadConst intv 1, binOp(plus, 3λ),
store 3λ,
loadVar 0λ, store 4λ,
loadVar 0λ, binOp(plus, 4λ),
binOp(plus, 3λ),
store 1λ,
loadVar 0λ, store 3λ,
loadConst intv 1, binOp(plus, 3λ),
store 0λ,
loadVar 1λ, output⟩

```

gives the meaning of the whole program as

$$result ==$$

```

outOfI( MI[[Aida(⟨loadConst intv 1, store 0λ,
loadConst intv 1, store 1λ,
input, store 2λ,
loadVar 1λ, output,
label 0φ,
loadVar 2λ, store 3λ,
loadVar 0λ, binOp(less, 3λ),
jump 1φ⟩
^Ibody
^⟨goto 0φ,
label 1φ⟩)]](∅, ⟨3⟩, ⟨⟩)

```

To calculate the meaning of *result* we need to define the various components needed for calculating the meaning of an Aida construct. This includes the environment, mapping the two labels to their continuations

$$\rho_{\iota} == \{0_{\phi} \mapsto \vartheta_0, 1_{\phi} \mapsto \vartheta_1\}$$

and the two continuations representing the computations after the labels

$$\begin{aligned} \vartheta_0 &== \\ &\quad \mathcal{M}_{I^*} \llbracket \langle \text{loadVar } 2_\lambda, \text{store } 3_\lambda, \text{loadVar } 0_\lambda, \text{binOp}(\text{less}, 3_\lambda), \text{jump } 1_\phi \rangle \\ &\quad \quad \widehat{I_{body}} \widehat{\langle \text{goto } 0_\phi \rangle} \rrbracket \rho_i \vartheta_1 \\ \vartheta_1 &== \mathcal{M}_{I^*} \llbracket \langle \rangle \rrbracket \rho_i (\text{id } State_I) \end{aligned}$$

Evaluating the Aida construct gives

$$\begin{aligned} &result \\ &= outOf_I(\mathcal{M}_{I^*} \llbracket \langle \text{loadConst } int_v 1, \text{store } 0_\lambda, \text{loadConst } int_v 1, \text{store } 1_\lambda, \\ &\quad \text{input}, \text{store } 2_\lambda, \text{loadVar } 1_\lambda, \text{output} \rrbracket \rho_i \vartheta_0(\emptyset, \langle 3 \rangle, \langle \rangle)) \end{aligned}$$

Separating out the first instruction of the list gives

$$\begin{aligned} &= outOf_I(\mathcal{M}_I \llbracket \text{loadConst } int_v 1 \rrbracket \rho_i (\mathcal{M}_{I^*} \llbracket \langle \text{store } 0_\lambda, \\ &\quad \text{loadConst } int_v 1, \text{store } 1_\lambda, \\ &\quad \text{input}, \text{store } 2_\lambda, \text{loadVar } 1_\lambda, \text{output} \rrbracket \rho_i \vartheta_0)(\emptyset, \langle 3 \rangle, \langle \rangle)) \end{aligned}$$

Evaluating this `loadConst` instruction gives

$$\begin{aligned} &= outOf_I(\mathcal{M}_{I^*} \llbracket \langle \text{store } 0_\lambda, \text{loadConst } int_v 1, \text{store } 1_\lambda, \\ &\quad \text{input}, \text{store } 2_\lambda, \text{loadVar } 1_\lambda, \text{output} \rrbracket \rho_i \vartheta_0(\{A \mapsto int_v 1\}, \langle 3 \rangle, \langle \rangle)) \end{aligned}$$

Proceeding similarly with the rest of the instructions leads to

$$= outOf_I(\vartheta_0(\{A \mapsto int_v 1, 0_\lambda \mapsto int_v 1, 1_\lambda \mapsto int_v 1, 2_\lambda \mapsto int_v 3\}, \langle \rangle, \langle 1 \rangle))$$

Substituting for  $\vartheta_0$  gives

$$\begin{aligned} &= outOf_I(\mathcal{M}_{I^*} \llbracket \langle \text{loadVar } 2_\lambda, \text{store } 3_\lambda, \text{loadVar } 0_\lambda, \text{binOp}(\text{less}, 3_\lambda), \text{jump } 1_\phi \rangle \\ &\quad \widehat{I_{body}} \widehat{\langle \text{goto } 0_\phi \rangle} \rrbracket \rho_i \vartheta_1(\{A \mapsto int_v 1, \\ &\quad \quad 0_\lambda \mapsto int_v 1, 1_\lambda \mapsto int_v 1, 2_\lambda \mapsto int_v 3\}, \langle \rangle, \langle 1 \rangle)) \end{aligned}$$

Evaluating the first four instructions (up to the `jump`) gives

$$\begin{aligned} &= outOf_I(\mathcal{M}_{I^*} \llbracket \langle \text{jump } 1_\phi \rangle \widehat{I_{body}} \widehat{\langle \text{goto } 0_\phi \rangle} \rrbracket \rho_i \vartheta_1(\{A \mapsto bool_v \top, \\ &\quad \quad 0_\lambda \mapsto int_v 1, 1_\lambda \mapsto int_v 1, 2_\lambda \mapsto int_v 3, 3_\lambda \mapsto int_v 3\}, \\ &\quad \quad \langle \rangle, \langle 1 \rangle)) \end{aligned}$$

The value in the accumulator is  $\top$  (corresponding to the test on the Tosca while loop being  $\top$ ), so the `jump` does nothing: the instructions corresponding to the body of the loop are executed. Evaluating the rest of the instructions up to the `goto` (corresponding to the end of the loop) gives

$$\begin{aligned} &= outOf_I(\mathcal{M}_I \llbracket \text{goto } 0_\phi \rrbracket \rho_i \vartheta_1(\{A \mapsto int_v 4, \\ &\quad \quad 0_\lambda \mapsto int_v 2, 1_\lambda \mapsto int_v 4, 2_\lambda \mapsto int_v 3, \\ &\quad \quad \quad 3_\lambda \mapsto int_v 1, 4_\lambda \mapsto int_v 1\}, \langle \rangle, \langle 1, 4 \rangle)) \end{aligned}$$

The `goto` means the following computation is performed with continuation  $\vartheta_0$  (corresponding to jumping back to the beginning of the loop):

$$= \text{outOf}_I(\vartheta_0(\{A \mapsto \text{int}_v 4, 0_\lambda \mapsto \text{int}_v 2, 1_\lambda \mapsto \text{int}_v 4, 2_\lambda \mapsto \text{int}_v 3, \\ 3_\lambda \mapsto \text{int}_v 1, 4_\lambda \mapsto \text{int}_v 1\}, \langle \rangle, \langle 1, 4 \rangle))$$

Substituting for  $\vartheta_0$  leaves us back at the beginning of the loop, but with a different state:

$$= \text{outOf}_I(\mathcal{M}_I^*[\langle \text{loadVar } 2_\lambda, \text{store } 3_\lambda, \text{loadVar } 0_\lambda, \text{binOp}(\text{less}, 3_\lambda), \text{jump } 1_\phi \rangle \\ \hat{\ } I_{\text{body}} \hat{\ } \langle \text{goto } 0_\phi \rangle] \rho_i \vartheta_1(\{A \mapsto \text{int}_v 4, \\ 0_\lambda \mapsto \text{int}_v 2, 1_\lambda \mapsto \text{int}_v 4, 2_\lambda \mapsto \text{int}_v 3, \\ 3_\lambda \mapsto \text{int}_v 1, 4_\lambda \mapsto \text{int}_v 1\}, \langle \rangle, \langle 1, 4 \rangle))$$

Evaluating the first four instructions (up to the `jump`) again, gives

$$= \text{outOf}_I(\mathcal{M}_I^*[\langle \text{jump } 1_\phi \rangle \hat{\ } I_{\text{body}} \hat{\ } \langle \text{goto } 0_\phi \rangle] \rho_i \vartheta_1(\{A \mapsto \text{bool}_v \top, \\ 0_\lambda \mapsto \text{int}_v 2, 1_\lambda \mapsto \text{int}_v 4, 2_\lambda \mapsto \text{int}_v 3, \\ 3_\lambda \mapsto \text{int}_v 3, 4_\lambda \mapsto \text{int}_v 1\}, \langle \rangle, \langle 1, 4 \rangle))$$

The value in the accumulator is still  $\top$ , so again the `jump` does nothing. Evaluating the rest of the instructions up to the `goto` gives

$$= \text{outOf}_I(\mathcal{M}_I[\text{goto } 0_\phi] \rho_i \vartheta_1(\{A \mapsto \text{int}_v 9, \\ 0_\lambda \mapsto \text{int}_v 3, 1_\lambda \mapsto \text{int}_v 9, 2_\lambda \mapsto \text{int}_v 3, \\ 3_\lambda \mapsto \text{int}_v 2, 4_\lambda \mapsto \text{int}_v 2\}, \langle \rangle, \langle 1, 4, 9 \rangle))$$

Again, the `goto` means the following computation is performed with continuation  $\vartheta_0$ . Executing the first four commands, up to the `jump`, results in

$$= \text{outOf}_I(\mathcal{M}_I^*[\langle \text{jump } 1_\phi \rangle \hat{\ } I_{\text{body}} \hat{\ } \langle \text{goto } 0_\phi \rangle] \rho_i \vartheta_1(\{A \mapsto \text{bool}_v \text{F}, \\ 0_\lambda \mapsto \text{int}_v 3, 1_\lambda \mapsto \text{int}_v 9, 2_\lambda \mapsto \text{int}_v 3, \\ 3_\lambda \mapsto \text{int}_v 3, 4_\lambda \mapsto \text{int}_v 2\}, \langle \rangle, \langle 1, 4, 9 \rangle))$$

This time the value in the accumulator is  $\text{F}$  (we have finished looping), so the `jump` behaves like a `goto` to label 1, corresponding to the end of the loop. So we have

$$= \text{outOf}_I(\vartheta_1(\{A \mapsto \text{bool}_v \text{F}, 0_\lambda \mapsto \text{int}_v 3, 1_\lambda \mapsto \text{int}_v 9, 2_\lambda \mapsto \text{int}_v 3, \\ 3_\lambda \mapsto \text{int}_v 3, 4_\lambda \mapsto \text{int}_v 2\}, \langle \rangle, \langle 1, 4, 9 \rangle))$$

Substituting for  $\vartheta_1$  gives

$$= \text{outOf}_I(\mathcal{M}_I^*[\langle \rangle] \rho_i (\text{id } \text{State}_I)(\{A \mapsto \text{bool}_v \text{F}, \\ 0_\lambda \mapsto \text{int}_v 3, 1_\lambda \mapsto \text{int}_v 9, 2_\lambda \mapsto \text{int}_v 3, \\ 3_\lambda \mapsto \text{int}_v 3, 4_\lambda \mapsto \text{int}_v 2\}, \langle \rangle, \langle 1, 4, 9 \rangle)) \\ = \text{outOf}_I(\{A \mapsto \text{bool}_v \text{F}, 0_\lambda \mapsto \text{int}_v 3, 1_\lambda \mapsto \text{int}_v 9, 2_\lambda \mapsto \text{int}_v 3, \\ 3_\lambda \mapsto \text{int}_v 3, 4_\lambda \mapsto \text{int}_v 2\}, \langle \rangle, \langle 1, 4, 9 \rangle) \\ = \langle 1, 4, 9 \rangle \\ \square$$



So we see that the Aida meaning of the translation of *square* is the same as the Tosca meaning of *square*, for an input of  $\langle 3 \rangle$ . Proving that the corresponding meanings are *always* the same, for *any* Tosca program, is the topic of the next chapter.

# The Proofs—Calculating the Meaning of the Templates

## 13.1 Introduction

So far, the translation from Tosca to Aida has been purely syntactic. In this chapter, the semantics of both languages are used to prove that this translation is in fact correct.

What constitutes a correct translation? At the most abstract level, an Aida program is a correct translation of a Tosca program if the Aida meaning of the translation is the same as the Tosca meaning of the original. So the required correctness proof is

$$\vdash \mathcal{M}_P[\llbracket \text{Tosca}(\Delta, \gamma) \rrbracket] = \mathcal{M}_A[\llbracket \mathcal{O}_P \langle \text{Tosca}(\Delta, \gamma) \rangle \rrbracket]$$

In order to be able to manage the sheer size of this correctness proof, it is decomposed into a lot of simpler smaller proofs, one proof for each language construct. The collection of these subproofs constitutes the complete proof.

## 13.2 Retrieve functions

Tosca constructs can modify the Tosca environment and state. Each individual translation of each Tosca construct is deemed correct if the resulting sequence of Aida instructions modifies the corresponding Aida state in the appropriate way.

What is the corresponding state? The Aida state is ‘bigger’ than the Tosca one: as well as locations to store values of variables, it also has memory locations for an accumulator (used to hold the values of expressions), and ones for storing temporary variables during complex expression evaluation. The translation environment holds information (the mapping from variable names to locations), and needs to be considered, too.

Let's define two retrieve functions (so called, because they 'retrieve' the relevant Tosca state and environment from an Aida state and translation environment).

### 13.2.1 Retrieve the environment

The environment retrieve function is defined to be

$$\left| \begin{array}{l} \mathfrak{R}_E : Env_O \rightarrow Env \\ \hline \forall \rho_o : Env_O \bullet \mathfrak{R}_E \rho_o = \rho_o \end{array} \right.$$

The Tosca environment corresponding to a translation environment refers to the same variables, and maps them to the same locations.

### 13.2.2 Retrieve the state

The state retrieve function is defined to be

$$\left| \begin{array}{l} \mathfrak{R}_S : Env_O \rightarrow State_I \rightarrow State \\ \hline \forall \rho_o : Env_O; \varsigma_\iota : Store_I; in : Input; out : Output \bullet \\ \mathfrak{R}_S \rho_o(\varsigma_\iota, in, out) = ((\text{ran } \rho_o) \triangleleft \varsigma_\iota, in, out) \end{array} \right.$$

The Tosca state retrieved from an Aida state is that part of the Aida state containing the values of named variables, which are found from the translation environment. Hence, it excludes Aida's accumulator and temporary locations.

It is also useful to have a function that restricts the Aida state to a state containing just those locations corresponding to the accumulator, Tosca locations, and the temporary locations from *top* up to, but not including, some value, whilst ignoring the temporary locations at and above that value. This function has the effect of restricting the state to those values currently being considered, and ignoring values of temporary variables, left over from previous nested expression evaluations:

$$\left| \begin{array}{l} restrict : Env_O \times Locn \times State_I \rightarrow State_I \\ \hline \forall \rho_o : Env_O; \lambda : Locn; \varsigma_\iota : Store_I; in : Input; out : Output \bullet \\ restrict(\rho_o, \lambda, (\varsigma_\iota, in, out)) = \\ ((\{A\} \cup \text{ran } \rho_o \cup (top .. \lambda - 1)) \triangleleft \varsigma_\iota, in, out) \end{array} \right.$$

The following lemmas about *restrict* are useful later.

**Lemma r1.** Restricting a state to a memory value, then restricting it to a smaller value, has the same effect as a single restriction to the smaller value:

$$n : \mathbb{N} \vdash restrict(\rho_o, \lambda, restrict(\rho_o, \lambda + n, \sigma_\iota)) = restrict(\rho_o, \lambda, \sigma_\iota)$$

In this and all the following proofs, each line of the proof is annotated by an explanation of the definition, law or lemma used in deriving that line from the previous one. Definitions corresponding to Tosca, Aida, or template specifications are also labelled by the relevant section number where they can be found. Laws correspond to standard laws about Z operators, as given in [Spivey 1992].

Proof:

$$\begin{aligned}
& \text{restrict}(\rho_o, \lambda, \text{restrict}(\rho_o, \lambda + n, (\varsigma_i, \text{in}, \text{out}))) \\
&= \text{restrict}(\rho_o, \lambda, \\
&\quad ((\{A\} \cup \text{ran } \rho_o \cup (\text{top} \dots \lambda + n - 1)) \triangleleft \varsigma_i, \text{in}, \text{out})) \\
&= ((\{A\} \cup \text{ran } \rho_o \cup (\text{top} \dots \lambda - 1)) \triangleleft \\
&\quad ((\{A\} \cup \text{ran } \rho_o \cup (\text{top} \dots \lambda + n - 1)) \triangleleft \varsigma_i), \text{in}, \text{out}) \\
&= (((\{A\} \cup \text{ran } \rho_o \cup (\text{top} \dots \lambda - 1)) \cap \\
&\quad (\{A\} \cup \text{ran } \rho_o \cup (\text{top} \dots \lambda + n - 1))) \triangleleft \varsigma_i, \text{in}, \text{out}) \\
&= ((\{A\} \cup \text{ran } \rho_o \cup (\text{top} \dots \lambda - 1)) \triangleleft \varsigma_i, \text{in}, \text{out}) \\
&= \text{restrict}(\rho_o, \lambda, (\varsigma_i, \text{in}, \text{out}))
\end{aligned}$$

□

**Lemma r2.** Retrieving a restricted state has the same result as retrieving the unrestricted state:

$$\vdash \mathfrak{R}_S \rho_o(\text{restrict}(\rho_o, \lambda, \sigma_i)) = \mathfrak{R}_S \rho_o \sigma_i$$

Proof:

$$\begin{aligned}
& \mathfrak{R}_S \rho_o(\text{restrict}(\rho_o, \lambda, (\varsigma_i, \text{in}, \text{out}))) \\
&= \mathfrak{R}_S \rho_o((\{A\} \cup \text{ran } \rho_o \cup (\text{top} \dots \lambda - 1)) \triangleleft \varsigma_i, \text{in}, \text{out}) && \text{[defn } \textit{restrict}] \\
&= (\text{ran } \rho_o \triangleleft ((\{A\} \cup \text{ran } \rho_o \cup (\text{top} \dots \lambda - 1)) \triangleleft \varsigma_i), \text{in}, \text{out}) && \text{[defn } \mathfrak{R}_S] \\
&= (\text{ran } \rho_o \cap ((\{A\} \cup \text{ran } \rho_o \cup (\text{top} \dots \lambda - 1)) \triangleleft \varsigma_i), \text{in}, \text{out}) && \text{[law } \triangleleft] \\
&= (\text{ran } \rho_o \triangleleft \varsigma_i, \text{in}, \text{out}) && \text{[law } \cap, \cup] \\
&= \mathfrak{R}_S \rho_o(\varsigma_i, \text{in}, \text{out}) && \text{[defn } \mathfrak{R}_S]
\end{aligned}$$

□

### 13.3 Correctness conditions

The correctness arguments have the following structure.

1. An initial hypothesis. The initial Tosca state and environment are related by the retrieve functions to the initial Aida state and translation environment

$$Init_S == \sigma = \mathfrak{R}_S \rho_o \sigma_i$$

$$Init_E == \rho = \mathfrak{R}_E \rho_o$$

2. A final Tosca state and environment. These are calculated by applying the relevant meaning function to the before state and environment.
3. A final translation environment. This is found by translating the Tosca construct.
4. A final Aida state. This is calculated from the meaning of the sequence of Aida instructions produced by the translation.
5. A proof. The final Tosca state and environment are equal to the retrieved final state and environment.

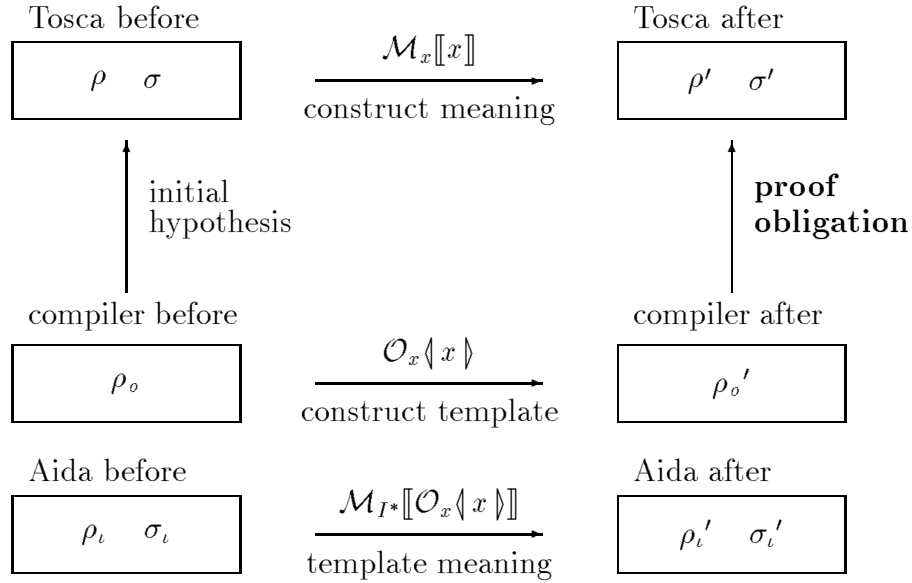
In summary, if you start in an Aida state, first retrieve the Tosca state, then perform the Tosca operation, you should end up in the same place as you would if you first performed the translated Aida instructions, then retrieved the Tosca state. These two equivalent paths can be summarized in a generic ‘correctness diagram’ (Figure 13.1).

The initial Tosca state (unprimed) is the name given to the retrieved Aida state. The final states (those primed) are names given to the quantities as transformed by the appropriate Tosca, Aida, or translation meaning functions. So three of the relationships are determined by the retrieve functions, Tosca’s semantics, the operational semantics, and Aida’s semantics. That the fourth relationship holds is what must be proved.

### 13.4 Proof by structural induction

Let’s say that some property needs to be proved for a general Tosca command. A command can be one of several kinds—a **block**, a **skip**, and so on—as given by the structure of the syntax for commands (section 4.7). If the property holds for each particular sort of command, then it holds for any command in general. So a proof for a general command consists of a separate proof for each branch in its free type syntax definition.

Now consider a particular branch in the syntax definition. If it is a ‘base case’ branch (**skip** in the case of commands) then the property must be proved for that case. In the recursive branches the construct is built from various subcomponents. For example, a **choice** command has three subcomponents: an expression and two



**Figure 13.1** A generic correctness diagram

commands. To prove a property of a **choice** command, it is sufficient to assume that the relevant property holds for its subcomponents, and then just prove that the way they have been combined to form the *choice* is correct. This assumption is called the *induction hypothesis*, which assumes the relevant properties for the components when attempting to prove a property of the composite construct. This assumption works because it mirrors the recursive structure of the language definition; all recursive constructs are ultimately built from primitive ‘base case’ constructs (**skip** for commands, **const** and **var** for expressions) and so the chain of assumptions bottoms-out at some point.

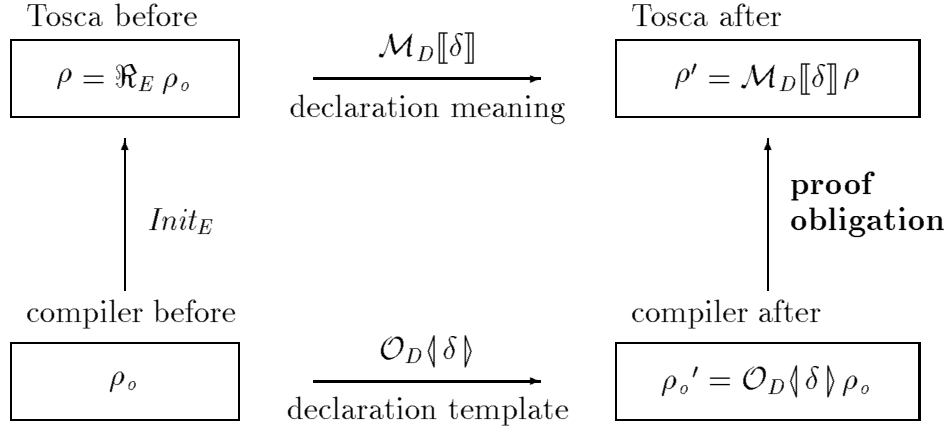
## 13.5 Declarations

### 13.5.1 Correctness condition

Evaluating a Tosca declaration changes the environment, but not the state. Translating a Tosca declaration changes the translation environment; it translates to no Aida instructions, so has no effect on the Aida state or environment. The correctness diagram for declarations is shown in Figure 13.2.

The translation is correct if the final Tosca environment corresponds to the final translation environment. The correctness condition is

$$Init_E; \rho' = \mathcal{M}_D[[\delta]]\rho; \rho_o' = \mathcal{O}_D\langle\delta\rangle\rho_o$$



**Figure 13.2** The correctness diagram for declarations

$$\vdash \rho' = \mathfrak{R}_E \rho_o'$$

The correctness condition for multiple declarations is analogous:

$$\begin{aligned} & Init_E; \rho' = \mathcal{M}_{D^*}[[\Delta]]\rho; \rho_o' = \mathcal{O}_{D^*}\langle\Delta\rangle\rho_o \\ & \vdash \rho' = \mathfrak{R}_E \rho_o' \end{aligned}$$

### 13.5.2 Variable declaration

The specification of the memory location allocation in the dynamic semantics and in the translation have deliberately been left loose. Hence, the locations can be chosen so as to simplify the proofs. Let's assume that the *same* locations are allocated in the Tosca dynamic semantics as in translation.

Here  $\delta = \text{declVar}(\xi, \tau)$ . So

$$\begin{aligned} & \rho_o' \\ &= \mathcal{O}_D\langle\text{declVar}(\xi, \tau)\rangle\rho_o && \text{[defn } \rho_o'] \\ &= \rho_o \oplus \{\xi \mapsto \lambda\} && \text{[template 11.2.2]} \\ &= \rho \oplus \{\xi \mapsto \lambda\} && \text{[Init}_E\text{]} \\ &= \mathcal{M}_D[[\text{declVar}(\xi, \lambda)]]\rho && \text{[Tosca 8.2.1.4]} \\ &= \rho' && \text{[defn } \rho'] \end{aligned}$$

□

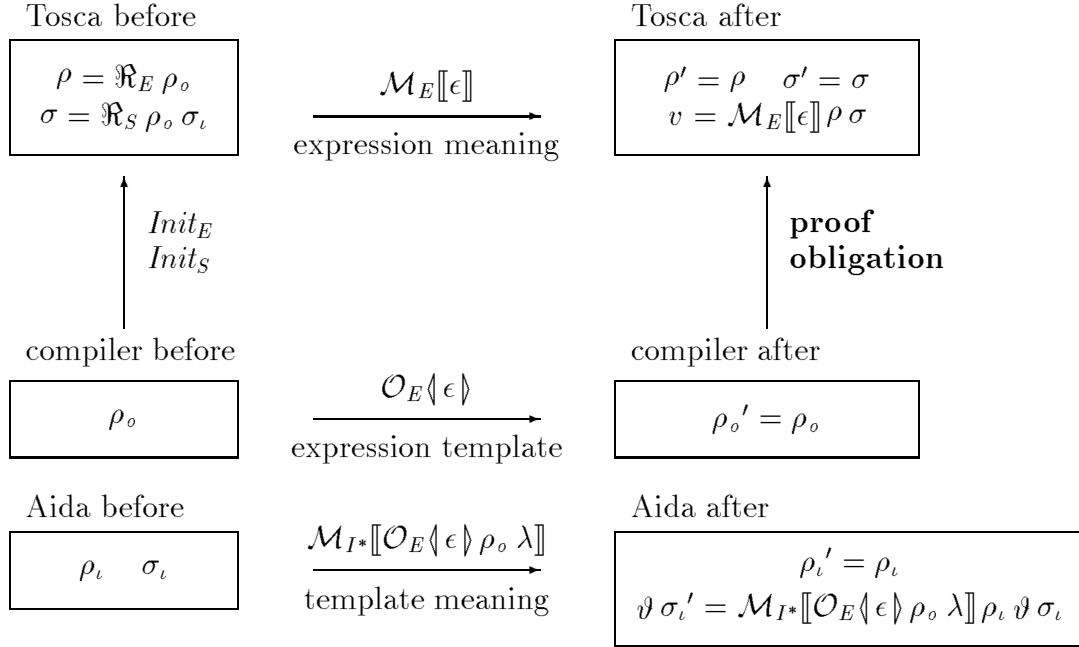


Figure 13.3 The correctness diagram for expressions

### 13.5.3 Multiple declarations

The definition of the translation for multiple declarations follows the form of the definition of the meaning of multiple declarations. Given the correctness of the single declaration translation, the multiple declaration correctness follows directly.

## 13.6 Expressions

### 13.6.1 Correctness condition

Evaluating a Tosca expression produces a value, and changes neither the environment nor the state (except possibly for some temporary variables). The translation is correct if doing the translation and evaluating the translated fragments leaves the states and environments corresponding, while storing the associated value in the accumulator. The correctness diagram for expressions is shown in Figure 13.3.

Expression evaluation does not change the environment. After evaluation, the result of an expression should be stored in the accumulator. None of the values stored in those Aida locations corresponding to Tosca locations should be changed, neither should those in temporary locations below the current ‘next location’ value.



Ones stored at and above this value may be changed during evaluation, since they may be intermediate results, no longer needed. The correctness condition is

$$\begin{aligned}
& \text{Init}_E; \text{Init}_S; \\
& \rho_o' = \rho_o; \rho_i' = \rho_i; \vartheta \sigma_i' = \mathcal{M}_{I^*}[\![\mathcal{O}_E \langle \epsilon \rangle \rho_o \lambda]\!] \rho_i \vartheta \sigma_i; \\
& \sigma' = \sigma; \rho' = \rho; \\
& v = \mathcal{M}_E[\![\epsilon]\!] \rho \sigma \\
& \vdash \text{restrict}(\rho_o', \lambda, \vartheta \sigma_i') = \text{restrict}(\rho_o, \lambda, \vartheta(\sigma_i \boxplus \{A \mapsto v\}))
\end{aligned}$$

For the two base cases (constant and variable) it is possible to prove the stronger condition

$$\dots \vdash \vartheta \sigma_i' = \vartheta(\sigma_i \boxplus \{A \mapsto v\})$$

because neither of these change any temporary locations.

### 13.6.2 Induction hypothesis

Where subexpressions occur in an expression, they are assumed to have been translated correctly, leaving the correct value in the accumulator and not affecting any of the temporary locations *below*  $\lambda$ :

$$\begin{aligned}
& \text{restrict}(\rho_o, \lambda_h, \mathcal{M}_{I^*}[\![\mathcal{O}_E \langle \epsilon \rangle, \rho_o, \lambda_h]\!] \rho_i \vartheta_h \sigma_i \\
& \quad = \text{restrict}(\rho_o, \lambda_h, \vartheta_h(\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\![\epsilon]\!] \rho \sigma\}))
\end{aligned}$$

### 13.6.3 Constant

Here  $\epsilon = \text{const } \chi$ . This is a base case, so there is no need for the induction hypothesis. So

$$\begin{aligned}
& \vartheta \sigma_i' \\
& \quad = \mathcal{M}_{I^*}[\![\mathcal{O}_E \langle \text{const } \chi \rangle \rho_o \lambda]\!] \rho_i \vartheta \sigma_i && \text{[defn } \sigma_i'] \\
& \quad = \mathcal{M}_I[\![\text{loadConst } \chi]\!] \rho_i \vartheta \sigma_i && \text{[template 11.3.1]} \\
& \quad = \vartheta(\sigma_i \boxplus \{A \mapsto \chi\}) && \text{[Aida 10.4.5]} \\
& \quad = \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\![\text{const } \chi]\!] \rho \sigma\}) && \text{[Tosca 8.4.2.4]} \\
& \quad = \vartheta(\sigma_i \boxplus \{A \mapsto v\}) && \text{[defn } v]
\end{aligned}$$

□

**13.6.4 Named variable**

Here  $\epsilon = \text{var } \xi$ . This is a base case, so there is no need for the induction hypothesis. So

$$\begin{aligned}
& \vartheta \sigma'_i \\
&= \mathcal{M}_{I^*}[\llbracket \mathcal{O}_E \langle \text{var } \xi \rangle \rho_o \lambda \rrbracket \rho_i \vartheta \sigma_i] && \text{[defn } \sigma'_i\text{]} \\
&= \mathcal{M}_I[\llbracket \text{loadVar } \rho_o \llbracket \xi \rrbracket \rrbracket \rho_i \vartheta \sigma_i] && \text{[template 11.3.2]} \\
&= \vartheta(\sigma_i \boxplus \{A \mapsto \text{storeOf}_I \sigma_i \rho_o \llbracket \xi \rrbracket\}) && \text{[Aida 10.4.6]} \\
&= \vartheta(\sigma_i \boxplus \{A \mapsto \text{storeOf } \sigma \rho \llbracket \xi \rrbracket\}) && \text{[defn } \sigma, \rho\text{]} \\
&= \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\llbracket \text{var } \xi \rrbracket \rho \sigma\}]) && \text{[Tosca 8.4.3.4]} \\
&= \vartheta(\sigma_i \boxplus \{A \mapsto v\}) && \text{[defn } v\text{]}
\end{aligned}$$

□

**13.6.5 Unary expression**

Here  $\epsilon = \text{unaryExpr}(\psi, \epsilon)$ . The induction hypothesis is assumed for the subexpression. So

$$\begin{aligned}
& \text{restrict}(\rho_o, \lambda, \vartheta \sigma'_i) \\
&= \text{restrict}(\rho_o, \lambda, \mathcal{M}_{I^*}[\llbracket \mathcal{O}_E \langle \text{unaryExpr}(\psi, \epsilon) \rangle \rho_o \lambda \rrbracket \rho_i \vartheta \sigma_i]) && \text{[defn } \sigma'_i\text{]} \\
&= \text{restrict}(\rho_o, \lambda, && \text{[template 11.3.3]} \\
&\quad \mathcal{M}_{I^*}[\llbracket \mathcal{O}_E \langle \epsilon \rangle \rho_o \lambda \wedge \langle \text{unaryOp } \psi \rangle \rrbracket \rho_i \vartheta \sigma_i]) \\
&= \text{restrict}(\rho_o, \lambda, && \text{[Aida 10.4.2]} \\
&\quad \mathcal{M}_{I^*}[\llbracket \mathcal{O}_E \langle \epsilon \rangle \rho_o \lambda \rrbracket \rho_i (\mathcal{M}_I[\llbracket \text{unaryOp } \psi \rrbracket \rho_i \vartheta \sigma_i]) \\
&= \text{restrict}(\rho_o, \lambda, && \text{[induction hyp.]} \\
&\quad \mathcal{M}_I[\llbracket \text{unaryOp } \psi \rrbracket \rho_i \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\llbracket \epsilon \rrbracket \rho \sigma\}])) \\
&= \text{restrict}(\rho_o, \lambda, \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_U[\llbracket \psi \rrbracket \mathcal{M}_E[\llbracket \epsilon \rrbracket \rho \sigma\}])) && \text{[Aida 10.4.8]} \\
&= \text{restrict}(\rho_o, \lambda, \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\llbracket \text{unaryExpr}(\psi, \epsilon) \rrbracket \rho \sigma\}])) && \text{[Tosca 8.4.4.4]} \\
&= \text{restrict}(\rho_o, \lambda, \vartheta(\sigma_i \boxplus \{A \mapsto v\})) && \text{[defn } v\text{]}
\end{aligned}$$

□

### 13.6.6 Binary expression

Here  $\epsilon = \text{binExpr}(\epsilon_1, \omega, \epsilon_2)$ . The induction hypothesis is assumed twice, once for each subexpression, with different values of  $\lambda_h$ . So

$$\begin{aligned}
& \text{restrict}(\rho_o, \lambda, \vartheta \sigma_i') \\
&= \text{restrict}(\rho_o, \lambda, \mathcal{M}_{I^*}[\![\mathcal{O}_E\langle \text{binExpr}(\epsilon_1, \omega, \epsilon_2) \rangle \rho_o \lambda]\!] \rho_i \vartheta \sigma_i) \quad [\text{defn } \sigma_i'] \\
&= \text{restrict}(\rho_o, \lambda, \mathcal{M}_{I^*}[\![\mathcal{O}_E\langle \epsilon_2 \rangle \rho_o \lambda \wedge \langle \text{store } \lambda \rangle \wedge \mathcal{O}_E\langle \epsilon_1 \rangle \rho_o(\lambda+1) \wedge \langle \text{binOp}(\omega, \lambda) \rangle]\!] \rho_i \vartheta \sigma_i) \quad [\text{template 11.3.4}] \\
&= \text{restrict}(\rho_o, \lambda, \mathcal{M}_{I^*}[\![\mathcal{O}_E\langle \epsilon_2 \rangle \rho_o \lambda]\!] \rho_i(\mathcal{M}_{I^*}[\![\langle \text{store } \lambda \rangle \wedge \mathcal{O}_E\langle \epsilon_1 \rangle \rho_o(\lambda+1) \wedge \langle \text{binOp}(\omega, \lambda) \rangle]\!] \rho_i \vartheta \sigma_i)) \quad [\text{Aida 10.4.2}] \\
&= \text{restrict}(\rho_o, \lambda, \mathcal{M}_{I^*}[\![\langle \text{store } \lambda \rangle \wedge \mathcal{O}_E\langle \epsilon_1 \rangle \rho_o(\lambda+1) \wedge \langle \text{binOp}(\omega, \lambda) \rangle]\!] \rho_i \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\![\epsilon_2]\!] \rho \sigma\})) \quad [\text{induction hyp.}] \\
&= \text{restrict}(\rho_o, \lambda, \mathcal{M}_{I^*}[\![\mathcal{O}_E\langle \epsilon_1 \rangle \rho_o(\lambda+1)]\!] \rho_i(\mathcal{M}_I[\![\text{binOp}(\omega, \lambda)]\!] \rho_i \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\![\epsilon_2]\!] \rho \sigma, \lambda \mapsto \mathcal{M}_E[\![\epsilon_2]\!] \rho \sigma\}))) \quad [\text{Aida 10.4.7}] \\
&= \text{restrict}(\rho_o, \lambda, \text{restrict}(\rho_o, \lambda+1, \mathcal{M}_{I^*}[\![\mathcal{O}_E\langle \epsilon_1 \rangle \rho_o(\lambda+1)]\!] \rho_i(\mathcal{M}_I[\![\text{binOp}(\omega, \lambda)]\!] \rho_i \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\![\epsilon_2]\!] \rho \sigma, \lambda \mapsto \mathcal{M}_E[\![\epsilon_2]\!] \rho \sigma\})))) \quad [\text{lemma r1}] \\
&= \text{restrict}(\rho_o, \lambda, \text{restrict}(\rho_o, \lambda+1, \mathcal{M}_I[\![\text{binOp}(\omega, \lambda)]\!] \rho_i \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\![\epsilon_1]\!] \rho \sigma, \lambda \mapsto \mathcal{M}_E[\![\epsilon_2]\!] \rho \sigma\}))) \quad [\text{induction hyp.}] \\
&= \text{restrict}(\rho_o, \lambda, \text{restrict}(\rho_o, \lambda+1, \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_B[\![\omega]\!] (\mathcal{M}_E[\![\epsilon_1]\!] \rho \sigma, \mathcal{M}_E[\![\epsilon_2]\!] \rho \sigma), \lambda \mapsto \mathcal{M}_E[\![\epsilon_2]\!] \rho \sigma\}))) \quad [\text{Tosca 10.4.9}] \\
&= \text{restrict}(\rho_o, \lambda, \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_B[\![\omega]\!] (\mathcal{M}_E[\![\epsilon_1]\!] \rho \sigma, \mathcal{M}_E[\![\epsilon_2]\!] \rho \sigma\}))) \quad [\text{lemma r1}] \\
&= \text{restrict}(\rho_o, \lambda, \vartheta(\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\![\text{binExpr}(\epsilon_1, \omega, \epsilon_2)]\!] \rho \sigma\})) \quad [\text{Tosca 8.4.5.4}] \\
&= \text{restrict}(\rho_o, \lambda, \vartheta(\sigma_i \boxplus \{A \mapsto v\})) \quad [\text{defn } v]
\end{aligned}$$

□

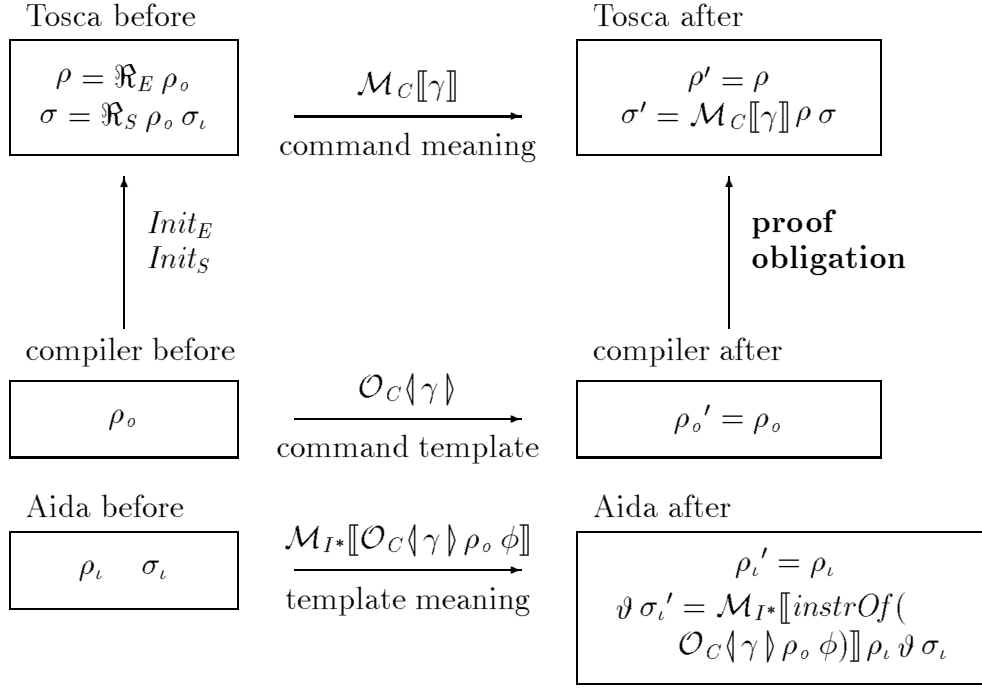


Figure 13.4 The correctness diagram for commands

## 13.7 Commands

### 13.7.1 Correctness condition

Evaluating a Tosca command can change the state, but not the environment. The translation is correct if doing the translation and evaluating the translated fragments make a corresponding change to the translation environment and Aida environment and state. The correctness diagram for commands is shown in Figure 13.4.

The correctness condition is

$$\begin{aligned}
 & Init_E; Init_S; \\
 & \rho' = \rho; \sigma' = \mathcal{M}_C[\gamma]\rho\sigma; \\
 & \rho_o' = \rho_o; \\
 & \rho_i' = \rho_i; \\
 & \vartheta \sigma_i' = \mathcal{M}_{I^*}[\text{instrOf}(\mathcal{O}_C\langle\gamma\rangle\rho_o\phi)]\rho_i\vartheta\sigma_i \\
 & \vdash \sigma' = \mathfrak{R}_S \rho_o \sigma_i'
 \end{aligned}$$

The correctness condition for multiple commands is analogous:

$$\begin{aligned}
& \text{Init}_E; \text{Init}_S; \\
& \rho' = \rho; \sigma' = \mathcal{M}_{C^*}[\Gamma] \rho \sigma; \\
& \rho_o' = \rho_o; \\
& \rho_i' = \rho_i; \\
& \vartheta \sigma_i' = \mathcal{M}_{I^*}[\text{instrOf}(\mathcal{O}_{C^*} \langle \Gamma \rangle \rho_o \phi)] \rho_i \vartheta \sigma_i \\
& \vdash \sigma' = \mathfrak{R}_S \rho_o \sigma_i'
\end{aligned}$$

Notice that, in order to prove  $f(\sigma_{i1}) = f(\sigma_{i2})$ , it is sufficient to prove  $f(\vartheta \sigma_{i1}) = f(\vartheta \sigma_{i2})$  for arbitrary  $\vartheta$ .

### 13.7.2 Induction hypothesis

The induction hypothesis for commands is that any subcommands are translated correctly:

$$\begin{aligned}
\mathfrak{R}_S \rho_o \sigma_i' &= \mathcal{M}_C[\gamma] \rho \sigma \\
&\text{ where } \vartheta_h \sigma_i' == \mathcal{M}_{I^*}[\text{instrOf}(\mathcal{O}_C \langle \gamma \rangle \rho_o \phi)] \rho_i \vartheta_h \sigma_i
\end{aligned}$$

### 13.7.3 Block

The correctness of the **block** instruction follows directly from the correctness of the multiple command translation.

### 13.7.4 Skip

Here  $\gamma = \mathbf{skip}$ . This is a base case, so there is no need for the induction hypothesis.

Lemma:

$$\begin{aligned}
& \vartheta \sigma_i' \\
& = \mathcal{M}_{I^*}[\text{instrOf}(\mathcal{O}_C \langle \mathbf{skip} \rangle \rho_o \phi)] \rho_i \vartheta \sigma_i && \text{[defn } \sigma_i'] \\
& = \mathcal{M}_{I^*}[\langle \rangle] \rho_i \vartheta \sigma_i && \text{[template 11.4.3]} \\
& = \vartheta \sigma_i && \text{[Aida 10.4.2]}
\end{aligned}$$

□

So

$$\begin{aligned}
& \mathfrak{R}_S \rho_o \sigma_i' \\
& = \mathfrak{R}_S \rho_o \sigma_i && \text{[lemma]} \\
& = \sigma && \text{[defn } \sigma] \\
& = \mathcal{M}_C[\mathbf{skip}] \rho \sigma && \text{[Tosca 8.5.4.4]} \\
& = \sigma' && \text{[defn } \sigma']
\end{aligned}$$

□

### 13.7.5 Assignment

Here  $\gamma = \mathbf{assign}(\xi, \epsilon)$ . The expression induction hypothesis is assumed for the expression component.

Lemma:

$$\begin{aligned}
& \mathit{restrict}(\rho_o, \mathit{top}, \vartheta \sigma_i') \\
&= \mathit{restrict}(\rho_o, \mathit{top}, \mathcal{M}_{I^*}[\mathit{instrOf}(\mathcal{O}_C \langle \mathbf{assign}(\xi, \epsilon) \rangle \rho_o \phi)] \rho_i \vartheta \sigma_i) && \text{[defn } \sigma_i'] \\
&= \mathit{restrict}(\rho_o, \mathit{top}, \mathcal{M}_{I^*}[\mathcal{O}_E \langle \epsilon \rangle \rho_o \mathit{top} \hat{\ } \langle \mathbf{store} \rho_o \llbracket \xi \rrbracket \rangle] \rho_i \vartheta \sigma_i) && \text{[template 11.4.4]} \\
&= \mathit{restrict}(\rho_o, \mathit{top}, \mathcal{M}_{I^*}[\mathcal{O}_E \langle \epsilon \rangle \rho_o \mathit{top}] \rho_i (\mathcal{M}_I[\mathbf{store} \rho_o \llbracket \xi \rrbracket] \rho_i \vartheta) \sigma_i) && \text{[Aida 10.4.2]} \\
&= \mathit{restrict}(\rho_o, \mathit{top}, \mathcal{M}_I[\mathbf{store} \rho_o \llbracket \xi \rrbracket] \rho_i \vartheta (\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\epsilon] \rho \sigma\})) && \text{[induction hyp.]} \\
&= \mathit{restrict}(\rho_o, \mathit{top}, \vartheta (\sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\epsilon] \rho \sigma, \rho_o \llbracket \xi \rrbracket \mapsto \mathcal{M}_E[\epsilon] \rho \sigma\})) && \text{[Aida 10.4.7]}
\end{aligned}$$

□

So

$$\begin{aligned}
& \mathfrak{R}_S \rho_o \sigma_i' \\
&= \mathfrak{R}_S \rho_o \mathit{restrict}(\rho_o, \mathit{top}, \sigma_i') && \text{[lemma } \mathbf{r2}] \\
&= \mathfrak{R}_S \rho_o \mathit{restrict}(\rho_o, \mathit{top}, \sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\epsilon] \rho \sigma, \rho_o \llbracket \xi \rrbracket \mapsto \mathcal{M}_E[\epsilon] \rho \sigma\}) && \text{[lemma]} \\
&= \mathfrak{R}_S \rho_o (\sigma_i \boxplus \{\rho_o \llbracket \xi \rrbracket \mapsto \mathcal{M}_E[\epsilon] \rho \sigma\}) && \text{[defn } \mathit{restrict}] \\
&= \sigma \boxplus \{\rho_o \llbracket \xi \rrbracket \mapsto \mathcal{M}_E[\epsilon] \rho \sigma\} && \text{[defn } \mathfrak{R}_S, \sigma] \\
&= \sigma \boxplus \{\rho \llbracket \xi \rrbracket \mapsto \mathcal{M}_E[\epsilon] \rho \sigma\} && \text{[defn } \rho] \\
&= \mathcal{M}_C[\mathbf{assign}(\xi, \epsilon)] \rho \sigma && \text{[Tosca 8.5.5.4]} \\
&= \sigma' && \text{[defn } \sigma']
\end{aligned}$$

□

### 13.7.6 Choice

Here  $\gamma = \text{choice}(\epsilon, \gamma_1, \gamma_2)$ . The expression induction hypothesis is assumed for the expression component, and the command induction hypothesis is assumed twice, once for each branch subcommand.

Lemma:

$$\begin{aligned}
& \text{restrict}(\rho_o, \text{top}, \vartheta \sigma'_i) \\
&= \text{restrict}(\rho_o, \text{top}, \mathcal{M}_{I^*}[\![\text{instrOf}(\mathcal{O}_C \langle \text{choice}(\epsilon, \gamma_1, \gamma_2) \rangle \rho_o \phi)\!] \rho_i \vartheta \sigma_i]) \quad [\text{defn } \sigma'_i] \\
&= \text{restrict}(\rho_o, \text{top}, \mathcal{M}_{I^*}[\![\mathcal{O}_E \langle \epsilon \rangle \rho_o \text{top} \hat{\wedge} \langle \text{jump } \phi_2 \rangle \hat{\wedge} \text{instrOf}(\mathcal{O}_C \langle \gamma_1 \rangle \rho_o \phi) \\
&\quad \hat{\wedge} \langle \text{goto}(1 + \phi_2), \text{label } \phi_2 \rangle \hat{\wedge} \text{instrOf}(\mathcal{O}_C \langle \gamma_2 \rangle \rho_o \phi_1) \\
&\quad \hat{\wedge} \langle \text{label}(1 + \phi_2) \rangle]\!] \rho_i \vartheta \sigma_i) \quad [\text{template 11.4.5}] \\
&= \text{restrict}(\rho_o, \text{top}, \mathcal{M}_{I^*}[\![\mathcal{O}_E \langle \epsilon \rangle \rho_o \text{top}]\!] \rho_i \quad [\text{Aida 10.4.2}] \\
&\quad (\mathcal{M}_{I^*}[\![\langle \text{jump } \phi_2 \rangle \hat{\wedge} \text{instrOf}(\mathcal{O}_C \langle \gamma_1 \rangle \rho_o \phi) \\
&\quad \hat{\wedge} \langle \text{goto}(1 + \phi_2), \text{label } \phi_2 \rangle \hat{\wedge} \text{instrOf}(\mathcal{O}_C \langle \gamma_2 \rangle \rho_o \phi_1) \\
&\quad \hat{\wedge} \langle \text{label}(1 + \phi_2) \rangle]\!] \rho_i \vartheta \sigma_i) \\
&= \text{restrict}(\rho_o, \text{top}, \mathcal{M}_{I^*}[\![\langle \text{jump } \phi_2 \rangle \hat{\wedge} \text{instrOf}(\mathcal{O}_C \langle \gamma_1 \rangle \rho_o \phi) \hat{\wedge} \langle \text{goto}(1 + \phi_2), \text{label } \phi_2 \rangle \\
&\quad \hat{\wedge} \text{instrOf}(\mathcal{O}_C \langle \gamma_2 \rangle \rho_o \phi_1) \hat{\wedge} \langle \text{label}(1 + \phi_2) \rangle]\!] \rho_i \vartheta \sigma_{iA}) \quad [\text{induction hyp.}] \\
&\quad \text{where } \sigma_{iA} == \sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\![\epsilon]\!] \rho \sigma\} \\
&= \text{restrict}(\rho_o, \text{top}, \text{if } \mathcal{M}_E[\![\epsilon]\!] \rho \sigma = \text{bool}_v \text{ F} \quad [\text{Aida 10.4.4}] \\
&\quad \text{then } \rho_i[\![\phi_2]\!] \sigma_{iA} \\
&\quad \text{else } \mathcal{M}_{I^*}[\![\text{instrOf}(\mathcal{O}_C \langle \gamma_1 \rangle \rho_o \phi) \hat{\wedge} \langle \text{goto}(1 + \phi_2), \text{label } \phi_2 \rangle \\
&\quad \hat{\wedge} \text{instrOf}(\mathcal{O}_C \langle \gamma_2 \rangle \rho_o \phi_1) \hat{\wedge} \langle \text{label}(1 + \phi_2) \rangle]\!] \rho_i \vartheta \sigma_{iA}) \\
&= \text{restrict}(\rho_o, \text{top}, \text{if } \mathcal{M}_E[\![\epsilon]\!] \rho \sigma = \text{bool}_v \text{ T} \quad [\text{Aida 10.4.12}] \\
&\quad \text{then } \mathcal{M}_{I^*}[\![\text{instrOf}(\mathcal{O}_C \langle \gamma_1 \rangle \rho_o \phi)]\!] \rho_i (\mathcal{M}_{I^*}[\![\langle \text{goto}(1 + \phi_2), \text{label } \phi_2 \rangle \\
&\quad \hat{\wedge} \text{instrOf}(\mathcal{O}_C \langle \gamma_2 \rangle \rho_o \phi_1) \hat{\wedge} \langle \text{label}(1 + \phi_2) \rangle]\!] \rho_i \vartheta \sigma_{iA}) \\
&\quad \text{else } \mathcal{M}_{I^*}[\![\text{instrOf}(\mathcal{O}_C \langle \gamma_2 \rangle \rho_o \phi_1)]\!] \rho_i \\
&\quad (\mathcal{M}_{I^*}[\![\langle \text{label}(1 + \phi_2) \rangle]\!] \rho_i \vartheta \sigma_{iA})
\end{aligned}$$

$$\begin{aligned}
&= \text{restrict}(\rho_o, \text{top}, && \text{[induction hyp.]} \\
&\quad \text{if } \mathcal{M}_E[\epsilon] \rho \sigma = \text{bool}_v \top \\
&\quad \text{then } \mathcal{M}_{I^*}[\langle \text{goto}(1 + \phi_2), \text{label } \phi_2 \rangle \hat{\ } \text{instrOf}(\mathcal{O}_C \langle \gamma_2 \rangle \rho_o \phi_1) \\
&\quad \quad \hat{\ } \langle \text{label}(1 + \phi_2) \rangle] \rho_i \vartheta \sigma_{i1} \\
&\quad \text{else } \mathcal{M}_I[\text{label}(1 + \phi_2)] \rho_i \vartheta \sigma_{i2} \\
&\quad \text{where } \mathfrak{R}_S \rho_o \sigma_{ik} == \mathcal{M}_C[\gamma_k] \rho \sigma \\
&= \text{restrict}(\rho_o, \text{top}, && \text{[Aida 10.4.3]} \\
&\quad \text{if } \mathcal{M}_E[\epsilon] \rho \sigma = \text{bool}_v \top \\
&\quad \text{then } \rho_i[1 + \phi_2] \sigma_{i1} \\
&\quad \text{else } \mathcal{M}_I[\text{label}(1 + \phi_2)] \rho_i \vartheta \sigma_{i2} \\
&= \text{restrict}(\rho_o, \text{top}, && \text{[Aida 10.4.12]} \\
&\quad \text{if } \mathcal{M}_E[\epsilon] \rho \sigma = \text{bool}_v \top \text{ then } \vartheta \sigma_{i1} \text{ else } \vartheta \sigma_{i2} )
\end{aligned}$$

□

So

$$\begin{aligned}
&\mathfrak{R}_S \rho_o \sigma'_i \\
&= \mathfrak{R}_S \rho_o \text{ restrict}(\rho_o, \text{top}, \sigma'_i) && \text{[lemma r2]} \\
&= \mathfrak{R}_S \rho_o \text{ restrict}(\rho_o, \text{top}, && \text{[lemma]} \\
&\quad \text{if } \mathcal{M}_E[\epsilon] \rho \sigma = \text{bool}_v \top \text{ then } \sigma_{i1} \text{ else } \sigma_{i2}) \\
&= \text{if } \mathcal{M}_E[\epsilon] \rho \sigma = \text{bool}_v \top \text{ then } \mathfrak{R}_S \rho_o \sigma_{i1} \text{ else } \mathfrak{R}_S \rho_o \sigma_{i2} && \text{[lemma r2]} \\
&= \text{if } \mathcal{M}_E[\epsilon] \rho \sigma = \text{bool}_v \top && \text{[defn } \sigma_{i1} \sigma_{i2}] \\
&\quad \text{then } \mathcal{M}_C[\gamma_1] \rho \sigma \text{ else } \mathcal{M}_C[\gamma_2] \rho \sigma \\
&= \mathcal{M}_C[\text{choice}(\epsilon, \gamma_1, \gamma_2)] \rho \sigma && \text{[Tosca choice 8.5.6.4]} \\
&= \sigma' && \text{[defn } \sigma']
\end{aligned}$$

□

### 13.7.7 Loop

Here  $\gamma = \text{loop}(\epsilon, \gamma)$ . The expression induction hypothesis is assumed for the expression component, and the command induction hypothesis is assumed for the body subcommand.



Lemma:

$$\begin{aligned}
& \text{restrict}(\rho_o, \text{top}, \vartheta \sigma_i') \\
&= \text{restrict}(\rho_o, \text{top}, \mathcal{M}_{I^*}[\text{instrOf}(\mathcal{O}_C \langle \text{loop}(\epsilon, \gamma) \rangle \rho_o \phi)] \rho_i \vartheta \sigma_i) \quad [\text{defn } \sigma_i'] \\
&= \text{restrict}(\rho_o, \text{top}, \mathcal{M}_{I^*}[\langle \text{label } \phi_1 \rangle \wedge \mathcal{O}_E \langle \epsilon \rangle \rho_o \text{ top} \\
&\quad \wedge \langle \text{jump}(1 + \phi_1) \rangle \wedge \text{instrOf}(\mathcal{O}_C \langle \gamma \rangle \rho_o \phi) \\
&\quad \wedge \langle \text{goto } \phi_1, \text{label}(1 + \phi_1) \rangle] \rho_i \vartheta \sigma_i) \quad [\text{template 11.4.6}] \\
&= \text{restrict}(\rho_o, \text{top}, \mathcal{M}_{I^*}[\mathcal{O}_E \langle \epsilon \rangle \rho_o \text{ top}] \rho_i \vartheta \sigma_i) \quad [\text{Aida 10.4.12}] \\
&\quad (\mathcal{M}_{I^*}[\langle \text{jump}(1 + \phi_1) \rangle \wedge \text{instrOf}(\mathcal{O}_C \langle \gamma \rangle \rho_o \phi) \\
&\quad \wedge \langle \text{goto } \phi_1, \text{label}(1 + \phi_1) \rangle] \rho_i \vartheta \sigma_i) \\
&= \text{restrict}(\rho_o, \text{top}, \mathcal{M}_{I^*}[\langle \text{jump}(1 + \phi_1) \rangle \wedge \text{instrOf}(\mathcal{O}_C \langle \gamma \rangle \rho_o \phi) \\
&\quad \wedge \langle \text{goto } \phi_1, \text{label}(1 + \phi_1) \rangle] \rho_i \vartheta \sigma_{iA}) \quad [\text{induction hyp.}] \\
&\quad \text{where } \sigma_{iA} == \sigma_i \boxplus \{A \mapsto \mathcal{M}_E[\epsilon] \rho \sigma\} \\
&= \text{restrict}(\rho_o, \text{top}, \text{if } \mathcal{M}_E[\epsilon] \rho \sigma = \text{bool}_v \text{ F} \\
&\quad \text{then } \rho_i[1 + \phi_1] \sigma_{iA} \\
&\quad \text{else } \mathcal{M}_{I^*}[\text{instrOf}(\mathcal{O}_C \langle \gamma \rangle \rho_o \phi) \\
&\quad \wedge \langle \text{goto } \phi_1, \text{label}(1 + \phi_1) \rangle] \rho_i \vartheta \sigma_{iA}) \quad [\text{Aida 10.4.4}] \\
&= \text{restrict}(\rho_o, \text{top}, \text{if } \mathcal{M}_E[\epsilon] \rho \sigma = \text{bool}_v \text{ T} \\
&\quad \text{then } \mathcal{M}_{I^*}[\text{instrOf}(\mathcal{O}_C \langle \gamma \rangle \rho_o \phi)] \rho_o \\
&\quad (\mathcal{M}_{I^*}[\langle \text{goto } \phi_1, \text{label}(1 + \phi_1) \rangle] \rho_i \vartheta) \sigma_{iA} \\
&\quad \text{else } \vartheta \sigma_{iA}) \quad [\text{Aida 10.4.12}] \\
&= \text{restrict}(\rho_o, \text{top}, \text{if } \mathcal{M}_E[\epsilon] \rho \sigma = \text{bool}_v \text{ T} \\
&\quad \text{then } \mathcal{M}_{I^*}[\langle \text{goto } \phi_1, \text{label}(1 + \phi_1) \rangle] \rho_i \vartheta \sigma_{i1} \\
&\quad \text{else } \vartheta \sigma_{iA}) \quad [\text{induction hyp.}] \\
&\quad \text{where } \mathfrak{R}_S \rho_o \sigma_{i1} == \mathcal{M}_C[\gamma] \rho \sigma
\end{aligned}$$

$$\begin{aligned}
&= \mathit{restrict}(\rho_o, \mathit{top}, && \text{[Aida 10.4.3]} \\
&\quad \mathbf{if} \ \mathcal{M}_E[\epsilon] \rho \sigma = \mathit{bool}_v \top \\
&\quad \mathbf{then} \ \mathcal{M}_{I^*}[\mathit{instrOf}(\mathcal{O}_C \langle \mathit{loop}(\epsilon, \gamma) \rangle \rho_o \phi)] \rho_i \vartheta \sigma_{i1} \\
&\quad \mathbf{else} \ \vartheta \sigma_{iA})
\end{aligned}$$

□

So

$$\begin{aligned}
&\mathfrak{R}_S \rho_o \sigma'_i \\
&= \mathfrak{R}_S \rho_o \mathit{restrict}(\rho_o, \mathit{top}, \sigma_i) && \text{[lemma r2]} \\
&= \mathfrak{R}_S \rho_o \mathit{restrict}(\rho_o, \mathit{top}, && \text{[lemma]} \\
&\quad \mathbf{if} \ \mathcal{M}_E[\epsilon] \rho \sigma = \mathit{bool}_v \top \\
&\quad \mathbf{then} \ \mathcal{M}_{I^*}[\mathit{instrOf}(\mathcal{O}_C \langle \mathit{loop}(\epsilon, \gamma) \rangle \rho_o \phi)] \rho_i \sigma_{i1} \\
&\quad \mathbf{else} \ \sigma_{iA}) \\
&= \mathbf{if} \ \mathcal{M}_E[\epsilon] \rho \sigma = \mathit{bool}_v \top && \text{[defn } \sigma_{i1}, \sigma_{iA}] \\
&\quad \mathbf{then} \ \mathcal{M}_C[\mathit{loop}(\epsilon, \gamma)] \rho (\mathcal{M}_C[\gamma] \rho \sigma) \\
&\quad \mathbf{else} \ \sigma \\
&= \mathcal{M}_C[\mathit{loop}(\epsilon, \gamma)] \rho \sigma && \text{[Tosca 8.5.7.4]} \\
&= \sigma' && \text{[defn } \sigma']
\end{aligned}$$

□

### 13.7.8 Input

Here  $\gamma = \mathit{input}(\xi)$ . This is a base case, so no induction hypothesis is needed.

Lemma:

$$\begin{aligned}
&\vartheta \sigma'_i \\
&= \mathcal{M}_{I^*}[\mathit{instrOf}(\mathcal{O}_C \langle \mathit{input} \ \xi \rangle \rho_o \phi)] \rho_i \vartheta(\varsigma_i, \langle v \rangle \hat{\ } in, out) && \text{[defn } \sigma'_i] \\
&= \mathcal{M}_{I^*}[\langle \mathit{input}, \mathit{store} \rho_o[\xi] \rangle] \rho_i \vartheta(\varsigma_i, \langle v \rangle \hat{\ } in, out) && \text{[template]} \\
&= \mathcal{M}_I[\mathit{store} \rho_o[\xi]] \rho_i \vartheta(\varsigma_i \oplus \{A \mapsto v\}, in, out) && \text{[Aida 10.4.10]} \\
&= \vartheta(\varsigma_i \oplus \{A \mapsto v, \rho_o[\xi] \mapsto v\}, in, out) && \text{[Aida 10.4.7]}
\end{aligned}$$

□

So

$$\begin{aligned}
& \mathfrak{R}_S \rho_o \sigma'_i \\
&= \mathfrak{R}_S \rho_o (\varsigma_i \oplus \{A \mapsto v, \rho_o \llbracket \xi \rrbracket \mapsto v\}, in, out) && \text{[lemma]} \\
&= (\varsigma \oplus \{\rho \llbracket \xi \rrbracket \mapsto v\}, in, out) && \text{[defn } \mathfrak{R}_S, \sigma \text{]} \\
&= \mathcal{M}_C \llbracket \text{input } \xi \rrbracket \rho (\varsigma, \langle v \rangle \hat{\ } in, out) && \text{[Tosca 8.5.8.4]} \\
&= \sigma' && \text{[defn } \sigma' \text{]}
\end{aligned}$$

□

### 13.7.9 Output

Here  $\gamma = \text{output}(\epsilon)$ . The expression induction hypothesis is assumed for the expression component.

Lemma:

$$\begin{aligned}
& \text{restrict}(\rho_o, top, \vartheta \sigma'_i) \\
&= \text{restrict}(\rho_o, top, && \text{[defn } \sigma'_i \text{]} \\
&\quad \mathcal{M}_{I^*} \llbracket \text{instrOf}(\mathcal{O}_C \langle \text{output } \epsilon \rangle \rho_o \phi) \rrbracket \rho_i \vartheta(\varsigma_i, in, out)) \\
&= \text{restrict}(\rho_o, top, && \text{[template]} \\
&\quad \mathcal{M}_{I^*} \llbracket \mathcal{O}_E \langle \epsilon \rangle \rho_o \ top \hat{\ } \langle \text{output} \rangle \rrbracket \rho_i \vartheta(\varsigma_i, in, out)) \\
&= \text{restrict}(\rho_o, top, && \text{[induction hyp.]} \\
&\quad \mathcal{M}_I \llbracket \text{output} \rrbracket \rho_i \vartheta(\varsigma_i \oplus \{A \mapsto \mathcal{M}_E \llbracket \epsilon \rrbracket \rho \sigma\}, in, out)) \\
&= \text{restrict}(\rho_o, top, && \text{[Aida 10.4.11]} \\
&\quad \vartheta(\varsigma_i \oplus \{A \mapsto \mathcal{M}_E \llbracket \epsilon \rrbracket \rho \sigma\}, in, out \hat{\ } \langle \mathcal{M}_E \llbracket \epsilon \rrbracket \rho \sigma \rangle))
\end{aligned}$$

□

So

$$\begin{aligned}
& \mathfrak{R}_S \rho_o \sigma'_i \\
&= \mathfrak{R}_S \rho_o \text{restrict}(\rho_o, top, (\varsigma_i, in, out)) && \text{[lemma r2]} \\
&= \mathfrak{R}_S \rho_o \text{restrict}(\rho_o, top, && \text{[lemma]} \\
&\quad (\varsigma_i \oplus \{A \mapsto \mathcal{M}_E \llbracket \epsilon \rrbracket \rho \sigma\}, in, out \hat{\ } \langle \mathcal{M}_E \llbracket \epsilon \rrbracket \rho \sigma \rangle)) \\
&= \mathfrak{R}_S \rho_o (\varsigma_i, in, out \hat{\ } \langle \mathcal{M}_E \llbracket \epsilon \rrbracket \rho \sigma \rangle) && \text{[defn. } \text{restrict} \text{]} \\
&= (\varsigma, in, out \hat{\ } \langle \mathcal{M}_E \llbracket \epsilon \rrbracket \rho \sigma \rangle) && \text{[defn } \mathfrak{R}_S, \sigma \text{]} \\
&= \mathcal{M}_C \llbracket \text{output } \epsilon \rrbracket \rho \sigma && \text{[Tosca 8.5.9.4]} \\
&= \sigma' && \text{[defn } \sigma' \text{]}
\end{aligned}$$

□

### 13.7.10 Multiple commands

The definition of the translation for multiple commands follows the form of the definition of the meaning of multiple commands. Given the correctness of the single commands translation, and the unique allocation of labels, the multiple commands correctness follows directly.

## 13.8 Program

Given the correctness of the declaration translation and of the command translation, the Tosca program translation correctness follows directly.

# The Prolog Implementation

## 14.1 Necessary components

The Prolog implementation of the semantics specified so far needs several components over and above the translation of the various semantics into a DCTG. It also needs support for the DCTG operators themselves, a parser to convert Tosca's concrete syntax strings to an abstract syntax tree, and support for sets and set operations. These extra components are discussed below, and then the translation of the semantics is given.

## 14.2 Supporting constructs

### 14.2.1 DCTG support

Although standard Prologs have support built in for DCGs, they do not have such support for DCTGs. It is necessary to provide explicit support for converting a Prolog program written in DCTG form to ordinary Prolog clauses. [Abramson and Dahl 1989, Appendix II.3] gives a listing of such a Prolog interpreter for DCTGs.

### 14.2.2 Lexing and parsing

Prolog predicates to support lexing and parsing the input concrete syntax have to be written. Lexing breaks the input stream of individual characters into a stream of *tokens*: keywords, identifiers and operators; parsing builds the tokens into a tree structure as defined by the abstract syntax.

Prolog's pattern matching capabilities come into their own here, and a highly declarative definition of tokens, keywords and the structure of identifiers can be given. [Abramson and Dahl 1989, Chapter 9] includes the parser for a simple

DCTG. The compiler's lexing phase can be written to correspond closely with the concrete syntax definition of identifiers and keywords (not defined for Tosca).

### 14.2.3 Sets and set operations

The standard way to represent sets and tuples in Prolog is by using lists.  $Z$  functions are simply sets of pairs, and can be represented as lists of 2-element lists. So, for example, an environment such as

$$\{x \mapsto \text{integer}, b \mapsto \text{boolean}\}$$

can be represented in Prolog as

$$[ [x, \text{int}], [b, \text{bool}] ]$$

Sets of other compound elements, for example, the various *States*, can also be represented as lists of lists.

Many implementations of Prolog provide library support for set operations such as membership test, union and intersection. If not, these are quite simple to write. Special definitions do have to be written to support the more  $Z$ -specific operations such as function application (looking up a value), function overriding  $\oplus$ , and domain restriction  $\triangleleft$ . More definitions are needed to support those functions written specially for the semantics specification, such as *worseState*. These definitions are quite straightforward, and are not given below.

## 14.3 Translating the semantics

### 14.3.1 The approach

Translating the semantic meaning functions and the operational semantics needs to be done systematically, in order to provide the clearly visible path from the mathematics to the implementation.

Consider for the moment the dynamic meaning of the binary expression (section 8.4.5.4):

$$\left| \begin{array}{l} \mathcal{M}_E[-] : \text{EXPR} \rightarrow \text{Env} \rightarrow \text{State} \rightarrow \text{VALUE} \\ \hline \mathcal{M}_E[\text{binExpr}(\epsilon_1, \omega, \epsilon_2)] \rho \sigma = \\ \mathcal{M}_B[\omega](\mathcal{M}_E[\epsilon_1] \rho \sigma, \mathcal{M}_E[\epsilon_2] \rho \sigma) \end{array} \right.$$

This could be rewritten without the nested function calls as

$$\begin{array}{|l}
\mathcal{M}_E[-] : \text{EXPR} \mapsto \text{Env} \mapsto \text{State} \mapsto \text{VALUE} \\
\hline
\exists v_1, v_2 : \text{VALUE} \mid \\
\quad v_1 = \mathcal{M}_E[\epsilon_1] \rho \sigma \\
\quad \wedge v_2 = \mathcal{M}_E[\epsilon_2] \rho \sigma \bullet \\
\mathcal{M}_E[\text{binExpr}(\epsilon_1, \omega, \epsilon_2)] \rho \sigma = \mathcal{M}_B[\omega](v_1, v_2)
\end{array}$$

This is translated into a Prolog DCTG as

```

expr ::= tLPAREN, expr^^E1, tBINOP^^O, expr^^E2, tRPAREN
<:>
(meaning(Env, State, Value) :-
    E1^^meaning(Env, State, Value1),
    E2^^meaning(Env, State, Value2),
    O^^meaning(Value1, Value2, Value)
).

```

The argument corresponding to the *EXPR* appears in the syntax definition part of the Prolog, before the `<:>`. Each of the other arguments, and the resulting value, are supplied as arguments to the `meaning` goal, in the same order as in the *Z* specification (for clarity). Its meaning (the resulting `Value`) is given in terms of the meaning of the operator when supplied with two arguments that are the meanings of the two subexpressions. These have to be pulled out as separate statements in the Prolog.

In the *Z* specification, the different dynamic meaning functions—for declarations, operators, expressions, and commands—have to be given different names, because they have different types. Prolog is an untyped language, so all the dynamic meaning clauses can have the same name, `meaning`, even though they may take different numbers of arguments. Prolog’s pattern matching ensures the correct clauses are used.

These examples show the general form of the translation process.

1. The syntax part of the DCTG, before the `<:>`, follows the concrete syntax specification.
2. The arguments to the head of the Prolog statement (the part before the `:-`) correspond to the semantic arguments (state and environment as appropriate) and result of the meaning function. They are written in the same order in the Prolog as in the *Z*, for clarity.
3. The body of the Prolog statement (the part after the `:-`) corresponds to the specification of the meaning function. Nested function calls may have to be pulled out into separate statements.

### 14.3.2 Tosca’s semantics as a DCTG

The following shows a summary of Tosca’s DCTG. It has all five semantics attached to each node: the three non-standard static semantics (declaration-before-

use checking `declcheck`, type checking `typecheck` and initialization-before-use checking `usecheck`), the dynamic semantics `meaning` and the operational semantics `code`. These provide the various static checkers, an interpreter, and a compiler, respectively.

In the example below, the dynamic state includes only the store component, and not the lists of input and output values. These latter components are represented during interpretation by prompting the user for keyboard input, and by writing the output to the screen, respectively. Hence, the dynamic store and state are identified in the Prolog version:

```
compile(Source) :-
    lexemes(Source, Tokens),
    tosca(Tree, Tokens, [ ]),
    Tree^^declcheck(DCheck),
    (   DCheck = checkWrong,
        fatalError(['declaration(s) check wrong'])
    ;   DCheck = checkOK ),
    Tree^^typecheck(TCheck),
    (   TCheck = checkWrong,
        fatalError(['type(s) check wrong'])
    ;   TCheck = checkOK ),
    Tree^^usecheck(UCheck),
    (   UCheck = checkWrong,
        fatalError(['use(s) check wrong'])
    ;   UCheck = checkOK ),
    Tree^^meaning,
    Tree^^code(InstrList), formatcode(InstrList).
```

The `lexemes` goal splits the input source code, a stream of characters, into a list of `Tokens` (the definition of `lexemes` is not given here). The `tosca` goal parses these tokens into the abstract syntax `Tree`, by matching against the concrete syntax part of the DCTG. The definition of `tosca` in DCTG form is given in the next section; the one used here corresponds to the form after translation into plain Prolog (see section 3.3 for an example of the translation, which explains the form of the arguments to the `tosca` goal).

The three static checks are done by executing the `declcheck`, `typecheck` and `usecheck` semantics: the program stops with an error message if any of these semantics returns a `checkWrong` value. The interpreter runs by executing the `meaning` semantics, then the Aida abstract assembly code is obtained by executing the `code` semantics, then printed out in an appropriate form using `formatcode`. In practice, one or other of these last executions is commented out to provide either an interpreter or a compiler.

A full compiler also needs to report errors rather more informatively than Prolog's rather terse `no`. Extra goals are inserted into the relevant parts of the code to print out helpful diagnostics, for example, by formatting the various static states in a manner that shows which variables have been used improperly. However, these



have not been included in the Prolog given here, since they tend to clutter the code.

### 14.3.3 Program

A Tosca program is a list of declarations, and a command. For each semantics, the meaning of the declaration list is evaluated in an initially empty environment (modelled by the empty list `[]`) to produce the relevant declaration environment. The meaning of the command is executed in this environment, with the appropriate state:

```
tosca ::= declList^^DL, tENDDECL, cmd^^C
<:>
  (declcheck(DCheck) :-
    DL^^declcheck([ ], DEnv),
    C^^declcheck(DEnv, DCheck)
  ),
  (typecheck(TCheck) :-
    DL^^typecheck([ ], TEnv),
    C^^typecheck(TEnv, TCheck)
  ),
  (usecheck(UCheck) :-
    DL^^usecheck([ ], UEnv),
    C^^usecheck(UEnv, [[ ], checkOK], [_ , UCheck])
  ),
  (meaning :-
    DL^^meaning([ ], Env),
    C^^meaning(Env, [ ], _)
  ),
  (code(InstrList) :-
    DL^^code([ ], OEnv),
    olocn(OLocn), assert(top(OLocn)),
    C^^code(OEnv, 0, _, InstrList)
  ).
```

The various check results (`DCheck`, `TCheck` and `UCheck`) are used in the `compiler` goal to check for static errors, and to stop if any are found. They all have to have different names, as do the different environments and states, otherwise Prolog would attempt to unify them: to find a solution where they all have the same value. Such a solution is unlikely to exist.

The result of a use check is a use state: a pair, modelled as a two-component list, consisting of the use store and a check value. The use check status of a program depends only on the final check value, not the final use store. So the final use store argument in the `usecheck` goal is an ‘anonymous variable’, written as an underscore, indicating that its value is not used elsewhere.

Similarly, the final state from the dynamic `meaning` semantics is not needed; the dynamic meaning of a Tosca program is defined to be its output stream, irrespective of its final state. So this final state is indicated by an anonymous variable, too. Fortunately, Prolog does not attempt to unify anonymous variables; they can happily have different values.

The result from the `code` semantics is `InstrList`, a list of Aida instructions. This is printed out by the `compile` goal. The anonymous variable in the final `code` goal corresponds to the final ‘next label’ value, which is not needed. The value of `top` is asserted, so that it can be used when allocated temporary locations.

#### 14.3.4 Declarations

A declaration declares a variable name to be of a particular type:

```
decl ::= tIDENT^^I, tCOLON, tTYPE^^T
<:>
  (declcheck(PreDEnv, PostDEnv) :-
    I^^meaning(Id),
    override(PreDEnv, Id, checkOK, PostDEnv)
  ),
  (typecheck(PreTEnv, PostTEnv) :-
    I^^meaning(Id),
    T^^meaning(Type),
    override(PreTEnv, Id, Type, PostTEnv)
  ),
  (usecheck(PreUEnv, PostUEnv) :-
    I^^meaning(Id),
    gensym(uloc, ULocn),
    override(PreUEnv, Id, ULocn, PostUEnv)
  ),
  (meaning(PreEnv, PostEnv) :-
    I^^meaning(Id),
    gensym(loc, Locn),
    override(PreEnv, Id, Locn, PostEnv)
  ),
  (code(PreOEnv, PostOEnv) :-
    I^^meaning(Id),
    gensym(oloc, OLocn),
    override(PreOEnv, Id, OLocn, PostOEnv)
  ).
```

The `meaning` of a name is simply the name itself. Similarly, the `meaning` of a type is (the name of) the type.

The predicate `override(Fun, X, Y, Fun1)` has to be specially written to implement the  $Z$  override,  $\oplus$ . Rather than taking a set of pairs, as in  $Z$ , the Prolog

version takes a single pair as two separate arguments. So it succeeds if

$$\text{Fun} \oplus \{X \mapsto Y\} = \text{Fun1}$$

and uses the convention that the result is written as the last argument to the Prolog predicate.

Unique locations are generated using Prolog's built-in `gensym` predicate. Repeated use of `gensym(str, X)` gives `X` the value `str1`, `str2`, and so on.

### 14.3.5 Operators

#### 14.3.5.1 Unary operators

```
tUNYOP ::= [unyop(tMINUS)]
<:>
  (typecheck(Type1, Type) :-
    (   Type1 = int, Type = int
      ;   Type1 \= int, Type = typeWrong)),
  (meaning(X, Value) :- Value = - X),
  (code(negate))).
```

Prolog's semicolon denotes the disjunction ('or-ing') of goals. So the type checking semantics reads 'Type1 is int and Type is int, or Type1 is not int, and Type is typeWrong'.

Note that there are no declaration or use checking semantics defined for operators:

```
tUNYOP ::= [unyop(tNOT)]
<:>
  (typecheck(Type1, Type) :-
    (   Type1 = bool, Type = bool
      ;   Type \= bool, Type = typeWrong)),
  (meaning(X, Value) :-
    (   X = bTRUE, Value = bFALSE
      ;   X = bFALSE, Value = bTRUE)),
  (code(not))).
```

#### 14.3.5.2 Binary operators

Only one in each group of binary operators (arithmetic, comparison, logical) is shown below, the others are similar. Tosca's arithmetic and comparison operators can be implemented by using Prolog's built-in operators:

```
tBINOP ::= [binop('+')]
<:>
  (typecheck(Type1, Type2, Type) :-
    (   Type1 = int, Type2 = int, Type = int
      ;   Type = typeWrong)),
```

```
(meaning(X, Y, Value) :- Value is X + Y),
(code(add)).
```

Note that the type checking definition depends on the order in which goals are evaluated in Prolog: the goal written first in a disjunction is evaluated first, and so `Type` is set to `typeWrong` only if the first goal fails. If the order were not determined, if the two goals in the disjunction could be evaluated in either order, it would have to be written as

```
Type1 = int, Type2 = int, Type = int
; (Type1 \= int ; Type2 \= int), Type = typeWrong
```

The original version is clearer and more closely related to the corresponding Z specification:

$$\mathcal{T}_B[\text{binArithOp } \omega_\alpha] =$$

**if**  $\tau_1 = \text{integer} \wedge \tau_2 = \text{integer}$  **then** `integer` **else** *typeWrong*

The second Prolog version would be a better translation if the Z specification had been written, less clearly, as

$$(\tau_1 = \text{integer} \wedge \tau_2 = \text{integer} \wedge \mathcal{T}_B[\text{binArithOp } \omega_\alpha] = \text{integer})$$

$$\vee ((\tau_1 \neq \text{integer} \vee \tau_2 \neq \text{integer}) \wedge \mathcal{T}_B[\text{binArithOp } \omega_\alpha] = \text{typeWrong})$$

```
tBINOP ::= [bin(le)]
<:>
```

```
(typecheck(Type1, Type2, Type) :-
  ( Type1 = int, Type2 = int, Type = bool
  ; Type = typeWrong)),
(meaning(X, Y, Value) :-
  ( X =< Y, Value = bTRUE
  ; Value = bFALSE)),
(code(le)).
```

```
tBINOP ::= [bin(and)]
<:>
```

```
(typecheck(Type1, Type2, Type) :-
  ( Type1 = bool, Type2 = bool, Type = bool
  ; Type = typeWrong)),
(meaning(X, Y, Value) :-
  ( X = bTRUE, Y = bTRUE, Value = bTRUE
  ; Value = bFALSE)),
(code(and)).
```

### 14.3.6 Expressions

#### 14.3.6.1 Constant

```

expr ::= tCONST^^X
<:>
  (declcheck(_, checkOK)),
  (typecheck(_, Type) :-
    X^^meaning(Value),
    (    (Value = bTRUE ; Value = bFALSE), Type = bool
      ;    Type = int )
  ),
  (usecheck(_, UState, UState)),
  (meaning(_, _, Value) :- X^^meaning(Value)),
  (code(_, _, [loadConst(Value)]) :- X^^meaning(Value)).

```

The various meanings of a constant expression are independent of any environment or state. Hence, these are all indicated by anonymous variables.

#### 14.3.6.2 Named variable

```

expr ::= tIDENT^^I
<:>
  (declcheck(DEnv, DCheck) :-
    I^^meaning(Id),
    domain(DEnv, DDom),
    (    member(Id, DDom), DCheck = checkOK
      ;    DCheck = checkWrong )
  ),
  (typecheck(TEnv, Type) :-
    I^^meaning(Id),
    lookup(TEnv, Id, Type)
  ),
  (usecheck(UEnv, PreUState, PostUState) :-
    I^^meaning(Id),
    lookup(UEnv, Id, ULocn),
    PreUState = [PreUStore, PreUse],
    domain(PreUStore, UDom),
    (    member(ULocn, UDom),
      lookup(PreUStore, ULocn, Use),
      worse([PreUse, Use], PostUse),
      PostUState = [PreUStore, PostUse]
    );
    override(PreUStore, ULocn, checkWrong, PostUStore),
    PostUState = [PostUStore, checkWrong]
  ),
  ),

```

```

(meaning(Env, State, Value) :-
    I^^meaning(Id),
    lookup(Env, Id, Locn),
    lookup(State, Locn, Value)
),
(code(OEnv, _, [loadVar(OLocn)]) :-
    I^^meaning(Id),
    lookup(OEnv, Id, OLocn)
).

```

The Prolog predicate `member(Elem, Set)` implements ‘element of’; it succeeds if  $\text{Elem} \in \text{Set}$ .

`domain(Fun, Dom)` finds the domain of a function; it succeeds if  $\text{Dom} = \text{dom Fun}$ . `lookup(Fun, X, Y)` implements function application; it succeeds if  $\text{Fun}(X) = Y$ .

`worse([U1, U2, ..., Un], U)` implements the  $\bowtie$  function; it succeeds if

$$U1 \bowtie U2 \bowtie \dots \bowtie Un = U$$

### 14.3.6.3 Unary expression

```

expr ::= tUNYOP^^O, expr^^E
<:>
(declcheck(DEnv, DCheck) :- E^^declcheck(DEnv, DCheck)),
(typecheck(TEnv, Type) :-
    E^^typecheck(TEnv, TypeE),
    O^^typecheck(TypeE, Type)
),
(usecheck(UEnv, PreUState, PostUState) :-
    E^^usecheck(UEnv, PreUState, PostUState) ),
(meaning(Env, State, Value) :-
    E^^meaning(Env, State, ValueE),
    O^^meaning(ValueE, Value)
),
(code(OEnv, OLocn, [ExprCode, unyOp(Op)]) :-
    E^^code(OEnv, OLocn, ExprCode),
    O^^code(Op)
).

```

Note that the declaration and use checking semantics are independent of the operator `O`.

#### 14.3.6.4 Binary expression

```

expr ::= tLPAREN, exprE1, tBINOPOp, exprE2, tRPAREN
<:>
  (declcheck(DEnv, DCheck) :-
    E1declcheck(DEnv, DCheck1),
    E2declcheck(DEnv, DCheck2),
    worse([DCheck1, DCheck2], DCheck)
  ),
  (typecheck(TEnv, Type) :-
    E1typecheck(TEnv, Type1),
    E2typecheck(TEnv, Type2),
    Optypecheck(Type1, Type2, Type)
  ),
  (usecheck(UEnv, PreUState, PostUState) :-
    E1usecheck(UEnv, PreUState, UState1),
    E2usecheck(UEnv, PreUState, UState2),
    worseState(UState1, UState2, PostUState)
  ),
  (meaning(Env, State, Value) :-
    E1meaning(Env, State, Value1),
    E2meaning(Env, State, Value2),
    Opmeaning(Value1, Value2, Value)
  ),
  (code(OEnv, OLocn, [ExprCode2, store(OLocn), ExprCode1,
    binOp(Op, OLocn)]) :-
    E2code(OEnv, OLocn, ExprCode2),
    OLocn1 is OLocn + 1,
    E1code(OEnv, OLocn1, ExprCode1),
    Opcode(Op)
  ).

```

`worseState(S1, S2, S)` implements the *worseState* function; it succeeds if

$$\text{worseState}(S1, S2) = S$$

#### 14.3.7 Commands

##### 14.3.7.1 Block

```

cmd ::= tBEGIN, cmdListCL, tEND
<:>
  (declcheck(DEnv, DCheck) :- CLdeclcheck(DEnv, DCheck)),
  (typecheck(TEnv, TCheck) :- CLtypecheck(TEnv, TCheck)),
  (usecheck(UEnv, PreUState, PostUState) :-
    CLusecheck(UEnv, PreUState, PostUState) ),
  (meaning(Env, PreState, PostState) :-
    CLmeaning(Env, PreState, PostState) ),

```

```
(code(OEnv, PreLabel, PostLabel, InstrList) :-
    CL^^code(OEnv, PreLabel, PostLabel, InstrList) ).
```

### 14.3.7.2 Skip

```
cmd ::= tSKIP
<:>
(declcheck(_, checkOK)),
(typecheck(_, checkOK)),
(usecheck(_, UState, UState)),
(meaning(_, State, State)),
(code(_, Label, Label, [ ])).
```

### 14.3.7.3 Assignment

```
cmd ::= tIDENT^^I, tASSIGN, expr^^E
<:>
(declcheck(DEnv, DCheck) :-
    I^^meaning(Id),
    domain(DEnv, DDom),
    (    member(Id, DDom), DCheckI = checkOK
    ;    DCheckI = checkWrong ),
    E^^declcheck(DEnv, DCheckE),
    worse([DCheckI, DCheckE], DCheck)
),
(typecheck(TEnv, TCheck) :-
    I^^meaning(Id),
    lookup(TEnv, Id, TypeI),
    E^^typecheck(TEnv, TypeE),
    (    TypeI = TypeE, TypeE \= typeWrong, TCheck = checkOK
    ;    TCheck = checkWrong )
),
(usecheck(UEnv, PreUState, PostUState) :-
    I^^meaning(Id),
    lookup(UEnv, Id, ULocn),
    E^^usecheck(UEnv, PreUState, MidUState),
    MidUState = [MidUStore, _],
    domain(MidUStore, UDom),
    (    member(ULocn, UDom), PostUState = MidUState
    ;    updateStoreU(MidUState, ULocn, checkOK, PostUState) )
),
(meaning(Env, PreState, PostState) :-
    I^^meaning(Id),
    E^^meaning(Env, PreState, Value),
    lookup(Env, Id, Locn),
    updateStore(PreState, Locn, Value, PostState)
),
```



```

(code(OEnv, Label, Label, [InstrListE, store(OLocn)]) :-
  top(TempLocn),
  E^^code(OEnv, TempLocn, InstrListE),
  I^^meaning(Id),
  lookup(OEnv, Id, OLocn)
).

```

updateStoreU(UState, L, C, UState1) implements  $\boxplus_v$ ; it succeeds if

$$UState \boxplus_v \{L \mapsto C\} = UState1$$

updateStore(State, L, V, State1) implements  $\boxplus$ ; it succeeds if

$$State \boxplus \{L \mapsto V\} = State1$$

#### 14.3.7.4 Choice

```

cmd ::= tIF, expr^^E, tTHEN, cmd^^C1, tELSE, cmd^^C2
<:>
(declcheck(DEnv, DCheck) :-
  E^^declcheck(DEnv, DCheckE),
  C1^^declcheck(DEnv, DCheckC1),
  C2^^declcheck(DEnv, DCheckC2),
  worse([DCheckE, DCheckC1, DCheckC2], DCheck)
),
(typecheck(TEnv, TCheck) :-
  E^^typecheck(TEnv, TypeE),
  C1^^typecheck(TEnv, TCheckC1),
  C2^^typecheck(TEnv, TCheckC2),
  ( TypeE = bool, TCheckE = checkOK
  ; TCheckE = checkWrong ),
  worse([TCheckE, TCheckC1, TCheckC2], TCheck)
),
(usecheck(UEnv, PreUState, PostUState) :-
  E^^usecheck(UEnv, PreUState, MidUState),
  C1^^usecheck(UEnv, MidUState, UState1),
  C2^^usecheck(UEnv, MidUState, UState2),
  worseState(UState1, UState2, PostUState)
),
(meaning(Env, PreState, PostState) :-
  E^^meaning(Env, PreState, Value),
  ( Value = bTRUE,
    C1^^meaning(Env, PreState, PostState)
  ;
    Value = bFALSE,
    C2^^meaning(Env, PreState, PostState) )
),

```

```

(code( OEnv, PreLabel, PostLabel,
      [TestCode, jump(L1), ThenCode, goto(L2),
       label(L1), ElseCode, label(L2)]) :-
  top(TempLocn),
  E^^code(OEnv, TempLocn, TestCode),
  C1^^code(OEnv, PreLabel, MidLabel, ThenCode),
  C2^^code(OEnv, MidLabel, L1, ElseCode),
  L2 is L1 + 1,
  PostLabel is L1 + 2
).
```

### 14.3.7.5 Loop

```

cmd ::= tWHILE, expr^^E, tDO, cmd^^C
<:>
(declcheck(DEnv, DCheck) :-
  E^^declcheck(DEnv, DCheckE),
  C^^declcheck(DEnv, DCheckC),
  worse([DCheckE, DCheckC], DCheck)
),
(typecheck(TEnv, TCheck) :-
  E^^typecheck(TEnv, TypeE),
  C^^typecheck(TEnv, TCheckC),
  (   TypeE = bool, TCheckE = checkOK
   ;   TCheckE = checkWrong ),
  worse([TCheckE, TCheckC], TCheck)
),
(usecheck(UEnv, PreUState, PostUState) :-
  E^^usecheck(UEnv, PreUState, UStateE),
  C^^usecheck(UEnv, UStateE, UStateC),
  worseState(UStateE, UStateC, PostUState)
),
(meaning(Env, PreStore, PostStore) :-
  while(Env, PreStore, PostStore, E, C)
),
(code(OEnv, PreLabel, PostLabel,
      [label(L1), TestCode, jump(L2), BodyCode,
       goto(L1), label(L2)]) :-
  top(TempLocn),
  E^^code(OEnv, TempLocn, TestCode),
  C^^code(OEnv, PreLabel, L1, BodyCode),
  L2 is L1 + 1,
  PostLabel is L1 + 2
).
```

The dynamic meaning of the loop is pulled out as a separate clause, `while`, so that it can be consulted recursively. The `copy_term` is used to provide a copy of the

tree that can be instantiated with values during one execution of the loop:

```
while(Env, PreStore, PostStore, E, C) :-
  copy_term(E, E1),
  E^^meaning(Env, PreStore, Value),
  (
    Value = bTRUE,
    copy_term(C, C1),
    C^^meaning(Env, PreStore, MidStore),
    while(Env, MidStore, PostStore, E1, C1)
  );
  Value = bFALSE,
  PostStore = PreStore
).
```

#### 14.3.7.6 Input

```
cmd ::= tINPUT, tIDENT^^I
<:>
(declcheck(DEnv, DCheck) :-
  I^^meaning(Id),
  domain(DEnv, DDom),
  (
    member(Id, DDom), DCheck = checkOK
  );
  DCheck = checkWrong )
),
(typecheck(TEnv, TCheck) :-
  I^^meaning(Id),
  lookup(TEnv, Id, Type),
  (
    Type = int, TCheck = checkOK
  );
  TCheck = checkWrong )
),
(usecheck(UEnv, PreUState, PostUState) :-
  I^^meaning(Id),
  lookup(UEnv, Id, ULocn),
  PreUState = [PreUStore, _],
  domain(PreUStore, UDom),
  (
    member(ULocn, UDom), PostUState = PreUState
  );
  updateStoreU(PreUState, ULocn, checkOK, PostUState) )
),
(meaning(Env, PreState, PostState) :-
  I^^meaning(Id),
  writel(['input: ', Id, ' : ']), read(Value),
  lookup(Env, Id, Locn),
  updateStore(PreState, Locn, Value, PostState)
),
```

```

    (code(OEnv, OLocn, OLocn, [input, store(IdLocn)]) :-
      I^^meaning(Id),
      lookup(OEnv, Id, IdLocn)
    ).

```

The dynamic semantics is interpreted slightly differently in the Prolog. Rather than having an initial input stream, and taking the first item from it on each input command, instead the interpreter prompts the user to type the input, and reads the input value from the keyboard.

#### 14.3.7.7 Output

```

cmd ::= tOUTPUT, expr^^E
<:>
(declcheck(DEnv, DCheck) :- E^^declcheck(DEnv, DCheck)),
(typecheck(TEnv, TCheck) :-
  E^^typecheck(TEnv, Type),
  (   Type = int, TCheck = checkOK
    ;   TCheck = checkWrong )
),
(usecheck(UEnv, PreUState, PostUState) :-
  E^^usecheck(UEnv, PreUState, PostUState) ),
(meaning(Env, PreState, PostState) :-
  E^^meaning(Env, PreState, Value),
  PostState = PreState,
  writel(['output : ', Value, nl])
),
(code(OEnv, OLocn, OLocn, [ExprCode, output]) :-
  top(TempLocn),
  E^^code(OEnv, TempLocn, ExprCode)
).

```

The dynamic semantics is interpreted slightly differently in the Prolog. Rather than storing all the outputs up into a final output stream, the interpreter writes the outputs to the screen on each output command.

# Part IV

## Winding Up



## Further Considerations

### 15.1 One small step

Tosca is very similar to the classic tutorial language of ‘while’ programs, but here much more emphasis has been placed on static semantics. Although languages almost as small as Tosca are actually used in developing some high integrity applications, the reason is not that they are particularly appropriate, but simply that larger languages are not considered safe, for the reasons outlined in Chapter 1. Tosca is merely a first small step away from assembly language.

There are a variety of concerns that need to be addressed for developing a compiler for a ‘full’ language. These range from theoretical concerns about what language features should be supported, to pragmatic ones of how to manage the development process in practice. Some of these are discussed below.

### 15.2 Other language features

Tosca lacks some of the language features needed to support good software engineering practice, and treats others in a less than satisfactory manner. Some of these are discussed below.

Note that as more sophisticated features are added, the approach of sets and partial functions taken in this book becomes inadequate. The full power of domain theory is required. However, the approach described here (separate static semantics, proof by structural induction, and implementation as a DCTG) is just as applicable in the more powerful mathematical formalism.

**15.2.1 Data structures: arrays and records**

Data structures appropriate to the application, not to the hardware, are one of the first features that need to be supplied. More than integers and booleans are needed. Statically bounded arrays and non-recursive record structures (exactly the kind of restrictions usually required of a high integrity language) are relatively straightforward to specify and prove.

The introduction of arrays begs for the introduction of a **for** loop, too, to iterate over them. This is also quite straightforward: simpler than a **while** loop in many respects.

A good, careful, specification of the type checking semantics of record types is needed to curtail those interminable traditional discussions of whether **foo** and **baz** have the same type or not in a declaration such as

```
foo : record( x:int ; y:int ) ;
baz : record( x:int ; y:int ) ;
```

**15.2.2 Functions and procedures**

Tosca's main disadvantage is the lack of an abstraction mechanism. Procedures or functions are essential for modularizing large programs.

Most traditional books on compiler writing give informal descriptions of algorithms for compiling procedures and functions. The challenge for a high integrity compiler specifier is not only to formalize such an algorithm, but to formalize it in such a way that it is possible to prove it correct against the dynamic semantics specification.

**15.2.3 Recursion**

Recursion (both in procedure calls and data structures) is usually forbidden in high integrity languages, because of the danger that an embedded processor could run out of memory during program execution. For a non-recursive language such as Tosca, it is possible to work out at compile time how much memory a program requires, and so to check that the proposed target machine has enough memory to run it.

But there are critical applications that are not embedded, and where it is perfectly allowable for them to stop and complain that they have run out of memory, provided they do not produce any partial, incorrect output. An example of such an application is a compiler for a high integrity language! Recursion is allowable (and in fact highly desirable) in a high integrity development language.



#### 15.2.4 Separate compilation

Once procedures and functions have been added to the language, separate compilation becomes a possibility. As much care needs to be taken over specifying the meaning of separate modules as on any other language feature. On the dynamic side, the initial environment can be taken as non-empty, but rather containing the relevant procedures. On the static side, careful definitions are needed to provide safe separate compilation.

From the high integrity perspective, there is an extra potential for errors introduced by separate compilation. The configuration system needs to ensure that the correct versions of modules are provided for linking. The newly needed linker needs to be proved correct, too, which can be done using the same techniques as described here for the compiler.

### 15.3 Tool support

Tosca is small enough that the specification, proof and translation work can feasibly be done by hand. As the language grows, tool support at all stages of the process becomes imperative.

There are tools to support writing and checking Z specification. But set-based Z is not appropriate for larger languages, and so tool support for domain theory based specifications is necessary. Both the correctness proofs and the translation into executable form are highly stylized, so tool support for these should also be feasible.

### 15.4 Optimization

Optimization can be dangerous. It can produce obscure code, and it can introduce bugs. Hence the traditional motto runs: *First law of optimization: don't do it. Second law of optimization (for experts only): don't do it yet.*

#### 15.4.1 Of the compiler

It is highly inadvisable to optimize the performance of the compiler. One of the requirements of the method advocated in this book is that there be a clear, visibly correct, translation from the mathematical definition of the semantics to the Prolog DCTG implementation. Optimization could only compromise this visibility.

### 15.4.2 Of the target code

One of the requirements for a high integrity compiler is that there be a clear mapping between each fragment of target code and its corresponding source code. This would seem to rule out optimization, which tends to obscure any such correspondence. However, the reason for this requirement is that the compiler is not trusted, and so this correspondence is required to enable an extra validation step to be performed. When the compiler can be trusted, this validation will not be required, and optimization becomes possible.

All optimizations must be performed with the same care and proof work as the compiler development, to ensure that no bugs are introduced. The semantics of the language should be used to prove that any proposed optimization is a *meaning preserving* transform.

## 15.5 Axiomatic semantics

As stated in Chapter 1, one issue not addressed here is the problem of how one would go about proving that a high integrity application written in the source language is itself correct. In fact, denotational semantics is not the most appropriate formalism for reasoning about the properties of a program; axiomatic semantics is a better approach.

The axiomatic semantics of a language should, however, be proved *sound* against the denotational semantics: that everything provable using the axiomatic semantics is in fact true. The logic for Z itself has been proved sound against its denotational semantics in such a manner.

## 15.6 Testing

The process of proving the compiler correct is a process of greatly increasing assurance in its correctness, not a process of providing absolute certainty. Just because the operational semantics templates have been proved correct, and the translation performed in a clear and visible manner, does not mean that the resulting compiler is infallible. There are many possible sources of bugs still around. Some of these are discussed below.

There could be an error in the proof. If a proof is done by hand, certain ‘obvious’ steps tend to be left out, for brevity. This could introduce an error, as could a simple mistake in copying from one line to the next. Cutting out the human step, and using a machine-based proof assistant, removes one class of errors and introduces another: there might be a bug in the proof tool. Since machine-produced proofs tend to be exceedingly long and dull (no steps are left out, and the steps tend to be smaller) there is less chance that a human reviewer might catch the

error. Assurance can be increased by checking the proof produced by one tool with a separately developed tool, the hope being that if the second tool also has bugs, they might be different bugs.

There could be an error in the translation from Z to Prolog. Again, this could be due to a transcription error if done by hand, or a programming error in an automated translator. Some of the more trivial typographical errors are detected as syntax errors by the Prolog system when the compiler is run. But some errors might slip through.

There could be an error in the development environment. This includes the possibility of errors in the DCTG implementation, in the Prolog system itself, in the underlying operating system, and in the underlying hardware.

There could be an error in the formal semantics of the target microprocessor because the action of its instructions has been misunderstood, and formalized incorrectly.

The moral of this tale is that, no matter how much mathematical specification and proof is carried out, good old-fashioned testing still has a role to play. It provides yet another degree of increased assurance. But remember Dijkstra's famous aphorism: *Program testing can be used to show the presence of errors, but never to show their absence!*

## 15.7 Validation versus verification

Yet another potential source of bugs is that there could be an error in the target microprocessor so that it does not implement its specified semantics. There is a subtle difference from the last point made in the previous section, and arises from the difference between verification and validation.

*Verification* is the process of proving some property of a piece of mathematics, for example, that one specification is a correct refinement of another, or that a specification exhibits certain properties expressed in a theorem. Verification is a formal, mathematical process.

*Validation* is the process of demonstrating that a model (here assumed to be a mathematical model) has the desired properties, for example, that it suitably captures the system requirements, or adequately models the operation of a physical device. Validation is inherently informal, and cannot even in principle be a mathematical process, because it is attempting to demonstrate some correspondence between a piece of mathematics and the physical real world. It is impossible to *prove* a mathematical model faithfully reflects the physical system it is modelling, that is, to prove that everything that needs to be modelled has been modelled, and that what has been modelled has been done so accurately. So, to return to the example that started this discussion, it is impossible ever to prove, in the mathematical sense of the word, that a given microprocessor implements its mathematical semantics. This argument holds for any applications that interact with

or control aspects of the real world. No less a person than Einstein said: *As far as the laws of mathematics refer to reality, they are not certain, and as far as they are certain, they do not refer to reality.* There is always a need to validate the formal mathematical specification against informal requirements or behaviour of a physical device. Validation is a fancy name for testing.

This point is laboured because there is frequently misunderstanding about what a formal specification provides. It provides greatly increased assurance, but it does not provide certainty. This is not a failing peculiar to mathematics, however. Nothing provides certainty.

## 15.8 Further reading

A catalogue of the various tools currently available for supporting  $Z$  can be found in [Parker 1991].

[Tennent 1991] describes how to prove the soundness of an axiomatic semantics against the denotational semantics of a programming language. [Woodcock and Brien 1992] describe a logic for  $Z$ , and discuss its soundness proofs.

For a thought-provoking argument on what constitutes a valid proof, and how much assurance the existence of a proof actually gives, see [De Millo *et al.* 1979].

# Concluding Remarks

## 16.1 Summary of the approach

In summary, the approach to building a high assurance compiler described in this book has the following steps.

1. Specify a denotational semantics for the source language. Many problems and ambiguities arising in the language definition can be resolved at this stage. This specification should include both dynamic and static semantics.
2. Write each semantics as a Prolog DCTG. Each static semantics provides the relevant checker (for example, type checker, declaration checker). The dynamic semantics provides an interpreter that can be used to provide further validation for the proposed semantics.
3. Specify a denotational semantics for the target language.
4. Specify an operational semantics of the source language as code templates in the target language. This specification is an algorithm for the compiler.
5. Calculate the meaning of these templates, using the target language semantics, to prove that they have the same meaning as the corresponding source language constructs. This proves that the proposed compiler performs a correct (meaning preserving) translation.
6. Write the operational semantics as a DCTG. This provides a compiler.

## 16.2 The criteria for high assurance compilation

How does this approach fit the criteria laid out for a high assurance compiler in Chapter 1?

1. *The high level source language must have a target-independent meaning.* The denotational semantics definition provides this meaning. The calculation of

the meaning of a program can proceed manually from the semantics, as shown in the ‘square’ example of Chapter 9, or mechanically by using the target-independent interpreter.

2. *The source language must have a mathematically defined semantics.* The denotational semantics definition of Tosca is a mathematical definition, and hence, can be reasoned about.
3. *The target language must also have a mathematically defined semantics.* The denotational semantics definition of Aida is a mathematical definition.
4. *The compiler . . . must be correct.* Each Aida template has been proved to have the same meaning as the corresponding Tosca statement.
5. *[The compiler] must be written clearly, and must be clearly related to the semantics.* The Prolog DCTG approach allows a clear one-to-one mapping between the mathematical definition and its implementation.
6. *The target code produced by the compiler must be clear, and easily related to the source code.* The template style and lack of optimization allows a clear mapping from target code to the corresponding source code. If a clearer correspondence is needed, it is a simple job to make the compiler annotate the target code.
7. *The semantics . . . must be made available for peer review and criticism.* See the descriptions in sections 8.2–8.6 and Chapter 10.

The small languages and the compiler described in this book were developed as a prototype demonstration, and it would not be surprising if mistakes are found in either the semantics or the compiler. Indeed, since one of the aims of the approach described here is to produce a specification and implementation of sufficient clarity to facilitate the discovery of such errors, it would be disappointing if they were not discovered!

**Part V**

**Appendices**





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## Recursive Definition of Loops

In section 8.5.7.4, the semantics of the while loop was defined recursively, in terms of itself. A non-recursive formulation of the semantics is possible, but it is less convenient for implementing the interpreter. Consider the following, which specifies an infinite family of functions  $\mathcal{W}$ , whose members are defined in terms of earlier members of the family, *not* in terms of themselves:

$$\left| \begin{array}{l}
 \mathcal{W} : \mathbb{N} \longrightarrow (CMD \times EXPR) \longrightarrow Env \leftrightarrow State \leftrightarrow State \\
 \hline
 \forall n : \mathbb{N}; \gamma : CMD; \epsilon : EXPR; \rho : Env; \sigma : State \bullet \\
 \mathcal{W}_0(\gamma, \epsilon) \rho = \emptyset[State \times State] \\
 \wedge \mathcal{W}_{n+1}(\gamma, \epsilon) \rho \sigma = \\
 \quad \mathbf{if} \ \mathcal{M}_E[\epsilon] \rho \sigma = \mathit{bool}_v \top \ \mathbf{then} \ \mathcal{W}_n(\gamma, \epsilon) \rho (\mathcal{M}_C[\gamma] \rho \sigma) \ \mathbf{else} \ \sigma
 \end{array} \right.$$

$\mathcal{W}_0(\gamma, \epsilon) \rho$  is the empty function. There is no state that is in its domain. Since the possibility of a non-terminating loop is being modelled by using a *partial* state transition function, the empty function can be interpreted as the specification of a non-terminating loop.

Each successive  $\mathcal{W}_n$  executes the body command one more time before it becomes the empty non-terminating function. So  $\mathcal{W}_n$  has the same behaviour as a loop that executes less than  $n$  times before terminating. For any particular loop that terminates, it is possible to find a particular value of  $n$  sufficiently large.

So  $\mathcal{W}_{n+1}$  has a larger domain than  $\mathcal{W}_n$  (it also includes loops that execute  $n$  times), but where their domains overlap (on loops that execute less than  $n$  times), their results agree. Hence we can take the union of all these  $\mathcal{W}_n$  and define the meaning of a general loop non-recursively as

$$\left| \ \mathcal{M}_C[\mathbf{loop}(\epsilon, \gamma)] = \bigcup \{ n : \mathbb{N} \bullet \mathcal{W}_n(\gamma, \epsilon) \}
 \right.$$

## Z's Free Type Construct

---

Z uses fairly standard mathematical notation for most of its constructs, but its free type (disjoint union) needs a little more explanation.

A disjoint union is a way of combining two sets into a new set so that all the elements of the new set ‘know’ which of the old sets it came from. The simplest way to do this is to tag every element. So, for example, the disjoint union of  $A = \{a, b, c\}$  and  $B = \{c, d, e\}$  could be written as  $\{(a, 1), (b, 1), (c, 1), (c, 2), (d, 2), (e, 2)\}$ . By examining its tag, it is possible to discover if any particular  $c$  comes from  $A$  or from  $B$ .

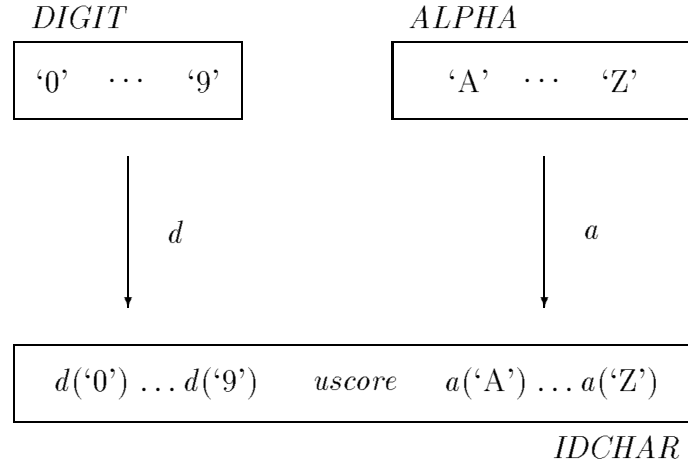
In mathematics, a disjoint union is often written as  $A + B$ , and the tags are often omitted, since the sets being unioned are often disjoint in the first place. So it is quite possible to see expressions like  $VALUE = NAT + BOOL$ .

Z does not have this luxury. It is based on *typed* set theory, and its type rules do not permit sets of different types to be combined in this way. A more elaborate approach is used: free type definitions. A simple example is

$$\begin{aligned}
 IDCHAR ::= & \text{uscore} \\
 & | d\langle\langle DIGIT \rangle\rangle \\
 & | a\langle\langle ALPHA \rangle\rangle
 \end{aligned}$$

which introduces the set  $IDCHAR$ , and puts some constraints on its members, given that  $DIGIT$  and  $ALPHA$  are sets introduced elsewhere.  $d$  and  $a$  act the part of tags: they are functions that map elements of  $DIGIT$  and  $ALPHA$  to members of  $IDCHAR$ . These constructor functions are total injections (one-to-one functions); they map every element in their domain to a different element of  $IDCHAR$ .  $uscore$  is a single element of  $IDCHAR$ . Extra conditions ensure that all these constructed elements of  $IDCHAR$  are distinct (that  $uscore$  and the ranges of  $a$  and  $d$  do not overlap), and that there are no other elements in  $IDCHAR$  (it is the smallest set containing these constructed elements). The construction is summarized in Figure C.1. An equivalent Z form is

$$[IDCHAR]$$



**Figure C.1** Construction of a free type

$$\left| \begin{array}{l}
 \text{uscore} : \text{IDCHAR} \\
 d : \text{DIGIT} \rightarrow \text{IDCHAR} \\
 a : \text{ALPHA} \rightarrow \text{IDCHAR}
 \end{array} \right. \\
 \hline
 \langle \{\text{uscore}\}, \text{ran } d, \text{ran } a \rangle \text{ partition IDCHAR}$$

A consequence of using free types is that definitions tend to be splattered with the names of these tag functions. For example, given a *DIGIT* called  $\delta$ , the corresponding *IDCHAR* is  $d(\delta)$ ; given an *IDCHAR* called  $\iota$  that is in the range of  $d$ , it corresponds to the *DIGIT*  $d^{-1}(\iota)$  (where the function  $d^{-1}$  is the inverse of the function  $d$ ).

Free type definitions may also be recursive. In such a case there must be at least one non-recursive base case, to allow the recursion to terminate (or, from a more constructive viewpoint, to allow the construction to begin). For example, it is possible to define a recursive *LIST* type as

$$\begin{array}{l}
 \text{LIST} ::= \text{nil} \\
 \quad | \text{cons} \langle \mathbb{N} \times \text{LIST} \rangle
 \end{array}$$

which is shorthand for

[LIST]

$$\left| \begin{array}{l}
 \text{nil} : \text{LIST} \\
 \text{cons} : \mathbb{N} \times \text{LIST} \rightarrow \text{LIST}
 \end{array} \right. \\
 \hline
 \langle \{\text{nil}\}, \text{ran } \text{cons} \rangle \text{ partition LIST}$$



The base element of *LIST* is *nil*. Using the constructor function with *nil* as its list argument constructs the further elements  $\text{cons}(0, \text{nil})$ ,  $\text{cons}(1, \text{nil})$ ,  $\text{cons}(2, \text{nil})$ ,  $\dots$ . Using *cons* with one of these ‘one-element lists’ constructs ‘two-element lists’ such as  $\text{cons}(0, \text{cons}(0, \text{nil}))$ ,  $\text{cons}(1, \text{cons}(0, \text{nil}))$ ,  $\text{cons}(2, \text{cons}(0, \text{nil}))$ ,  $\dots$ ,  $\text{cons}(0, \text{cons}(1, \text{nil}))$ ,  $\text{cons}(1, \text{cons}(1, \text{nil}))$ ,  $\text{cons}(2, \text{cons}(1, \text{nil}))$ ,  $\dots$ . And so on.

A recursive free type has an infinite number of elements, because it is always possible to use one of its constructor functions to make a new element from existing ones.

---

# Glossary of Notation

In order to reduce the number of declarations needed in definitions, most of them have been made implicit, and the same symbol consistently used for the same type of variable. These are summarized below.

## D.1 Syntactic variables

Name	Z type	Variable
command	<i>CMD</i>	$\gamma$
command list	seq <i>CMD</i>	$\Gamma$
declaration	<i>DECL</i>	$\delta$
declaration list	seq <i>DECL</i>	$\Delta$
expression	<i>EXPR</i>	$\epsilon$
instruction	<i>INSTR</i>	$\iota$
instruction list	seq <i>INSTR</i>	$I$
operator, binary	<i>BIN_OP</i>	$\omega$
operator, binary arithmetic	<i>BIN_ARITH_OP</i>	$\omega_\alpha$
operator, binary comparison	<i>BIN_COMP_OP</i>	$\omega_\chi$
operator, binary logic	<i>BIN_LOGIC_OP</i>	$\omega_\lambda$
operator, unary	<i>UNY_OP</i>	$\psi$
operator, unary arithmetic	<i>UNY_ARITH_OP</i>	$\psi_\alpha$
operator, unary logic	<i>UNY_LOGIC_OP</i>	$\psi_\lambda$

## D.2 Semantic variables

Name	Z type	Variable
boolean	<i>Boolean</i>	$b$
check	<i>CHECK</i>	$c$
constant value	<i>VALUE</i>	$\chi$
continuation	<i>Cont</i>	$\vartheta$
environment	<i>Env</i>	$\rho$
integer	$\mathbb{Z}$	$n$
label	<i>Label</i>	$\phi$
location	<i>Locn</i>	$\lambda$
name, identifier	<i>NAME</i>	$\xi$
store	<i>Store</i>	$\varsigma$
state	<i>State</i>	$\sigma$

## D.3 Use of subscripts

Subscripts are used to distinguish different, but related, variables:

- Any variable can take a numerical subscript
- Declaration-before-use types have a subscript  $D$ , variables a subscript  $\delta$
- Type-checking types have a subscript  $T$ , variables a subscript  $\tau$
- Initialization-before-use types have a subscript  $U$ , variables a subscript  $v$

So, for example,  $\phi, \phi_4$  represent variables of type *Label*, and  $\sigma_v, \sigma_{v_2}$  represent variables of type *State<sub>v</sub>*.

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