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To Carroll Morgan, on his 60th birthday

Abstract. Safety-Critical Java (SCJ) is a version of Java for real-time programming, restricted to facilitate certification of implementations of safety-critical systems. Its development is the result of an international effort involving experts from industry and academia. What we provide here is, as far as we know, the first formalisation of the SCJ model of memory regions. We use Hoare and He's unifying theories of programming (UTP), enabling the integration of our theory with refinement models for object orientation and concurrency. In developing the SCJ theory, we also make a contribution to UTP by providing a general theory of invariants (an instance of which is used in the SCJ theory). The results presented here are a first essential ingredient to formalise the novel programming paradigm embedded in SCJ, and enable the justification and development of formal reasoning techniques based on refinement.

Keywords: Safety-Critical Java; Memory safety; Semantics; Unifying theories of programming; Integration; Refinement

1. Introduction

Modern society is almost totally reliant on software-based infrastructure. This demands modelling and programming languages and techniques that facilitate and ensure quality. Subsets of two languages have dominated highintegrity real-time engineering. The first is Ada [Bar05], which provides good support through its Spark [Bar03] and Ravenscar subsets [Bur99] and the Spark Examiner Toolset, but which has a limited community. The second is C/C++ (see, for example [Hat95, MIS07]), which jointly have a much larger community, but whose safer subsets lack support for formal development. In both cases, various modern programming features found useful in other sectors of the software industry are left out on the grounds of safety.

An international community effort has produced a high-integrity real-time version of Java: Safety-Critical Java (SCJ) [SCJDraft]. It achieves a compromise between the safety of Ada and the popularity of C/C++, and provides an ambitious novel take on the combined safe use of object orientation and real-time programming. SCJ lacks, however, a formal underpinning for its programming models. In this paper, we provide a formalisation for its memory management model.

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Fig. 1. The internal architecture of the Java virtual machine (taken from [Ven07, Chapter 5])

SCJ is based on a subset of Java augmented by the real-time specification for Java (RTSJ) [Wel04]. In Java, programmers need not be concerned with memory management. In contrast, in SCJ (and the RTSJ), a programmer must consider in which area to create objects according to their anticipated lifetime, and tools and techniques are needed to ensure efficient use of memory and absence of run-time errors. In order to understand the full implications of the SCJ memory model, it is necessary to appreciate the run-time data structures that are maintained by a Java virtual machine—Fig. 1 shows its internal architecture. In the context of this paper, our main concern is with the heap and the programming stacks.

All objects are placed on the heap, which is traditionally scanned by a garbage collector to remove any that are unused, and so unreachable. Each thread of control has an associated stack that is used to store variables that are local to methods. Variables and object fields can be of a primitive type (int, short, and so on) or of a reference type. We ignore here all issues associated with native methods.

The RTSJ supplements Java's garbage-collected heap memory model with support for memory regions [Ven07] called *memory areas*. As with the Java heap, these regions are used to store dynamically created objects; variables local to methods are still managed by the stacks associated with each thread of control.

SCJ restricts the RTSJ memory model to prohibit use of the heap. The RTSJ and SCJ both introduce two new kinds of memory area: a collection of scoped memories and a single immortal memory. Objects allocated in a scoped memory have a lifetime that is determined by the number of threads that are currently using that scoped memory area. When there are no such threads, all the objects are collected. A program can have many scoped memory areas and their use can be nested. In contrast, each program has only a single instance of immortal memory, and objects created there have a lifespan equal to that of the program. Statically declared objects and fields are created in immortal memory, as are objects created by static initialisers.

Currently, SCJ includes annotations that can be used to document programs, and enable static verification of properties including memory safety (the avoidance of dangling references). The work in [TPV10] presents rules for use of the annotations, and a tool that checks statically that these rules are followed. It is not at all trivial to convince ourselves that the rules proposed and enforced by the tool achieve the level of memory safety claimed.



Fig. 2. Safety critical mission phases (taken from [SCJDraft])

While we do not necessarily expect to find any problems, the formalisation of the memory model (and of other aspects of the SCJ programming model) is essential for the justification of the soundness of such techniques.

Our first contribution is an informal description of the SCJ memory model that explains the rationale for its design. (For a discussion of the design of the concurrency model, we refer to [WK11].) As a second contribution, we provide a relational semantics for this model; it is based on Hoare and He's unifying theories of programming (UTP) [HH98]. Finally, we present a general UTP theory for operation and state invariants, which we instantiate to capture in an elegant and concise way the properties of the SCJ structure of memory areas.

The UTP is a relational framework that supports refinement-based reasoning about a variety of paradigms. It covers models for concurrent, functional and logic programming, for instance. It has also been used to define constructs related to object-orientation [SCS06] and time [SCJS10]. By casting the SCJ memory model in UTP, we pave the way for its integration with these theories, that cater for other, also very important, aspects of an SCJ program.

In the next section, we present informally the SCJ memory model; an introduction to UTP is provided in Sect. 3. Next, Sect. 4 presents a general UTP theory for program invariants. In Sect. 5, we instantiate those results to formalise the SCJ memory model. We draw our conclusions in Sect. 8.

2. Safety-Critical Java memory model

SCJ recognises that safety-critical software varies considerably in complexity. At one end of the spectrum, the application consists of a single thread executing a single function on a single processor with a simple timing constraint. At the other end, it is multithreaded, executing in multiple modes on multiple processors. Consequently, there are three compliance levels (0–2) for SCJ programs and implementations. In this work, we are concerned with Level 1, which, roughly, corresponds in complexity to the Ravenscar profile for Ada.

The SCJ programming model is based on the notion of missions, which are managed by a mission sequencer (see Fig. 2). At Level 1, missions may be composed into sequences, but nested missions are prohibited. A Level 1 mission consists of a bounded set of asynchronous event handlers (ASEHs), which can be considered as being equivalent to real-time threads. Both periodic and aperiodic event handlers are supported: each handler executes a sequence of releases that are either time triggered (periodic) or event triggered (aperiodic). Consequently, an SCJ program is a concurrent program with threads of control for the main program, the mission sequencer, and one for each of the ASEHs.

The main goal of the SCJ memory model is to support the disciplined use of dynamic memory management. Traditionally, safety-critical systems do not allocate memory during the execution of a mission due to (a) the error-prone nature of manual allocation and deallocation schemes (typified by malloc and free in C) that can result in a violation of safe memory accesses due to dangling references, and (b) the complexity of automatic memory deallocation schemes based on garbage collection (and resulting difficulty regarding certification).

The region-based approach of the RTSJ provides safer and more predictable support for dynamic memory management, but the overall model is still complex. SCJ, consequently, constrains the use of its features: garbage collection is not supported, and only a restricted version of the scoped memory model is provided.

Basically, the structure of the memory areas is fixed as shown in Fig. 3. Every thread of control in an SCJ program has a default memory allocation context: the memory area in which objects created by the allocator (the new operation) are placed. The main program's thread of control has immortal memory as its default allocation context. It is this thread that, for instance, creates the application's mission sequencer and any objects that should exist throughout the lifetime of the program, and places them in immortal memory.



Fig. 3. SCJ memory model

The mission sequencer's thread of control is then started with immortal memory as its default allocation context. It is responsible for creating the mission memory, a scoped memory area that becomes the default allocation context for a mission. There is no thread of control associated with a mission; instead, the mission sequencer's thread of control performs the mission initialisation. The ASEHs are created in mission memory during the mission initialisation phase. Mission memory is cleared at the end of each mission, so that the memory can be used for the next mission. Any objects that must remain across missions must be stored in immortal memory.

Each ASEH has an associated private "per-release" memory area, and this is the default memory allocation context every time the ASEH is released. It is cleared at the end of each release, for reuse in the next release. Any object that is required to live across releases must be placed in mission memory.

An ASEH can create a temporary, private, scoped-memory area and change its default allocation context to the newly created area. More than one of these can be created and they are used in a LIFO manner. The stack of private temporary memory areas arises from nested calls to a create method. As the inner calls are finished, memory areas are popped off.

In the example shown in Fig. 3 there are therefore:

- Six thread-of-control stacks: one for the main program, one for the mission sequencer, and one for each ASEH.
- A single immortal memory—accessible by all threads of control.
- A single mission memory—accessible by the ASEHs and the mission sequencer.
- One private per-release memory area for each ASEH—accessible only by the associated ASEH.
- A stack of temporary private scoped memory area for each ASEH—accessible only by the associated ASEH.

Hence, each ASEH can access the immortal and mission memory areas, and its associated per-release and private memory areas. The mission sequencer can access only the mission and the immortal memory, and the main program can access only the immortal memory. The thread stacks associated with the ASEHs, the mission sequencer, and the main program can hold only references to objects in the memory areas that they are able to access.

The aim of this restricted model is to ensure that dangling references cannot occur, and that programs are amenable to static analysis techniques that can determine the absence of run-time errors, such as illegal-assignment errors. As already said, an assertion-based technique is presented in [TPV10].

Immortal Memory
Objects created are never deleted.
Objects can reference other objects only in immortal memory.
Mission Memory
A form of scoped memory.
Objects created are automatically destroyed at the end of the associated mission
Objects created can access other objects only in the same mission memory or in
immortal memory.
Per-release Scoped Memory
A private memory for each ASEH.
The objects allocated are destroyed at the end of each release (period for periodic
ASEHs).
The memory is reused when the next release occurs.
An object created can access objects in the current mission memory, immorta
memory, and its own per-release memory.
Private Temporary Scoped Memory
Every ASEH can create one or more temporary scoped memory areas during its
release.
They must be explicitly entered and exited.
An object created can reference its mission memory, immortal memory, and per-
release scoped memory.
It can reference all of its outer-level nested private areas.
The objects are destroyed when the memory area is exited.
References in objects at each level are permitted to target only objects at that
level and higher levels, never lower levels.

Fig. 4. Summary of SCJ memory model

Figure 3 illustrates the SCJ memory model; Fig. 4 summarises the SCJ memory areas and their properties; and Sect. 5 formalises this model in UTP.

3. Unifying theories of programming

In UTP, relations are defined by predicates over an alphabet: a set of the names of observational variables that record information about the behaviour of a program. In the theory of general relations, these include the vector of programming variables v, and their dashed counterparts v', with v used to refer to an initial observation of the value of v, and v' to a later observation. The set of undecorated (unprimed) variables in the alphabet αP of a predicate P is called its input alphabet $in\alpha P$, and the set of dashed variables is its output alphabet $out\alpha P$. A condition is a predicate whose alphabet includes only input variables.

Theories are characterised by their alphabet and by healthiness conditions defined by monotonic idempotent functions on predicates. The theory contains all the predicates on the alphabet that are fixed points of the healthiness conditions. As an example, we consider designs.

The general theory of relations in UTP cannot distinguish between terminating and nonterminating programs. The distinction is achieved in the subtheory of designs, which includes two extra boolean observational variables to record the start and the termination of a program: ok and ok'. The monotonic idempotents used to specify the healthiness conditions for designs are defined by the following fixed-point equations.

H1
$$P = ok \Rightarrow P$$

H2 P = P; J where $J \stackrel{\frown}{=} (ok \Rightarrow ok') \land v' = v$

Here the alphabet of P is $\{ok, ok', v, v'\}$. If P is **H1**-healthy, then it makes no restrictions on the final value of variables before it starts. If P is **H2**-healthy, then termination must be a possible outcome from every initial state. The functional composition of **H1** and **H2** is named **H**.

Every design D can be written in the form $P \vdash Q$, where P is its precondition, and Q its postcondition; $P \vdash Q$ is defined as $ok \land P \Rightarrow ok' \land Q$. The set of relations constructed in this way is exactly the set fixed points of **H**. Moreover, every design D can be written as $\neg D^f \vdash D^t$, where f is the boolean false, t is true, and D^b is the predicate D[b/ok'] obtained by substituting b for ok' in D.

Typically, a theory defines a number of programming operators of interest. Common operators like assignment, sequence, and conditional, are defined for general relations. A conditional is written as $P \triangleleft b \triangleright Q$; its behaviour is (described by) P if the condition b holds, else it is defined by Q.

 $P \lhd b \rhd Q \stackrel{\simeq}{=} (b \land P) \lor (\neg b \land Q)$ where $\alpha(b) \subseteq \alpha(P) = \alpha(Q)$

Sequence is relational composition.

 $P; Q \cong \exists w_0 \bullet P[w_0/w'] \land Q[w_0/w], \text{ where } out\alpha(P) = in\alpha(Q)' = w'$

The relation P; Q is defined by a quantification that relates the intermediate values of the variables. It is required that $out\alpha(P)$ is equal to $in\alpha(Q)'$, which is named w'. The sets w, w', and w_0 are used as lists that enumerate the variables of w and the corresponding decorated variables in the same order.

A central concern of UTP is refinement. A program P is refined by a program Q, written $P \sqsubseteq Q$, if, and only if, $P \leftarrow Q$, for all possible values of the variables of the alphabet. The set of alphabetised predicates over a given alphabet forms a complete lattice with this ordering. Recursion is modelled by weakest fixed-points.

The design that models skip, the program that terminates without changing any variable, is II, which is defined as (**true** $\vdash v' = v$), where v is the list of programming variables in the alphabet. Interestingly, II is the left identity of sequential composition, but not necessarily the right identity. This requires that the precondition does not contain dashed variables, a property not adequate, for instance, in the theory of reactive designs used as a concurrency model (for CSP).

A theory needs to be closed under the programming operators: they need to take healthy predicates to healthy predicates, so that they can be used to define models compositionally. In the next section, we provide some general results for the healthiness conditions of a theory of designs with invariants.

4. Invariants in UTP

In [HH98], designs are used to construct more general relations to model, for example, reactive programs. For these, even in the presence of divergence, some properties hold; for instance, the history of previous interactions is not affected. In [CHW06, HCW08], we take a similar approach in a theory for objects and sharing as available in Java. Our theory captures essential physical properties of sharing; for instance, variables that share a memory location have the same value.

On the other hand, when an SCJ program aborts, there is no guarantee that its restrictions on memory areas are maintained. So we present our theory as a subset of the theory of designs. Other examples of design subtheories are presented in the line of work established in [He07], which provides UTP theories for BPEL-like languages, with new forms of nontermination to handle exceptions. Here, we provide a general account of design subtheories characterised by invariants and with the standard notion of termination.

It is in the spirit of UTP to define theories for particular programming features, and combine them to capture more complex paradigms. We could conceivably treat the two issues of the memory structure of SCJ programs and termination separately. We would then characterise a subtheory of relations using a healthiness condition HSCJ, for instance, and then use the designs' healthiness condition H to embed it in the theory of designs.

This is, however, not adequate. The design characterised by H(P), if P is an HSCJ-healthy predicate that does not have ok and ok' in its alphabet, is $\neg P \vdash false$. Its precondition considers the possibility of HSCJ not holding (even if we are not in an abortive state), and, in this case, it is miraculous. What we need instead is a theory that allows for the memory restrictions to be violated just in the case of nontermination, a situation from which a program cannot recover.

In what follows, subtheories of designs are defined by healthiness conditions that either capture operation invariants or invariants of a single state observation. In both cases, invariants are broken only by nontermination. We use these general results later to characterise our theory for the SCJ memory model.

4.1. Operation invariants

For an operation invariant defined by a predicate Ψ (a binary relation on before and after states), the subtheory of designs that satisfy this invariant is characterised by the healthiness condition **OIH**(operation-invariant healthiness).

 $OH(\Psi) \qquad D = D \land (ok \land \neg D^f \Rightarrow \Psi)$

An **OIH**(Ψ)-healthy design ensures that, when its precondition holds, so does Ψ .

Theorem 4.1 *OIH*(Ψ) *is a monotonic idempotent function on designs.*

Proof. First, we show that $OIH(\Psi)(D)$ is a design.

 $\begin{array}{l} \boldsymbol{OIH}(\Psi)(D) \\ = (\neg D^{f} \vdash D^{i}) \land (ok \land \neg D^{f} \Rightarrow \Psi) \\ = (ok \land \neg D^{f} \Rightarrow ok^{i} \land D^{i}) \land (ok \land \neg D^{f} \Rightarrow \Psi) \\ = \neg D^{f} \vdash D^{i} \land \Psi \end{array}$ [property of designs and definition of $\boldsymbol{OIH}(\Psi)$] [definition] [propositional calculus and definition of designs]

Since $ok \wedge \neg (\neg D^f \vdash D^t \wedge \Psi)^f = ok \wedge \neg D^f$, then **OIH**(Ψ) is idempotent. Finally, to establish monotonicity, we consider designs D_1 and D_2 such that $D_1 \Rightarrow D_2$. That **OIH**(Ψ)(D_1) \Rightarrow **OIH**(Ψ)(D_2), follows from $\neg D_2^f \Rightarrow \neg D_1^f$.

We define the healthy identity $\Pi_{OI}(\Psi) \cong OH(\Psi)(\Pi)$. For reflexive Ψ , that is, for those such that, for every v, $\Psi[v/v']$, we have that $\Pi_{OI}(\Psi)$ is the sequence left unit.

Theorem 4.2 If Ψ is reflexive, $\mathbb{I}_{OI}(\Psi)$; D = D, for every **OIH**(Ψ)-healthy D.

Proof.

$II_{OI}(\Psi)$; D	
$= OIH(\Psi)(\Pi); OIH(\Psi)(D)$	[definition of Π_{OI} and D is OIH (Ψ)-healthy]
$= (\textit{true} \vdash v' = v \land \Psi); (\neg D^f \vdash D^t \land \Psi)$	[Theorem 4.1]
$=\neg (v' = v \land \Psi ; D^{f}) \vdash v' = v \land \Psi ; D^{t} \land \Psi$	[design sequence closure]
$= \neg (\Psi[v/v'] \land D^f) \vdash \Psi[v/v'] \land D^t \land \Psi$	[sequence one-point rule]
$= \neg D^f \vdash D^t \land \Psi$	$[\Psi \text{ reflexive}]$
= D	[definition of design, Theorem 4.1, and D is $OH(\Psi)$ -healthy]

Lemma 4.1 If Ψ is reflexive, D; $\Pi_{OI}(\Psi) = (\neg \exists v' \bullet D^f \vdash D^t \land \Psi)$.

Proof.

[definition of I_{OI} and D is OIH (Ψ)-healthy]
[Theorem 4.1]
[sequence design closure]
[relational calculus]
[sequence one-point rule]
$[\Psi \text{ is reflexive}]$

 $O(H(\Psi))$ is closed with respect to conjunction, disjunction (which models nondeterminism) and conditional. These are established by the following theorems.

Theorem 4.3 *OIH*(Ψ) *is closed with respect to conjunction.*

Proof.

 $D_1 \wedge D_2$ $= OIH(\Psi)(D_1) \wedge OIH(\Psi)(D_2)$ $= D_1 \wedge (ok \wedge \neg D_1^f \Rightarrow \Psi) \wedge D_2 \wedge (ok \wedge \neg D_2^f \Rightarrow \Psi)$ $= D_1 \wedge D_2 \wedge ((ok \wedge \neg D_1^f) \vee (ok \wedge \neg D_2^f) \Rightarrow \Psi)$ $= D_1 \wedge D_2 \wedge (ok \wedge \neg (D_1 \wedge D_2)^f \Rightarrow \Psi)$ $= OIH(\Psi)(D_1 \wedge D_2)$

Theorem 4.4 *OIH*(Ψ) is closed with respect to disjunction.

Proof.

 $D_1 \vee D_2$ $= OIH(\Psi)(D_1) \vee OIH(\Psi)(D_2)$ [Theorem 4.1] $= (\neg D_1^f \vdash D_1^t \land \Psi) \lor (\neg D_2^f \vdash D_2^t \land \Psi)$ $= (\neg D_1^f \land \neg D_2^f \vdash (D_1^t \land \Psi) \lor (D_2^t \land \Psi))$ $= (\neg (D_1^f \lor \neg D_2^f) \vdash (D_1^t \lor D_2^t) \land \Psi)$ $= OIH(\Psi)(D_1 \vee D_2)$

Theorem 4.5 *OIH*(Ψ) *is closed with respect to conditional.* Proof.

 $D_1 \lhd b \triangleright D_2$ $= OIH(\Psi)(D_1) \triangleleft b \triangleright OIH(\Psi)(D_2)$ $[D_1 \text{ and } D_2 \text{ are } OH(\Psi) \text{-healthy}]$ $= (\neg D_1^f \vdash D_1^t \land \Psi) \lhd b \rhd (\neg D_2^f \vdash D_2^t \land \Psi)$ [Theorem 4.1] $= (\neg D_1^f \triangleleft b \rhd \neg D_2^f) \vdash (D_1^t \land \Psi \triangleleft b \rhd D_2^t \land \Psi)$ [design conditional closure] $= \neg (D_1^f \triangleleft b \triangleright D_2^f) \vdash (D_1^t \land \Psi \triangleleft b \triangleright D_2^t \land \Psi)$ [conditional negation] $= \neg (D_1^f \triangleleft b \triangleright D_2^f) \vdash (D_1^t \triangleleft b \triangleright D_2^t) \land \Psi$ [conditional conjunction] $= \neg (D_1^f \triangleleft b^f \triangleright D_2^f) \vdash (D_1^t \triangleleft b^t \triangleright D_2^t) \land \Psi$ [ok' not free in b] $= \neg (D_1 \lhd b \rhd D_2)^{f} \vdash (D_1 \lhd b \rhd D_2)^{t} \land \Psi$ [substitution] $= OIH(\Psi)(D_1 \lhd b \triangleright D_2)$ [Theorem 4.1]

For **OIH**(Ψ) to be closed with respect to sequence, we need Ψ to be transitive, that is, (Ψ ; Ψ) $\Rightarrow \Psi$. To prove that, we need the lemma below; it confirms that the postcondition of $P \vdash Q$ is indeed Q.

Lemma 4.2 $(P \vdash Q)^t = Q$, provided $ok \land P$ and ok' is not free in P.

Proof. $(P \vdash O)^t$ $= (ok \land P \Rightarrow ok' \land Q)^{t}$ [definition of designs] $= ok \land P \Rightarrow Q$ [ok' is not free in P and propositional calculus] $[ok \land P \text{ and propositional calculus}]$ Q

[by D_1 and D_2 are **OIH**(Ψ)-healthy] [definition of $OIH(\Psi)$] [propositional calculus] [propositional calculus] [definition of $OIH(\Psi)$]

> [definition of $OIH(\Psi)$] [design disjunction closure] [propositional calculus] [Theorem 4.1]

Finally, the theorem below establishes closure with respect to sequence.

Theorem 4.6 If D_1 and D_2 are **OIH**(Ψ)-healthy designs, and Ψ is transitive, then D_1 ; D_2 is **OIH**(Ψ)-healthy. *Proof.*

 $\begin{array}{ll} D_1 ; D_2 \\ = \neg (D_1 ; D_2)^f \vdash (D_1 ; D_2)^t & [design property] \\ = \neg (D_1 ; D_2)^f \vdash D_1^t ; D_2^t & [design sequence postcondition] \\ = \neg (D_1 ; D_2)^f \vdash D_1^t \land \Psi ; D_2^t \land \Psi & [D_1, D_2 \text{ are } \textit{OIH-healthy}] \\ = \neg (D_1 ; D_2)^f \vdash (D_1^t \land \Psi ; D_2^t \land \Psi) \land \Psi & [\Psi \text{ transitive}] \\ = \neg (D_1 ; D_2)^f \vdash (D_1^t ; D_2^t) \land \Psi & [D_1, D_2 \text{ are } \textit{OIH-healthy}] \\ = \neg (D_1 ; D_2)^f \vdash (D_1 ; D_2^t) \land \Psi & [D_1, D_2 \text{ are } \textit{OIH-healthy}] \\ = \neg (D_1 ; D_2)^f \vdash (D_1 ; D_2^t) \land \Psi & [design sequence postcondition] \\ = \textit{OIH}(D_1 ; D_2) & [Theorem 4.1] \end{array}$

The set of $OIH(\Psi)$ -healthy designs is a complete lattice, since it is the image of a monotonic idempotent healthiness condition [HH98], so recursion can still be defined using weakest fixed-points. The bottom and top of the lattice are the same as that for the lattice of designs: abort (that is, the design (*false* \vdash *true*), which is equal to (*false* $\vdash \Psi$)) and magic (*true* \vdash *false*).

4.2. State invariants

For a state invariant defined by a condition ψ , the subtheory of designs whose input variables satisfy ψ is characterised by the following healthiness condition, **ISH** (input-state healthiness).

 $ISH(\psi) \qquad D = D \lor (ok \land \neg D^f \land \psi \Rightarrow ok' \land D^t)$

Theorem 4.7 *ISH*(ψ) *is an idempotent function on designs.*

Proof. First, we show that $ISH(\psi)(D)$ is a design.

$$\begin{split} & ISH(\psi)(D) \\ &= (\neg D^f \vdash D^t) \lor (ok \land \neg D^f \land \psi \Rightarrow ok' \land D^t) & [\text{property of designs and definition of } ISH(\Psi)] \\ &= (ok \land \neg D^f \Rightarrow ok' \land D^t) \lor (ok \land \neg D^f \land \psi \Rightarrow ok' \land D^t) & [\text{definition}] \\ &= \neg ok \lor D^f \lor \neg \psi \lor ok' \land D^t & [\text{propositional calculus}] \\ &= \neg D^f \land \psi \vdash D^t & [\text{propositional calculus and definition of designs}] \end{split}$$

The arguments for idempotence and monotonicity are similar to those used in Theorem 4.1. \Box

We define the healthy identity $\pi_{IS}(\psi) \cong ISH(\psi)(\pi)$. It is indeed the left-unit of sequence; the proof of this result is a simple consequence of the definitions of $\pi_{IS}(\psi)$ and sequence, and Theorem 4.7 above.

Theorem 4.8 $\Pi_{IS}(\psi)$; D = D, for every **ISH**(ψ)-healthy D.

Proof.

$$\begin{split} & \Pi_{IS}(\psi) \text{ ; } D \\ &= ISH(\psi)(\Pi) \text{ ; } ISH(\psi)(D) & [\text{definition of } \Pi_{IS}(\psi) \text{ and } D \text{ is } ISH(\psi)\text{-healthy}] \\ &= (\psi \vdash v' = v) \text{ ; } (\neg D^f \land \psi \vdash D^t) & [\text{Theorem 4.7}] \\ &= \neg (\neg \psi \text{ ; } true) \land \neg ((v' = v) \text{ ; } \neg (\neg D^f \land \psi)) \vdash (v' = v) \text{ ; } D^t & [\text{sequence of designs}] \\ &= \psi \land \neg (D^f \lor \neg \psi) \vdash D^t & [\text{predicate calculus}] \\ &= \neg D^f \land \psi \vdash D^t & [\text{propositional calculus}] \\ &= ISH(\psi)(D) & [Theorem 4.7] \\ D & [D \text{ is } ISH(\psi)\text{-healthy}] \end{split}$$

Again, right unit does not hold in all cases.

Theorem 4.9 D; $\Pi_{IS}(\psi) = \neg (\exists v' \bullet D^f) \land \psi \land \neg (D^t; \neg \psi) \vdash D^t$, for every **ISH**(ψ)-healthy D. *Proof.*

 $D ; \Pi_{IS}(\psi) = ISH(\psi)(D) ; ISH(\psi)(\Pi) \qquad [definition of \Pi_{IS}(\psi) and D is ISH(\psi)-healthy] = (\neg D^f \land \psi \vdash D^t) ; (\psi \vdash v' = v) \qquad [Theorem 4.7] = \neg ((D^f \lor \neg \psi) ; true) \land \neg (D^t ; \neg \psi) \vdash D^t ; (v' = v) \qquad [sequence of designs] = \neg (\exists v' \bullet D^f \lor \neg \psi) \land \neg (D^t ; \neg \psi) \vdash \exists v_0 \bullet D^t[v_0/v'] \land v' = v_0 \qquad [sequence] = \neg (\exists v' \bullet D^f) \land \psi \land \neg (D^t ; \neg \psi) \vdash D^t$

So, not only does the precondition have to have no dashed variables, but the postcondition cannot break the invariant. The invariant ψ is assumed by all programs, including the identity. It aborts if the previous program does not establish ψ .

To establish the result for conjunction, we need the following lemma, which shows us that the precondition of a design $P \vdash Q$ is indeed P.

Lemma 4.3 $ok \land \neg (P \vdash Q)^f = ok \land P$, provided ok' is not free in P.

Proof.

$$ok \land \neg (P \vdash Q)^f$$
[definition of designs] $= ok \land \neg (ok \land P \Rightarrow ok' \land Q)^f$ [definition of designs] $= ok \land \neg \neg (ok \land P)$ [ok' is not free in P and propositional calculus] $= ok \land P$ [propositional calculus]

 $ISH(\psi)$ is closed with respect to conjunction, disjunction, conditional, and sequence. This is established by the following four theorems.

Theorem 4.10 *ISH*(ψ) *is closed with respect to conjunction.*

Proof.

$$\begin{array}{ll} D_1 \wedge D_2 \\ = \textit{ISH}(\psi)(D_1) \wedge \textit{ISH}(\psi)(D_2) & [by \ D_1 \ and \ D_2 \ are \ \textit{ISH}(\psi)-healthy] \\ = (\neg D_1^f \wedge \psi \vdash D_1^t) \wedge (\neg D_2^f \wedge \psi \vdash D_2^t) & [Theorem \ 4.7] \\ = ((\neg D_1^f \wedge \psi) \vee (\neg D_2^f \wedge \psi) \vdash (\neg D_1^f \wedge \psi \Rightarrow D_1^t) \wedge (\neg D_2^f \wedge \psi \Rightarrow D_2^t)) & [conjunction \ of \ designs] \\ = ((\neg D_1^f \vee \neg D_2^f) \wedge \psi \vdash \psi \Rightarrow (\neg D_1^f \Rightarrow D_1^t) \wedge (\neg D_2^f \Rightarrow D_2^t)) & [propositional \ calculus] \\ = ((\neg D_1^f \vee \neg D_2^f) \wedge \psi \vdash (\neg D_1^f \Rightarrow D_1^t) \wedge (\neg D_2^f \Rightarrow D_2^t)) & [property \ of \ designs] \\ = \textit{ISH}(\psi)((\neg D_1^f \vee \neg D_2^f) \vdash (\neg D_1^f \Rightarrow D_1^t) \wedge (\neg D_2^f \Rightarrow D_2^t)) & [Theorem \ 4.7 \ and \ Lemma \ 4.3] \\ = \textit{ISH}(\psi)(D_1 \wedge D_2) & [conjunction \ of \ designs] \end{array}$$

Theorem 4.11 *ISH*(ψ) *is closed with respect to disjunction.*

Proof.

$$D_1 \lor D_2$$

= **ISH**(ψ)(D_1) \lor **ISH**(ψ)(D_2) [by D_1 and D_2 are **ISH**(ψ)-healthy]
= $(\neg D_1^f \land \psi \vdash D_1^t) \lor (\neg D_2^f \land \psi \vdash D_2^t)$ [Theorem 4.7]

$$= (\neg D_1^f \land \neg D_2^f \land \psi \vdash D_1^t \lor D_2^t)$$

= ISH(\psi)(\gamma D_1^f \land \neg D_2^f \vdash D_1^t \lor D_2^t)
= ISH(\psi)((\neg (D_1 \lor \neg D_2)^f \vdash (D_1 \lor D_2)^t))
= ISH(\psi)(D_1 \vee D_2)

Theorem 4.12 *ISH*(ψ) *is closed with respect to conditional.*

Proof.

$$\begin{split} D_1 \lhd b \rhd D_2 \\ = ISH(\psi)(D_1) \lhd b \rhd ISH(\psi)(D_2) & [D_1 \text{ and } D_2 \text{ are } ISH(\Psi)\text{-healthy}] \\ = (\neg D_1^f \land \psi \vdash D_1^t) \lhd b \rhd (\neg D_2^f \land \psi \vdash D_2^t) & [Theorem 4.7] \\ = ((\neg D_1^f \land \psi) \lhd b \rhd (\neg D_2^f \land \psi) \vdash D_1^t \lhd b \rhd D_2^t) & [conditional of designs] \\ = ((\neg D_1^f \lhd b \rhd \neg D_2^f) \land \psi \vdash D_1^t \lhd b \rhd D_2^t) & [propositional calculus] \\ = ISH(\psi)(\neg D_1^f \lhd b \rhd \neg D_2^f \vdash D_1^t \lhd b \rhd D_2^t) & [Theorem 4.7] \\ = ISH(\psi)((\neg D_1 \lhd b \rhd \neg D_2)^f \vdash (D_1 \lhd b \rhd D_2)^t) & [propositional calculus] \\ = ISH(\psi)(D_1 \lhd b \rhd D_2) & [conditional of designs] \end{split}$$

Theorem 4.13 If D_1 is an **ISH**(ψ)-healthy design, then D_1 ; D_2 is **ISH**(ψ)-healthy.

Proof.

$D_1; D_2$	
$= ISH(\psi)(D_1) ; D_2$	$[D_1 \text{ is } \textbf{ISH}(\psi)\text{-healthy}]$
$= (\neg D_1^f \land \psi \vdash D_2^t) ; (\neg D_2^f \vdash D_2^t)$	[Theorem 4.7]
$= \neg ((D_1^f \lor \neg \psi); true) \land \neg (D_1^t; D_2^f) \vdash D_1^t; D_2^t$	[sequence of designs]
$= \neg (\neg \psi \lor (D_1^f; true)) \land \neg (D_1^t; D_2^f) \vdash D_1^t; D_2^t$	[relational calculus, ψ is a condition]
$=\psi \land \neg (D_1^f ; \textbf{true}) \land \neg (D_1^t ; D_2^f) \vdash D_1^t ; D_2^t$	[propositional calculus]
$= \textbf{ISH}(\psi)(\neg (D_1^f; \textbf{true}) \land \neg (D_1^t; D_2^f) \vdash D_1^t; D_2^t)$	[Theorem 4.7]
$= ISH(\psi)((\neg D_1^f \vdash D_2^t); (\neg D_2^f \vdash D_2^t))$	[design sequence]
$= ISH(\psi)(D_1; D_2)$	[designs]

The bottom of the lattice that it defines is abort, but the top is ($\psi \vdash false$). This is miraculous only when ψ holds.

The subtheory of designs whose output variables satisfy ψ' is characterised by the following healthiness condition, **OSH** (output-state healthiness). The predicate ψ' is that obtained by substituting all output alphabet variables for their input counterparts in ψ .

$$OSH(\psi) \qquad D = D \land (ok \land \neg D^f \land \psi \Rightarrow \psi')$$

We observe that $OSH(\psi)$ can be defined as $OIH(\psi \Rightarrow \psi')$, and that $\psi \Rightarrow \psi'$ is reflexive and transitive. So, it satisfies all the properties discussed in the previous section. Most importantly, as shown below, $ISH(\psi)$ and $OSH(\psi)$ commute.

[disjunction of designs] [Theorem 4.7] [propositional calculus] [disjunction of designs] **Theorem 4.14** *ISH*(ψ) and *OSH*(ψ) commute.

Proof.

$ISH(\psi) \circ OSH(\psi)(D)$	
$= ISH(\psi) \circ OIH(\psi \Rightarrow \psi')(D)$	[observation: $OSH(\psi) = OIH(\psi \Rightarrow \psi')$]
$= ISH(\psi)(\neg D^f \vdash D^t \land (\psi \Rightarrow \psi'))$	[Theorem 4.1]
$= \neg D^{f} \land \psi \vdash D^{t} \land (\psi \Rightarrow \psi')$	[Theorem 4.7]
$= \neg D^f \land \psi \vdash D^t \land \psi'$	[property of designs]
$= OSH(\psi)(\neg D^f \land \psi \vdash D^t)$	[Theorem 4.1]
$= \textbf{OSH}(\psi) \circ \textbf{ISH}(\psi)(D)$	[Theorem 4.7]

As shown above, an $ISH(\psi)$ and $OSH(\psi)$ -healthy design D can be written as $(\neg D^f \land \psi \vdash D^t \land \psi')$, so that ψ is assumed and established. Since $ISH(\psi)$ and $OSH(\psi)$ are idempotent, by Theorem 4.14, so is $SIH(\psi) \cong ISH(\psi) \circ OSH(\psi)$ [HH98]; this is our healthiness condition for a theory with state invariant ψ .

When healthiness functions C1 and C2 commute, then every predicate that is $(C1 \circ C2)$ -healthy is also C1 and C2-healthy.

Theorem 4.15 If $C1 \circ C2(P) = P$, then C1(P) = P and C2(P) = P, provided C1 and C2 commute.

Proof.

C1

[P is C1 • C2 -healthy]
[C1 is idempotent]
[P is C1 o C2 -healthy]

C2

C2 (P)	
$= \mathbf{C2}(\mathbf{C1}(\mathbf{C2}(P)))$	[P is C1 \circ C2 -healthy]
$= \mathbf{C2}(\mathbf{C2}(\mathbf{C1}(P)))$	[C1 ∘ C2 = C2 ∘ C1]
$= \mathbf{C2}(\mathbf{C1}(P))$	[C2 is idempotent]
= P	[P is $C1 \circ C2$ -healthy and $C1 \circ C2 = C2 \circ C1$]

From this and the theorems above and in Sect. 4.1, we can conclude that $SIH(\psi)$ distributes through conjunction, disjunction, and conditional.

Theorem 4.16 For any operator **op**, and healthiness conditions **C1** and **C2** closed with respect to **op**, if they commute, then **C1** \circ **C2** is closed as well.

Proof.

P op Q	
= C1(P op Q)	[P and Q are C1 -healthy (Theorem 4.15) and C1 is closed with respect to op]
= C1(C2(P op Q))	[P and Q are $C2$ -healthy (Theorem 4.15) and $C2$ is closed with respect to op]
$= \mathbf{C1} \circ \mathbf{C2}(P \text{ op } Q)$	[function composition]

For operation and state invariants Ψ_1 and ψ_2 , *OIH*(Ψ_1) and *SIH*(ψ_2) commute.

Theorem 4.17 *OIH*(Ψ_1) and *SIH*(ψ_2) commute.

Proof.

$$OIH(\Psi_1) \circ SIH(\psi_2)(D) \qquad [Theorem 4.14]$$

$$= OIH(\Psi_1)(\neg D^f \land \psi_2 \vdash D^t \land \psi'_2) \qquad [Theorem 4.1 and Lemmas 4.3 and 4.2]$$

$$= SIH(\psi_2)(\neg D^f \vdash D^t \land \Psi_1) \qquad [Theorem 4.14 and Lemmas 4.3 and 4.2]$$

$$= SIH(\Psi_2) \circ OIH(\psi_1)(D) \qquad [Theorem 4.1]$$

So, using an argument similar to that above, we can conclude that a theory characterised by
$$\frac{1}{2}$$

 $\textit{IH}(\Psi_1,\psi_2) \ \widehat{=} \ \textit{OIH}(\Psi_1) \circ \textit{SIH}(\psi_2)$

is closed with respect to conjunction, disjunction, conditional, and sequence. The same applies to theories characterised by two operation invariants Ψ_1 and Ψ_2 ; **OIH**(Ψ_1) and **OIH**(Ψ_2) commute, and define a theory with invariant $\Psi_1 \wedge \Psi_2$. A similar result holds for state invariants ψ_1 and ψ_1 . The UTP theory for the SCJ memory model presented in the next section combines several operation and state invariants.

The identity of a $H(\Psi_1, \psi_2)$ theory is $\pi_{IH}(\Psi_1, \psi_2) \cong H(\Psi_1, \psi_2)(\pi)$. It is the left unit of sequence, but not the right unit.

Theorem 4.18 If Ψ_1 is reflexive, $\Pi_{IH}(\Psi_1, \psi_2)$; D = D, for every $IH(\Psi_1, \psi_2)$ -healthy D.

Proof.

$$\begin{aligned} \pi_{IH}(\Psi_{1}, \psi_{2}) & ; D \\ &= IH(\Psi_{1}, \psi_{2})(\pi) & ; IH(\Psi_{1}, \psi_{2})(D) & [definition of IH(\Psi_{1}, \psi_{2})(\pi), D \text{ is } IH(\Psi_{1}, \psi_{2})-\text{healthy}] \\ &= (\psi_{2} \vdash v' = v \land \psi'_{2} \land \Psi_{1}) & ; (\neg D^{f} \land \psi_{2} \vdash D^{t} \land \psi'_{2} \land \Psi_{1}) & [Theorem 4.17] \\ &= \begin{pmatrix} \psi_{2} \land \neg ((v' = v \land \psi'_{2} \land \Psi_{1}) & ; (D^{f} \lor \neg \psi_{2})) \\ \vdash & \\ (v' = v \land \psi'_{2} \land \Psi_{1}) & ; (D^{t} \land \psi'_{2} \land \Psi_{1}) \end{pmatrix} & [sequence of designs] \\ &= \begin{pmatrix} \psi_{2} \land \neg (\psi_{2} \land \Psi_{1}[v/v'] \land (D^{f} \lor \neg \psi_{2})) \\ \vdash & \\ \psi_{2} \land \Psi_{1}[v/v'] \land D^{t} \land \psi'_{2} \land \Psi_{1} & [\Psi_{1} \text{ is reflexive}] \\ &= \psi_{2} \land \neg (\psi_{2} \land (D^{f} \lor \neg \psi_{2})) \vdash \psi_{2} \land D^{t} \land \psi'_{2} \land \Psi_{1} & [\Psi_{1} \text{ is reflexive}] \\ &= \psi_{2} \land \neg D^{f} \vdash D^{t} \land \psi'_{2} \land \Psi_{1} & [Propositional calculus and designs] \\ &= IH(\Psi_{1}, \psi_{2})(D) & [Theorem 4.17] \\ &= D & [D \text{ is } IH(\Psi_{1}, \psi_{2})-\text{healthy}] \end{aligned}$$

Again, right unit does not hold in all cases.

Theorem 4.19 D; $\Pi_{IH}(\Psi_1, \psi_2) = \neg (\exists v' \bullet D^f) \land \psi \land \neg (D^t; \neg \psi) \vdash D^t$, for every $IH(\Psi_1, \psi_2)$ -healthy D. *Proof.*

$$D ; \pi_{IH}(\Psi_1, \psi_2)$$

$$= IH(\Psi_1, \psi_2)(D) ; IH(\Psi_1, \psi_2)(\pi)$$
[definition of $\pi_{IH}(\Psi_1, \psi_2)$ and D is $IH(\Psi_1, \psi_2)$ -healthy]
$$= (\neg D^f \land \psi_2 \vdash D^t \land \psi'_2 \land \Psi_1) ; (\psi_2 \vdash v' = v \land \psi'_2 \land \Psi_1)$$
[Theorem 4.17]
$$= \begin{pmatrix} \neg ((D^f \lor \neg \psi_2) ; true) \land \neg ((D^t \land \psi'_2 \land \Psi_1) ; \neg \psi_2) \\ \vdash \\ (D^t \land \psi'_2 \land \Psi_1) ; (v' = v \land \psi'_2 \land \Psi_1) \end{pmatrix}$$
[Example 2.17]

[sequence of designs]

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$$= \begin{pmatrix} \neg (\exists v' \bullet D^{f} \lor \neg \psi_{2}) \land \neg (\exists v' \bullet D^{t} \land \psi_{2}' \land \Psi_{1} \land \neg \psi_{2}') \\ \vdash \\ \exists v_{0} \bullet D^{t}[v_{0}/v'] \land \psi_{2}'[v_{0}/v'] \land \Psi_{1}[v_{0}/v'] \land v' = v_{0} \land \psi_{2}' \land \Psi_{1}[v_{0}/v] \end{pmatrix}$$
$$= \neg (\exists v' \bullet D^{f} \lor \neg \psi_{2}) \vdash D^{t} \land \psi_{2}' \land \Psi_{1} \land \Psi_{1}[v'/v]$$
$$= \neg (\exists v' \bullet D^{f}) \land \psi_{2} \vdash D^{t} \land \psi_{2}' \land \Psi_{1}$$

[sequence] [predicate calculus] $[\Psi_1 \text{ is reflexive}]$

The bottom of the lattice is abort, and the top is ($\psi_2 \vdash false$).

5. A theory for the Safety-Critical Java memory model

In this section, we consider first a theory that captures the structure of memory areas in SCJ. Afterwards, we extend it to take into account the execution model of the missions, releases of the handlers, and the creation of private temporary memory areas. This separation of concerns simplifies the presentation of our theory, and allows the treatment of various aspects of the model independently.

Type definitions The elements of the stacks (for the program, mission sequencer, and handlers) are frames, which define a context of execution for a method. To provide a model for a frame, we introduce the notion of a variable name as just an element of the unspecified set VName, and of a reference, from the set Ref. We also define the set of values as $Value = PValue \cup Ref$, where PValue is the unspecified set of primitive values and the special value null. With these, we can define $Frame = VName \rightarrow Value$, so that a frame is a partial function associating the names of the variables in scope to their values.

A function *refsIn* : *Frame* $\rightarrow \mathbb{F}$ *Ref* defines the finite set of references (to objects in a memory area) in the stack. It is defined as *refsIn f* = ran(*f* \triangleright *Ref*), using the range restriction operator \triangleright .

We identify a memory area with its contents; for simplicity, we do not capture issues related to size, although this could be done by extending our model. Concretely, we define the set $MAreaC = Ref \rightarrow OValue$ of memory contents, where OValue is the set of record (object) values: functions that associate fields to their values, that is, $OValue = VName \rightarrow Value$.

We also define two functions refsRes, refsIn : $MAreaC \rightarrow \mathbb{F}$ Ref. For a memory area ma, the set refsRes ma contains the references that identify objects that reside in ma. The references used in these objects (to refer to other objects in the same or in other memory areas) are those in refsIn ma. Precisely, refsRes ma = dom ma, and refsIn ma = $\bigcup (\operatorname{ran}((\ \rhd Ref))((\operatorname{ran} ma))))$. For a memory area ma (or more precisely, for the contents ma of a memory area), ran ma gives its objects. By using relational image $_(] _)$ to apply the operator $(_ \triangleright Ref)$ to all of them, we project out all their fields with a primitive or null value. The ranges of these objects are the references used in ma; distributed union provides a single set containing all of them.

In order to identify the handlers of a mission, we consider the set *HName*. It contains valid handler identifiers, or names.

The alphabet of our theory includes eight extra observational variables defined below, and their dashed counterparts, in addition to ok, ok', and the programming variables (and their dashed counterparts). We have also ten healthiness conditions, which are also specified and discussed in the sequel.

Alphabet First, we have the stacks *pStack*, *msStack* : stack *Frame* for the program and the mission sequencer. The set *handlers* : \mathbb{F} *HName* records the handlers of the current mission, and the variable

$hStack : handlers \rightarrow stack Frame$

groups their stacks as a total function associating each handler to its stack.

To record the memory areas, we have first *immortal*, *mission* : *MAreaC*. The per-release memory areas are grouped in perR : *handlers* \rightarrow *MAreaC*. The temporary private memory areas are organised in a stack as recorded in the alphabet variable tPriv : handlers \rightarrow stack *MAreaC*. A simple model for a stack is, of course, a sequence, whose last element is the top of the stack.

A stacked temporary private memory area is called a parent in relation to all those areas of the same handler that are stacked afterwards. More generally, the immortal memory area is the parent of the mission memory area, which is a parent of all per-release memory areas. Additionally, the per-release memory area of a handler is a parent of all its stacked temporary private memory areas.

Healthiness conditions We can only add object values to the immortal area. This is an operation invariant, and gives rise to our first healthiness condition **HSCJ1**. To define it, we introduce a function *profile* : $MAreaC \rightarrow (Ref \rightarrow \mathbb{F} VName)$. For a memory area *ma*, the function *profile ma* associates each reference residing in *ma* with the set of fields of the object that it identifies in *ma*. This is the domain of the function (in *OValue*) that defines that object. Formally, we have *profile ma* = { $r : \text{dom } ma \bullet r \mapsto \text{dom}(ma r)$ }. Our healthiness condition **HSCJ1** requires that the immortal memory is changed only by adding new references to its profile. Existing references remain, and the structure of the objects to which they point (as captured by their sets of field names) is preserved.

HSCJ1 \cong **OIH**(profile immortal \subseteq profile immortal')

The operation invariant for **HSCJ1** is reflexive and transitive, because \subseteq is.

The references in the program stack can target only objects in the immortal memory. This is specified by the healthiness condition **HSCJ2**, which uses a lifted version of *refsIn* : stack *Frame* $\rightarrow \mathbb{F}$ *Ref* that applies to stacks of frames *sf* (instead of frames or memory areas). We can define it in terms of the version of *refsIn* for frames as *refsIn sf* = \bigcup (*refsIn*(ran *sf*)). The range of *sf* is a set of frames; we use relational image to apply *refsIn* to all of them. The distributed union collects together all references occurring in all frames of *sf*.

HSCJ2
$$\hat{=}$$
 SIH(refsIn pStack \subseteq refsRes immortal)

Analogously, the references in the immortal memory can target only objects in the immortal memory itself. This is the state invariant specified below.

HSCJ3
$$\cong$$
 SIH(refsIn immortal \subseteq refsRes immortal)

Similarly, the references in the mission-sequencer stack and in the mission memory area are for objects either in the immortal or in the mission memory areas. To capture this healthiness condition, we define $refsRes : \mathbb{F} MAreaC \rightarrow \mathbb{F} Ref$, for a set of memory areas mas as $refsRes mas = \bigcup(refsRes(mas))$. It collects the references in each of the memory areas in mas.

 $HSCJ4 \cong SIH(refsIn msStack \subseteq refsRes \{immortal, mission\})$

$HSCJ5 \cong SIH(refsIn mission \subseteq refsRes \{immortal, mission\})$

For each handler, the references in its stack are for objects in its own temporary private areas, in its own per-release area, or in the mission or immortal memory.

$$HSCJ6 \stackrel{\widehat{}}{=} SIH \begin{pmatrix} \forall h : handlers \bullet \\ refsIn (hStack h) \subseteq \\ refsRes (\{innmortal, mission, perR h\} \cup ran(tPriv h)) \end{pmatrix}$$

For each handler, the references in its per-release memory area are for objects in that same area, or in the mission or immortal memory areas.

HSCJ7
$$\cong$$
 SIH $\begin{pmatrix} \forall h : handlers \bullet \\ refsIn (perR h) \subseteq refsRes\{immortal, mission, perR h\} \end{pmatrix}$

Finally, in a temporary private memory area of any handler, the references target objects that can be in the immortal memory, in the mission memory, in the associated per-release memory for the same handler, in a parent stacked area, or in that same temporary private memory area.

HSCJ8 $\hat{=}$

$$SIH \begin{pmatrix} \forall h : handlers; i : 1 ... \#(tPriv h) \bullet \\ refsIn (tPriv h i) \subseteq \\ refsRes(\{immortal, mission, perR h\} \cup \{j : 1 ... i \bullet tPriv h j\}) \end{pmatrix}$$

We use #s to denote the size of the sequence (or stack) s.

As already said, private temporary memory areas are created by calls to a method create. For every handler, therefore, the size of its frame stack is greater than the size of the stack of its temporary private memory areas.

HSCJ9 \cong **SIH**(\forall h : handlers • #(hStack h) > #(tPriv h))

Finally, the memory areas are disjoint in their use of the reference space.

HSCJ10 $\hat{=}$

SIH(disjoint $\langle refsRes immortal, refsRes mission \rangle \cap seqPR per R \cap seqTP tPriv)$

We use seqPR *perR* and seqTP *tPriv* to denote the sequences of sets of references residing in the per-release and temporary private memory areas in *perR* and *tPriv*. Precisely, we have that #seqPR *perR* = #handlers and

 $\exists b : (1 .. \# handlers) \rightarrow handlers \bullet$ $\forall i : 1 .. \# seqPR \ perR \bullet seqPR \ perR \ i = refsRes \ (perR \ (b \ i))$

We use $A \rightarrow B$ to refer to the set of bijective functions between A and B. Additionally, we define seqTP $tPriv = \frac{1}{(seqS \ tPriv)}$, to get the sequence of all sets of references residing in all memory areas in all stacks in tPriv. In seqS tPriv, we have a sequence of sequences of sets of references. We define that $\#seqS \ tPriv = \#handlers$ and additionally

 $\exists b : (1 .. \#handlers) \rightarrow handlers \bullet$ $\forall i : 1 .. \#seqS tPriv \bullet seqS tPriv i = (tPriv (b i) \ (b i) \$

We use $(tPriv (b i) \circ refsRes)$ to denote the composition of tPriv (b i) with the function refsRes. We observe that tPriv (b i) is a stack of memory areas, and so a function from natural numbers to memory areas. By composing this function with refsRes, we get a function from natural numbers to the sets of references residing in the memory areas. Because tPriv (b i) is a sequence, so is $tPriv (b i) \circ refsRes$. This is a sequence of sets of references. The distributed concatenation of all such sequences (operator \frown) defines seqTP tPriv.

Our theory contains the fixed points of the healthiness conditions above. They are the fixed points of **HSCJ**, which we define as the composition of all the healthiness functions. With the results in Sect. 4, we conclude that **HSCJ** is closed with respect to conjunction, disjunction, conditional, and sequence.

6. Programming variables and their values

Programming variables in the alphabet can be either specification or allocated variables. As the name suggests, specification variables are used only to write abstract definitions of the behaviour of programs (or their components). They model, for instance, inputs and outputs. Allocated variables, on the other hand, are included in one of the frames of one of the stacks.

Our next three healthiness conditions require that the value of every allocated variable in the alphabet is in accordance with what is recorded in the stacks. This is what links our mathematical account of a program, in terms of the names of the variables in its alphabet, with the way those variables are represented as values in memory. We have one healthiness condition for the program stack (*HV1*), one for the mission-sequencer stack (*HV2*), and one for the handlers stacks (*HV3*).

To define these conditions, we use a function *vars* : stack *Frame* $\rightarrow \mathbb{F}$ *VName* that characterises the set of active variables according to a given stack. These are the variables in the domains of the frames:

vars
$$sf = \lfloor \rfloor dom(ran sf)$$

provided there are no redeclarations, that is, disjoint $\{i : 1 . . \# sf \bullet i \mapsto \text{dom}(sf i)\}$. (As usual, we assume that variable names are not reused in declarations to avoid handling stacks of values for alphabet variables.)

The value of a variable vn (according to a stack sf and its associated memory areas mas) is characterised by a set A of sequences of variable names, and a function V that associates some of these sequences to primitive values. If the value associated with vn in sf is primitive or null, then $\langle vn \rangle$ is the only sequence in A. If, on the other hand, the value of vn is a reference (to an object), then we also have all the (possibly infinite) extensions of $\langle vn \rangle$ that identify a field of that object, or a field of one of its fields, and so on. For example, for the variable z in the stack s of Fig. 5, A contains only $\langle z \rangle$. In the case of x, we have $\langle x \rangle$, $\langle x, m \rangle$, $\langle x, n, u \rangle$, and so on. For clarity, in examples, we describe these sequences as z, x, x.m, x.n, x.m.u, and so on.

The function V maps sequences of variable names to values. The sequences describe addressing paths that identify a variable or an object field, each of which contain a primitive or null value. V maps these paths, which are names, to the values they contain. For instance, it maps the name z to 2, and it maps the path x.m.u to null.



Fig. 5. Example memory configuration

This characterisation of values is the same used in [CHW06, HCW08], where we have defined a UTP theory for the Java memory model that captures the structure of objects and sharing.

We introduce a dereferencing function to yield the values associated with a variable name, relative to a stack and some memory areas. This function returns a pair: the first element is the set of all valid paths that start from variable name; the second element is a mapping from those paths to particular values.

Formally, we define the value !(vn, sf, mas) of a variable name vn according to a stack sf and associated memory areas mas using the dereferencing function

!_: $VName \times \text{stack } Frame \times \mathbb{F} MAreaC \rightarrow \mathbb{P} SName \times (SName \twoheadrightarrow PValue)$

specified as !(vn, sf, mas) = (A(vn, sf, mas), V(vn, sf, mas)). Here, *SName* is the set of possibly infinite sequences of variable names (from *VName*). The set *SName* \Rightarrow *PValue* is that of the finite partial functions from *SName* to *PValue*. The set A(vn, sf, mas) is defined as shown below.

$$A(vn, sf, mas) = \begin{cases} sn : SName \mid \\ (head sn = vn \land \\ let u == sval(vn, sf) \bullet \\ u \in PValue \land tail sn = \langle \rangle \lor path(tail sn, \bigcup mas, u) \end{cases}$$

Here $sval(vn, sf) = (\bigcup(ran sf)) vn$ is the value of vn as recorded in sf. The condition path(sn, ma, r) requires that the sequence of variable names sn identifies a path in the memory area ma starting from the reference r. We use it above to make sure that the extensions of $\langle vn \rangle$ are in accordance with the information in the memory areas mas. With the assumption that they are disjoint, we consider $\bigcup mas$. The starting reference is the value u of vn in sf.

The formal definition of path(sn, ma, r) is as follows. We require the existence of a (possibly infinite) sequence sr of references that can be traversed using the sequence of names sn. The last value of sn, if any, might be a primitive value, rather than a reference, so the type of sr is SVal, the set of sequences of values.

$$path(sn, ma, r) \Leftrightarrow \left(\exists sr : SVal \bullet \begin{pmatrix} head \ sr = r \land \\ (sr \ i) \in \text{dom} \ sn \ \bullet \\ (sr(i+1) = ma \ (sr \ i) \ (sn \ i) \end{pmatrix} \right) \end{pmatrix} \right)$$

For each name sn i in sn, the corresponding value sr i in sr must be a reference in ma to an object ma (sr i) with a field named sn i. Additionally, the next value sr (i + 1) in sr must be the value ma (sr i) (sn i) of that field.

The definition of V(vn, sf, mas) is in many ways similar. In its domain, we have only finite sequences in A(vn, sf, mas): those that lead to a primitive value.

$$V(vn, sf, mas) = \begin{cases} sn : seq \ VName; \ pv : PValue \mid \\ (sn \in A(vn, sf, mas) \land \\ let \ u == sval(vn, sf) \bullet \\ u \in PValue \land pv = u \lor tpath(tail \ sn, \bigcup mas, u, pv) \end{cases} \end{cases}$$

The condition tpath(sn, ma, r, pv) requires that sn identifies a path in the memory area ma starting from r and terminating at the primitive value pv. Its formalisation is similar to that of path(sn, ma, r) as shown below.

$$tpath(sn, ma, r) \Leftrightarrow \begin{pmatrix} head \ sr = r \land \#sr = \#sn + 1 \land last \ sr = pv \\ \begin{pmatrix} \forall \ i : 1 \dots \#sn \bullet \\ (sn \ i) \in \text{dom}(ma \ (sr \ i)) \land \\ sr(i + 1) = ma \ (sr \ i) \ (sn \ i) \end{pmatrix} \end{pmatrix} \end{pmatrix}$$

In this case, we require the existence of a finite sequence sr of values, with r as its first element, pv as its last element, and that can be traversed using sn.

The condition HV1 requires that the value of every variable v in the program stack is given by *pStack* itself and its associated *immortal* area.

HV1
$$\stackrel{\frown}{=}$$
 SIH($\bigwedge v : vars(pStack) \bullet v = !(v, pStack, {immortal}))$

The definition takes the conjunction over all the variable names in the program stack, and makes sure that the value for this name is the same as the one we get by dereferencing the corresponding name. We make the distinction between v as a mathematical variable, which denotes a value (the occurrence on the left-hand side in the equation above), and v as the name of a program variable (the occurrence on the right-hand side in the equation). The healthiness conditions *HV2* and *HV3* are similar.

$$HV2 \cong SIH(\land v : vars(msStack) \bullet v = !(v, msStack, \{immortal, mission\}))$$

$$HV3 \stackrel{\widehat{}}{=} SIH \left(\begin{array}{c} \forall h : handlers \bullet (\bigwedge v : vars(hStack h) \bullet \\ v = !(v, hStack h, \{immortal, mission, perR h\} \cup ran (tPriv h))) \end{array} \right)$$

Implicitly, these conditions require that all the stack variables *v* are in the alphabet, since they are in the alphabet of the conjunctions.

We define *HV* as the composition of the functions *HV1–HV3*.

7. History of memory usage

What we have not captured so far is, for instance, the fact that during the life time of a mission, we can only add objects to the mission memory, until it is cleared at the end of the mission. For the immortal memory, we have **HSCJ1**, which defines *profile immortal* \subseteq *profile immortal*' as an invariant. It is not the case, however, that *profile mission* \subseteq *profile mission*', for example, is an invariant of our theory. Since *mission* is cleared when the mission is finished, and later used when a new mission is started, there is no guarantee that *mission*' is related to *mission* in every pair of observations of an SCJ program. The same comments apply to the per-release and private temporary memory areas in *perR* and *tPriv* in relation to the handler releases and the calls to the create method. Once created, we can only add objects to these areas, until they are cleared.

To establish the required properties, we keep information about the program flow, in terms of the start and end of missions, of handler releases, and of create methods. For that, we consider a set *Ident* of identifiers for missions, releases, and create calls. The special identifier *none* is used when there is no mission or release in execution. For create calls, we use $Ident_p = Ident \setminus \{none\}$.

History is recorded in our theory by three additional alphabet variables; with them, we can state five extra healthiness conditions. We add to the alphabet first an injective non-empty sequence *missions* : $iseq_1$ *Ident* of mission identifiers. Similarly, to record the history of handler releases during the current mission, we use a sequence *releases* : *handlers* \rightarrow iseq_1*Ident*.

Finally, to record the sequence of nested create calls during the current release of a handler, we use creates : handlers \rightarrow seq₁(iseq Ident_p). An injective sequence of identifiers (different from none), that is, an element of iseq Ident_p, is used to represent a particular configuration of the stack of nested calls: each call has a different identifier. As the stack grows and shrinks, the history of individual changes is recorded in a sequence, an element of seq₁(iseq Ident_p). We have in creates such a sequence for the current release of each of the handlers. If there is no current release, the stack of calls is, of course, empty.

Example 7.1 If we use natural numbers as identifiers, when in a program three missions have started and finished, the value of *missions* might be (1, 2, 3, none). If a fourth mission is then started, its value might become (1, 2, 3, 4).

If for a particular mission, for example, 4 above, we have handlers a, b, and c, the function *releases* takes the value $\{a \mapsto \langle none \rangle, b \mapsto \langle none \rangle, c \mapsto \langle none \rangle\}$ when none of the handlers have been released yet. Similarly, in this situation, *creates* takes the value $\{a \mapsto \langle \langle \rangle \rangle, b \mapsto \langle \langle \rangle \rangle, c \mapsto \langle \langle \rangle \rangle\}$, because if no handler has been released, then no calls to create can have been made.

If the handler *a*, for instance, is released a few times, for instance, twice, *handlers a* takes a value like $\langle 1, 2 \rangle$, if the second release is still active, or the value $\langle 1, 2, none \rangle$, if it is now finished. In this latter situation, *creates a* is again $\langle \langle \rangle \rangle$, because there can be no current calls to create.

If *a* is then released for a third time, so that *handlers a* takes the value like $\langle 1, 2, 3 \rangle$, for example, and during this release, it makes two nested calls to create, then *creates a* takes the value $\langle \langle \rangle, \langle 1 \rangle, \langle 1, 2 \rangle \rangle$. If the second nested call is finished, and then a new call is made, the history record *creates a* is expanded to $\langle \langle \rangle, \langle 1 \rangle, \langle 1, 2 \rangle, \langle 1, 2, 3 \rangle, \langle 1, 2, 4 \rangle \rangle$. If all calls are finished, and a new fresh call is made, it evolves to $\langle \langle \rangle, \langle 1 \rangle, \langle 1, 2 \rangle, \langle 1, 2, 3 \rangle, \langle 1, 2, 4 \rangle \rangle$.

As said above, if there is no current mission, the last element of *missions* is the identifier *none*. In this case, the mission memory is empty, and there are no handlers. This is captured by our next healthiness condition **HH1**.

HH1 $\widehat{=}$ **SIH**(last missions = none \Rightarrow refsRes mission = $\emptyset \land$ handlers = \emptyset)

History records can only ever grow: history information should not be lost or altered. We do not keep in the record, however, the *none* identifiers. The following relation $s_1 \leq_H s_2$ between sequences of identifiers establishes when s_2 is a proper extension of the history record in s_1 .

 $s_1 \leq_H s_2 \cong \text{front } s_1 \leq s_2 \land \text{last } s_1 \neq \text{none} \Rightarrow \text{last } s_1 = s_2(\#s_1)$

The next healthiness condition uses this relation to restrict changes to missions.

HH2 $\widehat{=}$ **OIH**(missions \leq_H missions')

Reflexivity and transitivity of this invariant follows from that of \leq_H .

Finally, if the mission history does not change, that is, during the execution of a particular mission, we can only ever add objects to *mission*, the set of *handlers* does not change, and the history of releases is preserved.

HH3 $\stackrel{\frown}{=}$ **OIH** $\begin{pmatrix} missions = missions' \Rightarrow \\ profile mission \subseteq profile mission' \land \\ handlers' = handlers \land \\ \forall h : handlers \bullet releases h \leq_H releases'h \end{pmatrix} \end{pmatrix}$

Transitivity follows from transitivity of \subseteq , equality, and \leq_H .

If a handler is currently not released, then its per-release memory area is empty, as is its stack of temporary private memory areas.

HH4
$$\widehat{=}$$
 SIH $\begin{pmatrix} \forall h : handlers \mid last(releases h) = none \bullet \\ refsRes (perR h) = \emptyset \land creates h = \langle \langle \rangle \rangle \end{pmatrix}$

To identify the valid history records of all changes to the stack of calls to the create method, we first define a relation between sequences from iseq $Ident_p$. It holds when only one change is necessary to obtain one from the other.

$$s_1 \sim_S s_2 \ \widehat{=} \ |\#s_1 - \#s_2| = 1 \land (s_1 < s_2 \lor s_2 < s_1)$$

Additionally, in a proper history record for a sequence of stacks, if a new element is added to the stack at any point, its identifier should be fresh. This is the property *histSS ss* defined below for sequences *ss* of stacks.

$$histSS \ ss \ \ \widehat{=} \ \left(\begin{array}{c} \forall \ i : i \dots \# ss - 1 \bullet \\ (ss \ i \ \sim_S ss \ (i+1) \land \\ \#(ss(i+1)) > \#(s \ i) \Rightarrow \\ last(s(i+1)) \notin \bigcup ran(ran \ ((1 \dots i) \lhd ss)) \end{array} \right) \right)$$

The next healthiness condition establishes that all sequences of stacks in the range of *creates* satisfy the *histSS* property.

HH5 $\widehat{=}$ **SIH**(\forall h : handlers • histSS (creates h))

The current number of nested calls to create determines the size of the stack of temporary private memory areas of the associated handler.

HH6
$$\widehat{=}$$
 SIH(\forall h : handlers • #(last(creates h)) = #(tPriv h))

Finally, during the same release of a handler h, the contents of its per-release memory area can only be increased (with the addition of new objects), and the history of create calls can only be extended. Additionally, the contents of every stacked temporary private memory area that remains across observations, namely, those that correspond to the create calls in the range of the last stack in both *creates* h and *creates* 'h, can only be increased.

$$OIH \left(\begin{array}{c} missions = missions' \Rightarrow \forall h : handlers | releases h = releases' h \bullet \\ profile (per R h) \subseteq profile (per R' h) \land creates h \leq creates' h \land \\ (\forall id : ran(last(creates h)) \cap ran(last(creates' h)); \\ i : \mathbb{Z} | i = pos(id, last(creates h)) \bullet \\ profile(tPriv h i) \subseteq profile(tPriv' h i) \end{array} \right) \right)$$

The function pos(e, s) determines the unique position in the injective sequence s in which the element e occurs. We observe that, when the previous healthiness conditions hold, pos(id, last(creates h)) is equal to pos(id, last(creates' h)).

We call the composition of the above healthiness functions *HH*.

8. Conclusions

To the best of our knowledge, we have presented here the only formal characterisation of the SCJ memory model available so far. This is an essential ingredient to justify the soundness of assertion-based static checking techniques (like that in [TPV10]). As a UTP theory, our model is also adequate for unification with existing models of concurrency, object orientation, and timing.

We reuse the ideas of an existing UTP model for objects and sharing [HCW08] to address the relationship between the structure established by the references in the memory areas and the values of the programming variables and attribute accesses. What we do not cover are features of models like [HH03, CS06]; these do not consider the issue of variable values, but provide support for reasoning about the memory graph structure. For SCJ, we will need to build on such techniques to take advantage of the separation enforced by the memory areas.

Another assertion-based technique proposed for SCJ is SafeJML [HHL10]. It extends the well-established JML [Bur+05] to cover functionality and timing properties. The focus is on annotations that allow the use of existing technology for worst-case execution-time analysis to reason about SCJ programs.

Another contribution of this paper is a general characterisation of subset theories of designs. With this, we have given an elegant definition for the SCJ theory. Our general results are useful for all theories for programs that do not exhibit special forms of termination, and do not provide guarantees on abortion.

Our model does not capture the flow of control of an SCJ program, as partially depicted in Fig. 2. This is the subject of ongoing work, which formalises the SCJ programming model in *Circus* [OCW09], a refinement language based on Z and CSP. The semantic model of *Circus* is based on UTP, and it is our plan to use the theory

presented here as basis for the design of an extension of *Circus* that is appropriate to reason about SCJ programs. The intended model of a complete SCJ program will be a predicate of the stateless CSP theory, just like that of a complete *Circus* program. So, it will have the form shown below, where the alphabet variables representing the memory structure are local.

var immortal, mission ...; P; end immortal, mission ...

In this case, *P* will be a predicate in the theory resulting from the embedding of the SCJ model presented here in the *Circus* theory of reactive designs. In the long run, we plan to provide a reasoning framework for SCJ programs that can cater for concurrency, object-orientation, time, and sharing.

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