

Identifying Safety Dependencies in Modular Computer Systems

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Abstract

The aviation industry has started to adopt complex distributed computing networks (known as Integrated Modular Avionics (IMA)). IMA raises a number of new issues for system development. Firstly, IMA is designed to use logical partitioning of data, both to provide secure access to resources and also to support incremental change of one component with minimal impact on other components. Secondly, an incremental approach to development is often needed for example to purchase an operating system prior to application development. To support this there is a need for a modular approach to safety analysis which not only provides assurance of system safety, but also provides a breakdown of responsibilities across logical boundaries. However, current safety analysis techniques are not well suited to a modular approach, treating software as a monolithic entity. This paper presents a method used to perform safety analysis on the module support layer (MSL) of an IMA system as part of a modular approach. The approach takes the system context into account, using system level hazard analysis and component interaction diagrams to assess the impact and cause of failures occurring in each component. We discuss the advantages of this approach, such as potential support for incremental change. However, we also highlight some difficulties associated with the component based approach particularly when considering the re-use of components such as the MSL.

Introduction

Over the past few years the aviation industry (both civil and military) has started to adopt an open distributed system known as Integrated Modular Avionics (IMA). This type of system contains a number of networked computer modules. The software within these modules is layered in order to separate applications, operating system and hardware specific code. This type of architecture provides many potential benefits such as incremental change or replacement of hardware and software components with minimal impact on the other components. It is useful for manufacturers to design and purchase the supporting software, such as an operating system, prior to the applications in order to provide an accurate development environment for the applications. This means safety analysis needs to be performed for the OS prior to the development of the applications. However, current safety analysis techniques do not support a staged or modular process for software safety assessment.

This paper presents a process which can be used to perform early safety analysis on the OS and Module Support Layers within an IMA system, deriving safety requirements based on the model and design of the final system including the applications. These safety requirements form a set of guarantees which a component in the system must meet in order for the fully integrated system to fulfil its safety requirements. These guarantees also form the basis for a set of "safety contracts" between the components. We demonstrate how this approach has been applied to the MSL within a commercial IMA system, showing how analysis of this component provided a set of requirements guarantees not only for the MSL but also for other components in the system.

This paper is laid out as follows. The first section contains a general introduction to IMA and IMA system being analysed. The next section presents a general process for analysis of individual IMA components. Then the results obtained when this process was applied to the Module Support Layer within the IMA system are presented. Finally, conclusions and further work are discussed.

Integrated Modular Avionics

IMA systems are often referred to as “open distributed systems”, where open implies that a set of common standard interfaces are used for components to interact. There are two main standards for IMA, a civil standard based on ARINC 653 [1], and a military standard from the ASA Council [2]. The IMA system analysed for this paper was based on the latter standard, therefore this is the version now introduced. The software architecture for a single computer process module is shown in Figure 1.

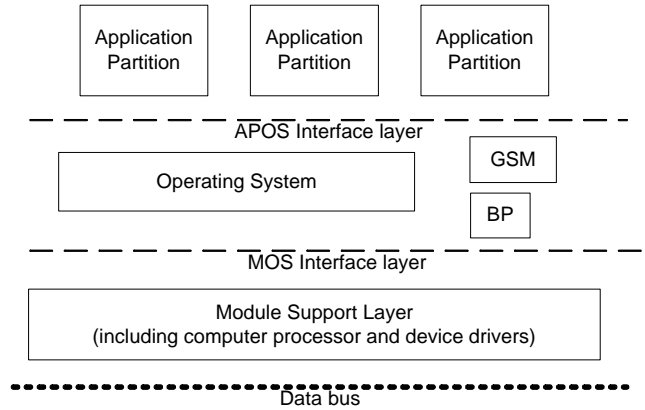


Figure 1 IMA Module

There are basically three layers within each module. At the bottom a layer of hardware specific software is used to control the computer hardware and to provide access to its resources. This is known as the Module Support Layer (MSL). The MSL also contains device drivers to connect to the data bus. Above this is an operating system layer. This consists of three components, the Operating System (OS), the General System Manager (GSM), and Blueprint manager (BP). The GSM provides health monitoring information. The BP controls the system layout information known as a blueprint. This includes each application's required resources, permit communications between applications and also scheduling requirements. The OS services requests from the applications and interacts with the MSL, using data from the GSM and BP in order to validate requests and also initialize the system. The OS communicates with the MSL using a fixed software interface. The advantage of this design is that new processing hardware can be inserted into the system, and as long as the new MSL has the same interface, the other layers can be moved to the new hardware without a need to alter the code. Above the OS layers are one or more application partitions. These contain software which performs avionics functions, such as navigation, or flight control. Again, a standard interface is used to communicate with the OS in order to permit change or replacement of components with minimal impact.

The IMA system analysed for this paper consisted of a small network of modules running a navigational display system. The precise details are not given here for reasons of commercial confidentiality, however enough detail is given to illustrate some key results from the analysis. Two key failure modes were identified at the system level, display of incorrect data and loss of display. These are associated with hazards such as controlled flight into terrain. These failures were partially mitigated by an independent display system and also pilot observation. The team developing the system wished to purchase the MSL software and hardware prior to the development of the application software. This meant that some safety analysis of the MSL was required in order to ensure system safety once all the components were developed and integrated.

Analysis Process

This section describes a generic process which can be used to assess individual components in the IMA system. The process is in three parts. Firstly system level analysis and decomposition is undertaken. Secondly analysis of individual components is undertaken. Finally, the Derived Safety Requirements (DSRs) found from the analysis are collated for each component.

The overall analysis process is summarised in Figure 2 and detailed below:

??Stage one:

- o Assemble system analysis team. It is useful to have representatives from each component supplier at the meeting to avoid misunderstandings and misrepresentations of components in the system.
- o Examine the system as a whole and undertake traditional analysis to establish system level hazards
- o Agree system design, producing a system decomposition model for each component. Note the role of each component at a high level of abstraction.
- o Assess the sequence of events and produce representation of event paths through components in order to assess which components have a direct impact on each other and assure system safety.
- o Assess the services each component provides in more detail as a pre-cursor analysis
- o State any assumptions made about components (e.g. if not designed yet)

??Stage two:

- o This stage applies to each component. Using any Failure Modes and Effects Analysis (FMEA) technique derive possible failure modes which could occur in the component. Assess effects of the failure modes at a system level as far as possible. It should be noted that causes of failures could be other components. When deriving safety requirements consider which component needs to address a particular failure. Any requirements for other components should be noted.

??Stage three:

- o Collate and consolidate derived safety requirements for each component (as shown in Figure 2). There will be three types of requirement:
 1. Requirements from this component's analysis which address internal failures.
 2. Requirements on this component which address external failures (in other components)
 3. Requirements on this component which are the results of another component's analysis.

Additionally, there may be "if – then" requirements where the results depend on the behaviour of another component.

- o Using unique identifiers for requirements, failures and components ensure those that cross component boundaries (categories 2 and 3 above)

listed along side the relevant derived requirements. This captures interdependencies between components. In addition these types of requirements provide the constraints for upgrading a component.

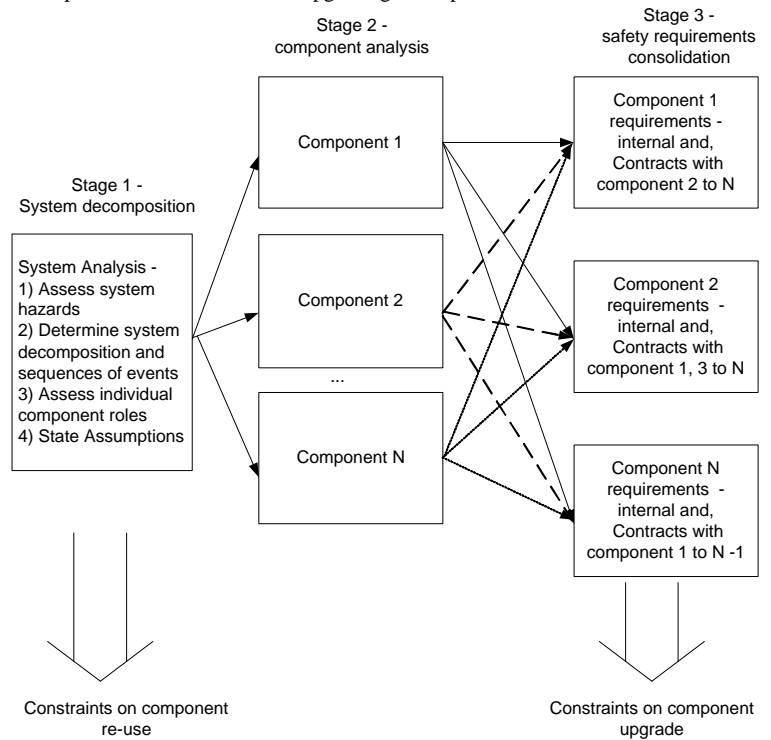


Figure 2 IMA Analysis process

Comments

Stage one of the process is designed to generate some context for the analysis of individual components. Then when stage two is undertaken the effects of component failure modes can be assessed at a system level. This is particularly important when performing analysis of a generic component such as the MSL as without any context it is impossible to determine whether a predicted failure mode is either credible or what its consequences may be. Further, with understanding its consequences it is impossible to determine whether any action is required to deal with that failure. It could be assumed that any failure is credible, with severe consequences and hence must be dealt with. However, this would lead to very inefficient and bloated software. In addition, it may not be appropriate for the component actually being analysed to deal with failure (e.g. for an application to deal with a hardware failure).

There is no easy guidance on the level of detail required as it will differ from system to system. However, at the highest level a one line sentence describing the component could be used, :

when examining the components in a little more detail a list of essential services, and perhaps peculiarities could be used.

Undertaking the process for just the MSL was made easier by assembling an analysis team which included suppliers of other components. Failure modes of these other components could be assessed as to their credibility, and appropriate roles and responsibilities of the other components understood. Additionally, it is important to reassess the system decomposition when the other components are analysed to ensure its validity. This reassessment may mean components which have already been analysed, such as the MSL, may need to be re-analysed and even re-engineered! In this particular instance because representatives from each component supplier were present during the initial phases of analysis we have a high degree of confidence that the analysis is valid. It is highly recommended that all suppliers are involved when analysing the system.

Capturing inter-dependencies between components in the requirements consolidation phase is key to both ensuring that the initial system will be acceptably safe, and to ensuring any update to a component will be acceptably safe. In the latter case, if a component still meets the requirements and constraints placed on it by other components then the impact of its alteration should be limited. Where one component relies on another to address a particular failure, a "contract" is formed between the two. This contract should describe the interdependency between the components, but at a high enough level to allow the specific details to be hidden from one component from another.

On the left hand side of Figure 2 is a comment noting that the initial system analysis provides constraints on component re-use. Basically, this indicates that as the safety analysis is undertaken within this system context, a component may not necessarily be safe outside of this context. On the right hand side is a comment noting that the results provide constraints on component upgrade. This means that when a component is altered in some way it is constrained by the safety requirements of the system.

Analysing the Module Support Layer

This section describes and comments upon the analysis undertaken for the MSL component, stage by stage.

Stage 1: System analysis and decomposition

A team consisting of the application designers, system designers and MSL supplier was assembled for the first stage of analysis. A system architecture diagram was produced, overlaid with arrows to indicate data flow into the system, between the layers, and out to the display. In identifying the top level system hazards, and determining the sequence of events through the components in the system it was possible to examine the effects that a failure in an individual component could have at a system level. For example, the MSL could corrupt calculated display data after it had left the main application and then pass that data on, and hence incorrect data would be displayed to the pilot (potentially causing a system level hazard). Thus a set of derived requirements needed to be developed for the MSL.

A number of basic assumptions were made about the system and its components. As this analysis concentrated on the MSL, the assumptions made concerned the functionality and responsibilities of components with direct interactions to the MSL such as OS and applications. The assumptions made were as follows. Firstly, it was assumed that the OS and applications worked broadly as specified. That is, they may still fail in some way, but are designed and will perform their

individual tasks in the manner specified. For example, when a communications request is made the OS will check the validity of the request against the blueprint but may return an incorrect value due to some kind of software bug. However, it is assumed it is the responsibility of the application to perform this task and any fault management associated with it.

Once the system decomposition was agreed, an assessment of the essential services provided by the MSL was examined, using its current specification. The important features included not only services provided via direct interface calls, but also indirect calls. For example, memory management and context switches are not explicitly called but they are essential services for the application and OS.

The overall process of system decomposition took some time for the analysis group. In particular specifying the MSL services and agreeing a place to start the analysis was difficult. Agreeing the correct level of abstraction was also difficult. For example, should individual MOS calls be used, or grouped together? Should each scenario be examined? In the end it was decided that services should be grouped, and the general effect on each of the other components was considered. For example, the context switch was chosen as the first service to analyse, simply because it takes place so often, and the effects of failure to provide this service were considered for the OS and Applications.

Stage 2: Component Analysis

The next stage in the process is to analyse each individual component. The process is intended to allow any kind of Failure Modes and Effects Analysis (FMEA) to be undertaken for a component. The analysis process used for the MSL was based on the SHARD approach [3]. This technique uses five guidewords to suggest possible failure modes for an interaction between two components. These guidewords are shown in **Table 1**. The cause and effects of the failure mode are then assessed, and DSR determined to address the failure.

Guide word	Meaning
Omission	A service is not provided
Commission	A service is provided when it is not required (a perfectly functioning system would have done nothing)
Early	A service is provided early in relation to an expected time frame
Late	A service is provided late in relation to an expected time frame
Value	There are two types of value error to be considered – Detectable/Coarse and Undetectable/Subtle

Table 1 SHARD guidewords

One of the most frequently used functions of the MSL is the context switch. A context switch occurs when the MSL is passing access to the processor from itself to either the applications or the OS so they can execute. A further context switch occurs when control is returned to the MSL at the end of the processing time slot. A selection of the analysis results for the context switch is shown in **Table 2**. The complexity of some of the results reflects the dependencies between different layers and components. For example, the MSL is obliged to copy data from the communications bus without interruption to avoid data corruption. However, if data is still arriving and hence the MSL is late completing a data copy, this could have a knock on effect preventing the context switch to an application from occurring on time. This may in turn mean the application does not complete its execution. The effects of this are difficult to predict, and depend on whether the application will restart from scratch during each processing slot.

whether it is re-entrant (i.e. will restart from the position it stopped at after a suspension at the end of its time slot).

The results also show why it is essential to have knowledge of the system context for the MSL analysis. An example is the value failure where incorrect values are returned to the application from the context stack, potentially meaning incorrect display data is generated, and thus shown to pilot.

The analysis has thrown up instances where a derived requirement is made on another component. For example, if a context switch is made early then some OS processing requested by the application may be incomplete. The only method of detecting this by the application is if OS results passed back. Therefore it is essential that the OS set all return values to "failed" until the last commands in the sequence. Figure 2 shows the links between the sets of derived requirements for each layer. It is important to capture the requirements on other components and pass them on to the component supplier. This helps ensure interoperability.

Deviation	Causes	Effects	DSR
Omission Switch to correct app not made	Entry point in saved table is corrupted - OS or App has corrupted area, random failure. Context switch is interrupted by something else Processor stops / failed. Deadline for process been reached (process overrun, timeout in OS)	Switch to wrong application Control is not returned to application -> timeout Unintended process is executed -> Very difficult to predict effects of omission when application is re-entrant. Therefore must stop causes.	MSL s/ware should not switch to invalid entry context/wrong app MSL must detect writes outside memory area MSL must implement watchdog for failed/stopped processor (e.g. in initialisation)
Commission	See omission	Application run when not required	As for omission
Early	Processor failure Systematic software failure	Switches back to application too soon: Data could be corrupted/data not copied / partially copied. Application unaware of failure	Alternatives: Shall not happen or/(and?) O/S set status to failed as first time on data copy so app can detect (no MSL requirement)
Late	Data held up on bus Omission of data copy MSL is in critical section so cannot be interrupted	Data has arrived but too late for applications to complete Stale data at output Incomplete processing of app when end of slot occurs (strategy for restart required)	If we start at beginning of app each time then lose context data and entry point If start where stopped the must save context data at save entry point – re-entrant (MSL must keep itself in consistent state must be able to pass control back to OS this is to do with critical section requirements)
Value wrong app	See omission re-entrant	Wrong application run	As for omission
Value	Restore incorrect parameters /values in application stack Restore incorrect parameters /values in OS stack		For this system not too big from app point of view – there is pilot perception a range limiting

Table 2 Analysis Results looking at context switch between MSL and generic application

Stage 3: Consolidation of Analysis results

The final stage in the process is to consolidate results for each component in the system. At time of writing this stage had not been fully completed, so the initial results only are shown here. The DSRs for many of the failures shown in **Table 2** appear more than once. For example omission, commission and value failures all have the DSR that the “MSL must detect writes outside memory area”. This requirement also appears in other sections of the results (not shown in this paper) such as data transmission where an incorrect data address may have been sent to OS, and hence data may be written at an invalid address. All the DSRs need to be consolidated into a single table for each component in order to ensure that they are consistent. They can also be numbered within the consolidated table, and the analyses tables to capture traceability from failure to solution.

As discussed above, the analysis of the MSL also produced safety requirements for other components which needed to be captured. As these are linked back to failures in the MSL, traceability across components is also important. As discussed earlier, when one component relies on another to address a particular failure a “contract” is formed between the two. This contract contains the dependencies between components. The importance of contracts is not just to ensure that the initial system is safe, but also to allow upgrades of components to be undertaken with limited impact on other components. When a component is altered (e.g. the MSL is upgraded) it must adhere to its contracts. If the DSRs within the contract can still be met then the integrated system should still be acceptably safe, and no alteration should be required to the other components.

An example of a contract here is “MSL must detect writes outside of memory area”. This DSR protects against failures in both the OS and applications, preventing data corruption of memory space. This requirement is therefore a contract with the other two types of component. If the processor, and hence the MSL, is upgraded it must still meet this requirement as the failure can still occur. However the means by which the MSL meets the requirement can change.

Table 3 shows a method for capturing the interdependencies across components, using the first results row of **Table 2** as an example. The first derived requirement, MSL_DSR_1, addresses failures in two external components – applications and the OS. Therefore when upgrading the MSL this derived requirement must still be met. The second requirement, MSL_DSR_2, addresses an internal failure. In this case if the failure still exists then it must be addressed by the MSL, but if it doesn't then the derived requirement is no longer required. Clearly the failures need to be numbered or summarised within the analysis results tables in order to use this method.

Derived Requirement	Derived Requirement No	Failure + cause	Failure No
MSL must detect writes outside of memory area	MSL_DSR_1	OS or App has corrupted memory area	App_F1, OS_F1
Set maximum numbers of interrupts and hence re-entrance entries in tables.	MSL_DSR_2	No more space in entry table	MSL_F1

Table 3 MSL Consolidated requirements

Further discussion

One of the potential benefits of IMA is the ability to re-use components, such as the OS in a different context. However, this process uses a fixed system context to analyse generic components such as the MSL and this may mean that there are limitations on how the finished component can be re-used. For example, the MSL design was analysed for a display system or Most hazardous failures for this system involve data corruption. If the MSL was to be used in an IMA system which supported an application such as flight control, timing failures may be much more critical. One way around this problem would be to list other likely scenarios for re-use during the first stage of the process. This approach has the advantage that the MSL is developed and built for more scenarios. However, it may also mean that the MSL is over-engineered for a particular project and hence may be more difficult to certify for that project.

The other potential benefit of IMA is the ability to upgrade components with minimal impact on other components. This process supports this feature via the generation of DSRs which hide specific implementation details from the other component suppliers. For example, say the MSL in an IMA system is required to ensure data integrity to a certain probability and it ensures this using hardware supported methods. If the processor, and hence the MSL, were upgraded then as long as the same level of data integrity is assured then the assumptions made by other components are still valid, even if the MSL has to use a software based method to ensure data integrity.

One potential problem with the staged development process in general (rather than the analytical process used) is that when other components are analysed later in development further derived requirements may be found for the components which may already have been completed. Potentially the completed items may need to be altered. There is no obvious solution to this problem, however, it is hoped that the use of a guideline based technique in this particular instance has provided enough coverage of those OS and application failures which effect the MSL to avoid any major requirements being missed.

The system examined was relatively simple, in that only one application was running on the IM system. If more applications were to be used the analysis would be lengthier and it would potentially be more difficult to assess derived requirements due to conflicts in application requirements. The ability to consolidate these competing requirements is a major feature of a future "design by contract" approach. Additionally, it should be noted that scenarios for the re-use of a component might need to be examined to avoid potential future conflicts. This should be born in mind when performing the analysis in order to allocate adequate time and resources to the team.

Conclusions

This paper has presented a process for analysis of individual IMA components. The process supports the incremental development of IMA systems often required by airframe manufacturers. This process has been applied to the MSL for a simple IMA based display system, generating a set of derived requirements on all system components. This methodology also supports incremental change as it captures safety requirement dependencies between components. When one component relies on the service of another to address a particular failure a "contract" is formed between the two. Further work is being undertaken at York to develop these contracts fully by examining exactly how a component meets its safety requirements. This will generate a full set of constraints upon a component relying on that service which will not only improve assurance of interoperability, and provide better support for incremental change.

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Biography

Philippa Conmy is a research associate within the Defence and Aerospace Research Partners at the University of York. This is funded jointly by the EPSRC, UK DTI, BAE SYSTEMS and Rolls Royce. She attained an MSc in Computer Science from the University of Leeds in 1999. Prior to her current position she was a research associate within the Dependable Computing Systems Centre at York. She is currently studying for a PhD, examining techniques for safety analysis of component-based systems.

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Prof. John McDermid has been Professor of Software Engineering at the University of York since 1987 where he runs the High Integrity Systems Engineering (HISE) research group. He studies a broad range of issues in systems, software and safety engineering, and works closely with the UK aerospace industry. Professor McDermid is the Director of the Rolls-Royce funded University Technology Centre (UTC) in Systems and Software Engineering and the BAE SYSTEMS-funded Dependable Computing System Centre (DCSC). He is author or editor of several books, and has published about 250 papers.